

TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

TPC8040-H

High Efficiency DC-DC Converter Applications
 Notebook PC Applications
 Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: $Q_{SW} = 5.1 \text{ nC (typ.)}$
- Low drain-source ON-resistance: $R_{DS(ON)} = 6.4 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 48 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \text{ }\mu\text{A (max) (}V_{DS} = 30 \text{ V)}$
- Enhancement mode: $V_{th} = 1.3 \text{ to } 2.3 \text{ V (}V_{DS} = 10 \text{ V, } I_D = 0.2 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

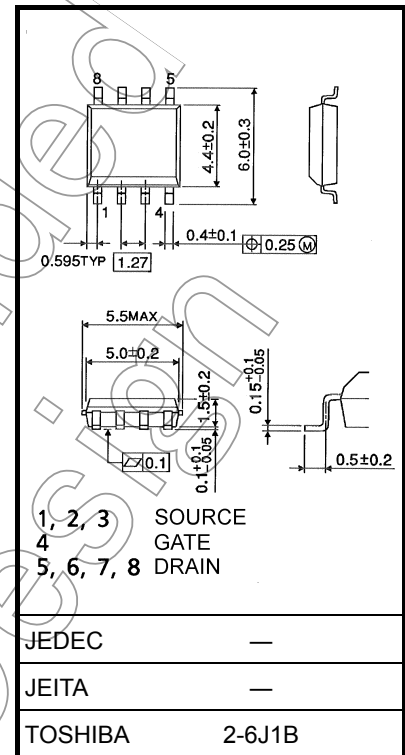
| Characteristic | | Symbol | Rating | Unit |
|--|-----------------|-----------|------------|------|
| Drain-source voltage | | V_{DSS} | 30 | V |
| Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$) | | V_{DGR} | 30 | V |
| Gate-source voltage | | V_{GSS} | ± 20 | V |
| Drain current | DC (Note 1) | I_D | 13 | A |
| | Pulsed (Note 1) | I_{DP} | 52 | |
| Drain power dissipation (t = 10 s) (Note 2a) | | P_D | 1.9 | W |
| Drain power dissipation (t = 10 s) (Note 2b) | | P_D | 1.0 | W |
| Single pulse avalanche energy (Note 3) | | E_{AS} | 110 | mJ |
| Avalanche current | | I_{AR} | 13 | A |
| Repetitive avalanche energy (Note 2a) (Note 4) | | E_{AR} | 2.0 | mJ |
| Channel temperature | | T_{ch} | 150 | °C |
| Storage temperature range | | T_{stg} | -55 to 150 | °C |

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating" Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

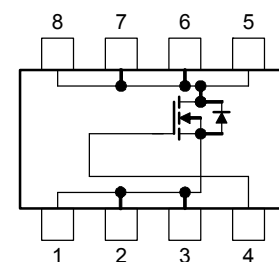
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



Weight: 0.085 g (typ.)

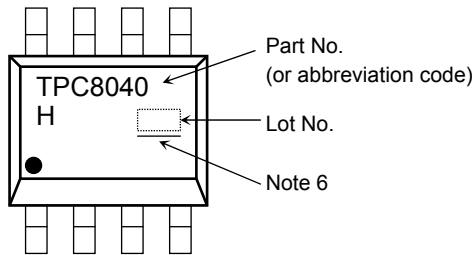
Circuit Configuration



Thermal Characteristics

| Characteristic | Symbol | Max | Unit |
|--|----------------|------|------|
| Thermal resistance, channel to ambient (t = 10 s) (Note 2a) | $R_{th(ch-a)}$ | 65.8 | °C/W |
| Thermal resistance, channel to ambient (t = 10 s) (Note 2b) | $R_{th(ch-a)}$ | 125 | °C/W |

Marking (Note 5)

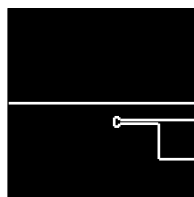


Note 6: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

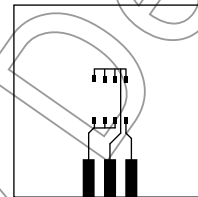
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



(a)

FR-4
25.4 × 25.4 × 0.8
(Unit: mm)



(b)

FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

Note 3: $V_{DD} = 24\text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 500\ \mu\text{H}$, $R_G = 25\ \Omega$, $I_{AR} = 13\ \text{A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

* Weekly code: (Three digits)



Week of manufacture

(01 for the first week of the year: sequential number up to 52 or 53)

Year of manufacture

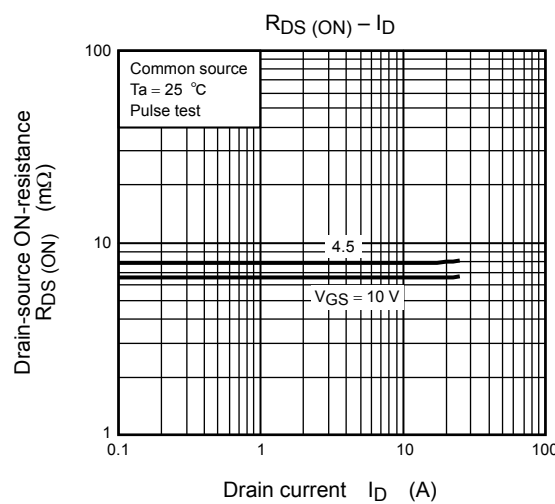
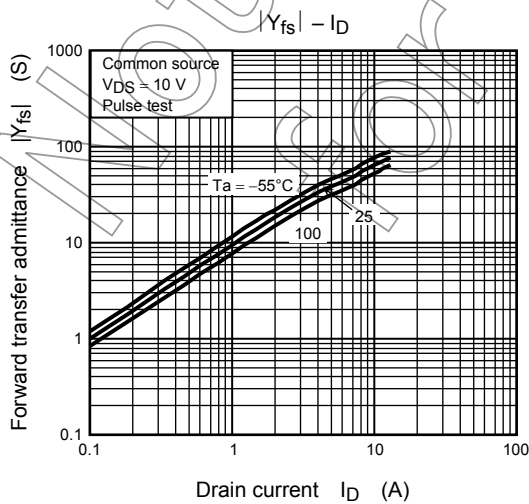
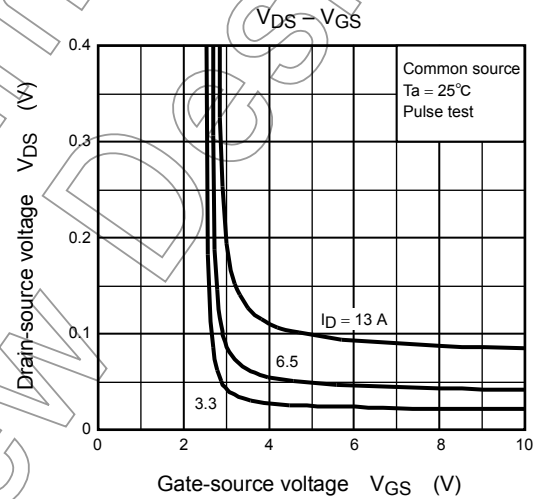
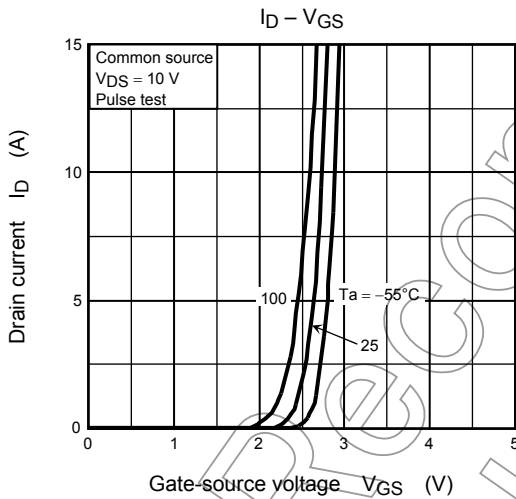
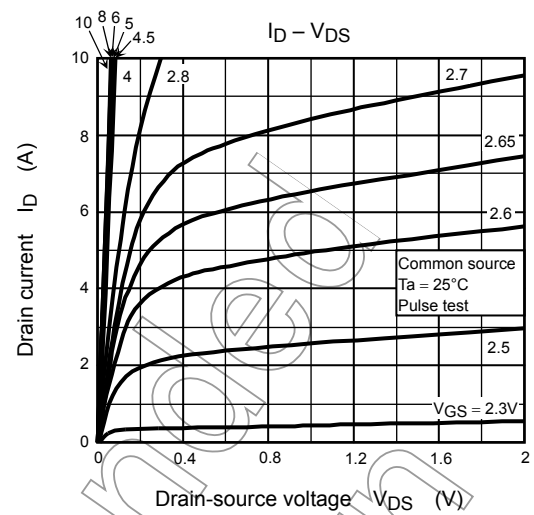
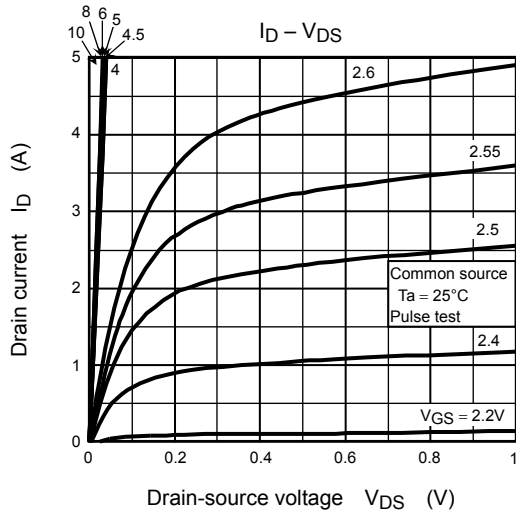
(The last digit of the year)

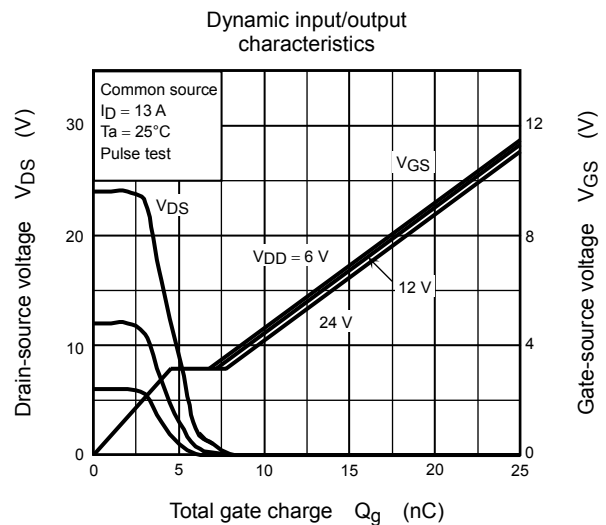
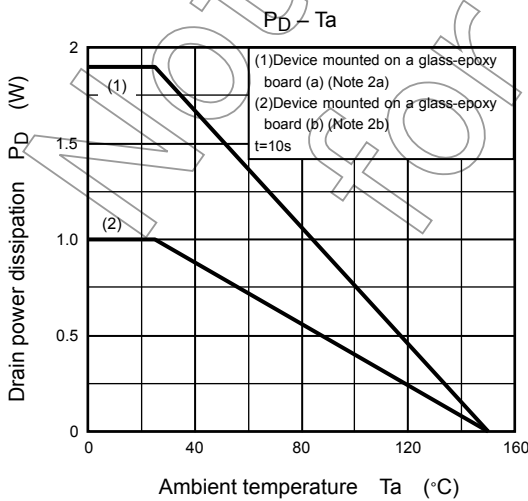
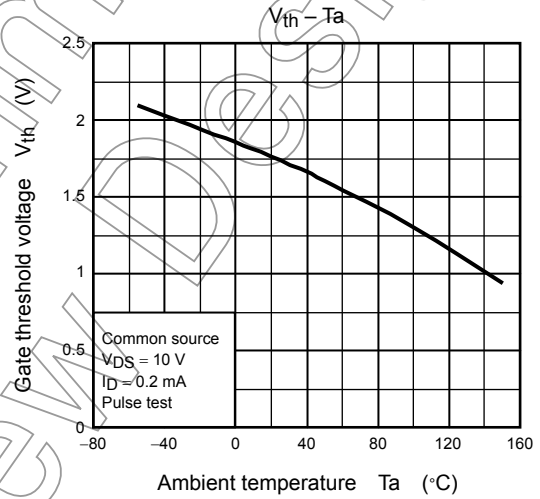
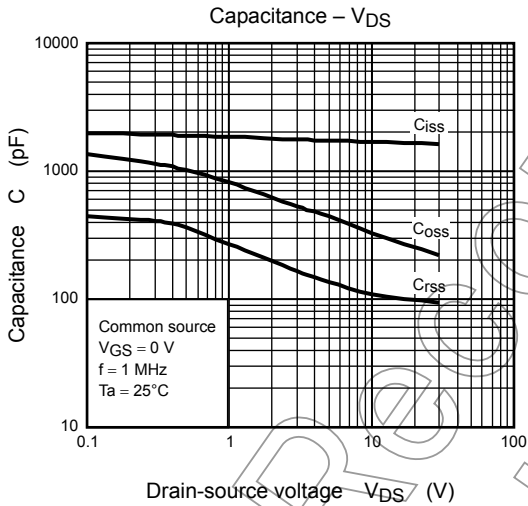
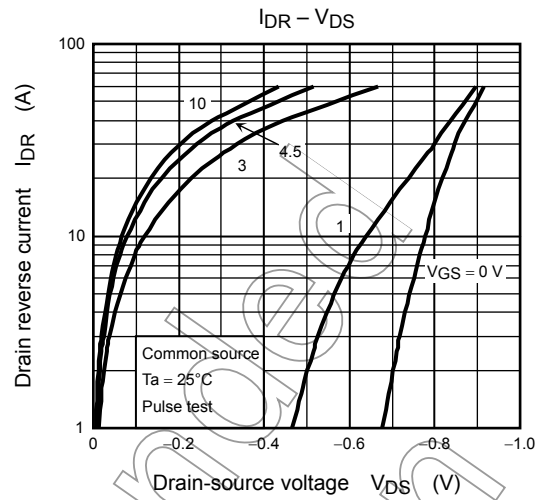
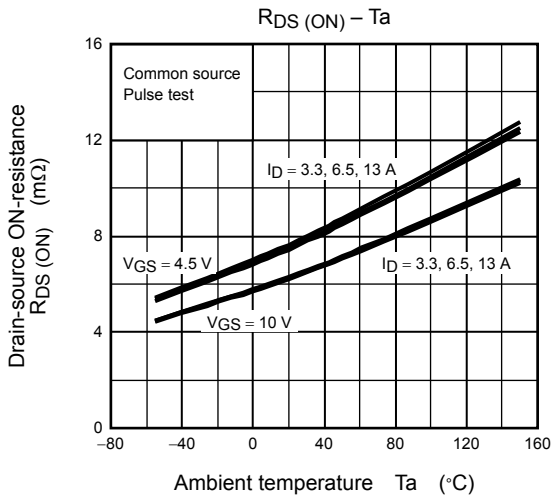
Electrical Characteristics (Ta = 25°C)

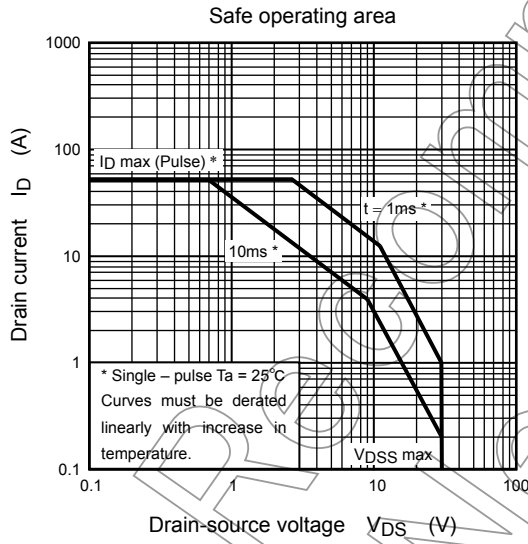
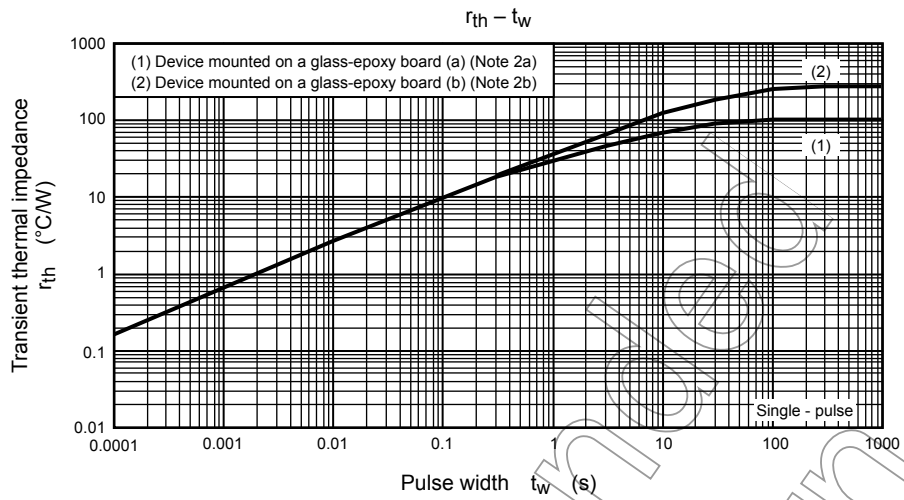
| Characteristic | | Symbol | Test Condition | Min | Typ. | Max | Unit |
|---|---------------|---------------|---|-----|------|-----------|---------------|
| Gate leakage current | | I_{GSS} | $V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$ | — | — | ± 100 | nA |
| Drain cutoff current | | I_{DSS} | $V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$ | — | — | 10 | μA |
| Drain-source breakdown voltage | | $V_{(BR)DSS}$ | $I_D = 10\text{ mA}, V_{GS} = 0\text{ V}$ | 30 | — | — | V |
| | | $V_{(BR)DSX}$ | $I_D = 10\text{ mA}, V_{GS} = -20\text{ V}$ | 15 | — | — | |
| Gate threshold voltage | | V_{th} | $V_{DS} = 10\text{ V}, I_D = 0.2\text{ mA}$ | 1.3 | — | 2.3 | V |
| Drain-source ON-resistance | | $R_{DS(ON)}$ | $V_{GS} = 4.5\text{ V}, I_D = 6.5\text{ A}$ | — | 7.7 | 11.1 | m Ω |
| | | | $V_{GS} = 10\text{ V}, I_D = 6.5\text{ A}$ | — | 6.4 | 9.7 | |
| Forward transfer admittance | | $ Y_{fs} $ | $V_{DS} = 10\text{ V}, I_D = 7\text{ A}$ | 24 | 48 | — | S |
| Input capacitance | | C_{iss} | $V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$ | — | 1700 | 2200 | pF |
| Reverse transfer capacitance | | C_{rss} | | — | 110 | 170 | |
| Output capacitance | | C_{oss} | | — | 330 | — | |
| Gate resistance | | r_g | $V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 5\text{ MHz}$ | — | 2.3 | 3.5 | Ω |
| Switching time | Rise time | t_r | <p>$V_{GS} = 10\text{ V}, 0\text{ V}$ $I_D = 6.5\text{ A}$ $V_{DD} \approx 15\text{ V}$ $R_L = 2.3\Omega$ $Duty \leq 1\%, t_w = 10\ \mu\text{s}$</p> | — | 5.5 | — | ns |
| | Turn-on time | t_{on} | | — | 15 | — | |
| | Fall time | t_f | | — | 8.6 | — | |
| | Turn-off time | t_{off} | | — | 39 | — | |
| Total gate charge (gate-source plus gate-drain) | | Q_g | $V_{DD} \approx 24\text{ V}, V_{GS} = 10\text{ V}, I_D = 13\text{ A}$ | — | 24 | — | nC |
| | | | $V_{DD} \approx 24\text{ V}, V_{GS} = 5\text{ V}, I_D = 13\text{ A}$ | — | 12 | — | |
| Gate-source charge 1 | | Q_{gs1} | $V_{DD} \approx 24\text{ V}, V_{GS} = 10\text{ V}, I_D = 13\text{ A}$ | — | 4.6 | — | |
| Gate-drain ("miller") charge | | Q_{gd} | | — | 3.2 | — | |
| Gate switch charge | | Q_{sw} | | — | 5.1 | — | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | | Symbol | Test Condition | Min | Typ. | Max | Unit |
|-------------------------|----------------|-----------|---|-----|------|------|------|
| Drain reverse current | Pulse (Note 1) | I_{DRP} | — | — | — | 52 | A |
| Forward voltage (diode) | | V_{DSF} | $I_{DR} = 13\text{ A}, V_{GS} = 0\text{ V}$ | — | — | -1.2 | V |







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