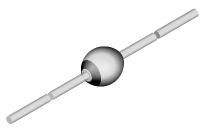


Vishay Semiconductors

Standard Avalanche Sinterglass Diode



949539

FEATURES

- · Glass passivated junction
- Hermetically sealed package
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912





COMPLIANT HALOGEN FREE

MECHANICAL DATA

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

Mounting position: any Weight: approx. 369 mg

APPLICATIONS

- High voltage rectification
- Efficiency diode in horizontal deflection circuits

ORDERING INFORMATION (Example)				
DEVICE NAME	IAME ORDERING CODE TAPED UNITS MINIMUM ORDER QU			
BY458	BY458TR	5000 per 10" tape and reel	25 000	
BY458	BY458TAP	5000 per ammopack	25 000	

PARTS TABLE				
PART	TYPE DIFFERENTIATION	PACKAGE		
BY448	V _R = 1500 V, I _{FAV} = 2 A	SOD-57		
BY458	V _R = 1200 V, I _{FAV} = 2 A	SOD-57		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
Reverse voltage	See electrical characteristics	BY448	$V_R = V_{RRM}$	1500	V	
neverse voltage		BY458	$V_R = V_{RRM}$	1200	V	
Peak forward surge current	$t_p = 10$ ms, half sine wave		I _{FSM}	30	Α	
Average forward current			I _{FAV}	2	Α	
Junction temperature			T _j	140	°C	
Storage temperature range			T _{stg}	- 55 to + 175	°C	
Non repetitive reverse avalanche energy	$I_{(BR)R} = 0.4 A$		E _R	10	mJ	

MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	I = 10 mm, T _L = constant	R_{thJA}	45	K/W	
	On PC board with spacing 25 mm	R_{thJA}	100	K/W	



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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX	UNIT
Forward voltage	I _F = 3 A	V_{F}	1	-	1.6	V
Reverse current	$V_R = V_{RRM}$	I _R	-	-	3	μA
	$V_R = V_{RRM}$, $T_j = 140$ °C	I _R	-	-	140	μA
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_R = 0.25 \text{ A}$	t _{rr}	1	-	2000	ns
Total reverse recovery time	$I_F = 1 A$, - $dI_F/dt = 0.05 A/\mu s$	t _{rr}	- 1	-	20	μs

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

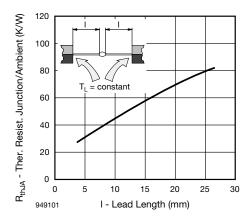


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

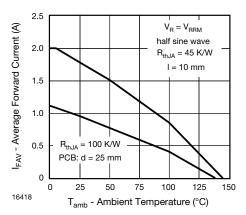


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

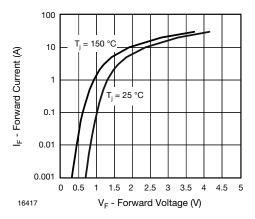


Fig. 2 - Forward Current vs. Forward Voltage

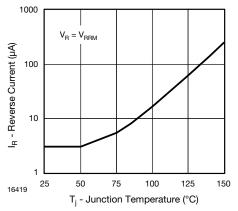


Fig. 4 - Reverse Current vs. Junction Temperature



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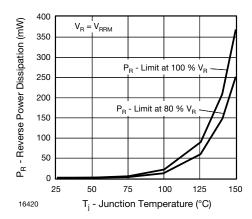


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

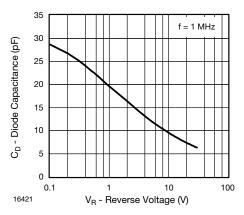
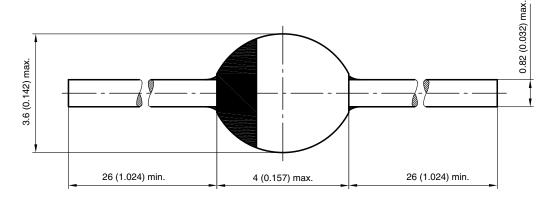


Fig. 6 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-57



20543 Rev. 3 - Date: 09.February 2005 Document no.:6.563-5006.3-4



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