SYSMAC CJ-series CJ2M CPU Units, Pulse I/O Modules

CJ2M-CPU3 /- CPU1 /- MD21

CSM_CJ2M-CPU3__-CPU1__-MD21__DS_E_2_1

Since 2001, CJ1M-series PLCs are in control of a wide variety of applications worldwide.

The accumulated experience and advancements in technology now result in CJ2M; fully compatible, yet fully new.







CJ2M-MD21□

CJ2M-CPU3□

CJ2M-CPU1□

- Increased performance, and increased memory capacity
- Up to 40 I/O unit on any CPU
- Pulse I/O Modules add position control functions to any CPU
- USB for plug-and-play access to the PLC
- All models available with or without Ethernet port
- Choice of serial port plug-in modules

Features

- Five variations in program capacity from 5K steps to 60K steps; scale the CPU to your application needs.
- Faster processors; LD instruction execution time is reduced to 40 ns, floating point trigonometrics in less than 1 µs.
- Optional Pulse I/O Modules can be mounted to enable positioning functions for up to four axes. The module provides high-speed counters, interrupt inputs and pulse train/PWM outputs. (CJ2M CPU Units with Unit Version 2.0 or Later)
- Faster Function Block calls and execution, faster interrupt handling, less overhead time.
- · Added execution memory for Function Blocks allows structured, object-oriented programming even in entry-level CPUs.
- General-purpose Ethernet port supports EtherNet/IP tag-based data links, connection to Support Software, communications between PLCs, FTP data transfers, and more (CJ2M-CPU3□).
- Standard USB port on all models allows Support Software to connect directly through standard USB cable.
- A Serial Option Module can be mounted to add RS-232C or RS-422A/485 communications ports (CJ2M-CPU3

).
- Compatible with all existing CJ1 power supply-, I/O-, control- and communication units.

Ordering Information

International Standards

- The standards are abbreviated as follows: U: UL, U1: UL (Class I Division 2 Products for Hazardous Locations), C: CSA, UC: cULus, UC1: cULus (Class I Division 2 Products for Hazardous Locations), CU: cUL, N: NK, L: Lloyd, and CE: EC Directives.
- Contact your OMRON representative for further details and applicable conditions for these standards.

CJ2M CPU Units (Built-in EtherNet/IP)

	Specifications						Current consumption (A)			
Product name	I/O capacity/ Mountable Units (Expansion Racks)	Program capacity	Data memory capacity	LD instruction execution time	EtherNet/IP function	Option board slot	5 V 24 V		Model	Standards
CJ2M (Built-in		60K steps 160K words (DM: 32K words,				CJ2M-CPU35				
EtherNet/IP) CPU Units	2,560 points/	30K steps	EM: 32K words × 4 banks)	<					CJ2M-CPU34	
	40 Units (3 Expansion Racks max.)	20K steps	64K words	0.04 μs	YES	YES	0.7 (See note.)	-	CJ2M-CPU33	UC1, N, L, CE
		10K steps	(DM: 32K words, EM: 32K words ×				note.)			
The state of the s		5K steps EM: 32K words x							CJ2M-CPU31	

Note: Add 0.005A, 0.030A and 0.075A when using Serial Communications Option Boards (CP1W-CIF01/11/12), respectively.

Add 0.15A/Unit when using NT-AL001 RS-232C/RS-422A Adapters.

Add 0.04A/Unit when using CJ1W-CIF11 RS-422A Adapters.

Add 0.20A/Unit when using NV3W-M□20L Programmable Terminals.

CJ2M CPU Units

		Specifications						rent otion (A)		
Product name	I/O capacity/ Mountable Units (Expansion Racks)	lountable Units Program Data memory Instruction Ethernetian board		board	5 V	24 V	Model	Standards		
O IOM OBIL		60K steps	160K words (DM: 32K words,	0.04 μs –					CJ2M-CPU15	
CJ2M CPU Units	2,560 points/	30K steps	EM: 32K words × 4 banks)					CJ2M-CPU14		
	40 Units (3 Expansion	20K steps	64K words		-	-	0.5 (See note.)	_		UC1, N, L, CE
	Racks max.)	10K steps	(DM: 32K words				note.)			
100		5K steps	1 bank)						CJ2M-CPU11	

Note: Add 0.15A/Unit when using NT-AL001 RS-232C/RS-422A Adapters.
Add 0.04A/Unit when using CJ1W-CIF11 RS-422A Adapters.
Add 0.20A/Unit when using NV3W-M□20L Programmable Terminals.

Serial Communications Option Boards (Only CJ2M-CPU3□)

The serial communications port can be equipped by installing the serial communications option board to the option board slot in front of CPU unit.

Product name	Specifications	Serial communications	Current consumption (A)		Model	Standards
		illoue	5 V	24 V		
RS-232C Option Board	One RS-232C port Connector: D-Sub, 9 pin, female Maximum transmission distance: 15m One RS-232C connector (D-Sub, 9 pin, male) is included. (Plug: XM2A-0901, Hood: XM2S-0911-E)		0.005	-	CP1W-CIF01	
RS-422A/485 Option Board	One RS-422A/485 port Terminal block: using ferrules Maximum transmission distance: 50m	Host Link, 1:N NT Link, No- protocol, Serial PLC Link Slave, Serial PLC Link Master, Serial Gateway converted to CompoWay/F, and Tool Bus *	0.030	-	CP1W-CIF11	UC1, N, L, CE
RS-422A/485 Isolated-type Option Board	One RS-422A/485 port (Isolated) Terminal block: using ferrules Maximum transmission distance: 500m		0.075	-	CP1W-CIF12	

Note: It is not possible to use a CP-series Ethernet Option Board (CP1W-CIF41), LCD Option Board (CP1W-DAM01) with a CJ2M CPU Unit. *The following modes cannot be used: 1:1 NT Link, Serial Gateway converted to Host Link FINS, 1:1 Link Master, and 1:1 Link Slave.

Pulse I/O Modules (Only CJ2M CPU Unit with Unit Version 2.0 or Later)

Optional Pulse I/O Modules can be mounted to enable pulse I/O. Up to two Pulse I/O Modules can be mounted to the left side of a CJ2M CPU Unit.

Product name	Specifications		rent ption (A)	Model	Standards
		5 V	24 V		
	Sinking outputs, MIL connector 10 inputs (including four interrupt/quickresponse inputs and two high-speed counter inputs) 6 outputs (including two pulse outputs and two PWM outputs)	0.08		NEW CJ2M-MD211	UC1, N, L,
	Sourcing outputs, MIL connector 10 inputs (including four interrupt/quickresponse inputs and two high-speed counter inputs) 6 outputs (including two pulse outputs and two PWM outputs)	0.08		NEW CJ2M-MD212	CE

Note: Connectors are not provided with Pulse I/O Modules. Purchase the following Connector, an OMRON Cable with Connectors for Connector Terminal Block Conversion Units, or an OMRON Cable with Connectors for Servo Relay Units.

Connecting to Pulse I/O Modules

On wiring, refer to Pulse I/O Modules Connector Wiring Methods.

Product name	Specifications		Model	Standards
Applicable Connector	MIL Flat Cable Connectors (Pressure-fitted Connectors)		XG4M-4030-T	
	Slim type (M3 screw terminals, 40-pin)	XW2D-40G6		
Connector-Terminal Block Conversion Units	Through type (M3 screw terminals, 40-pin)	XW2B-40G4		
	Through type (M3.5 screw terminals, 40-pin)	XW2B-40G5		
		Cable length: 1 m	XW2Z-100K	
		XW2Z-150K		
Cable for Connector-Terminal Block Conversion Unit		XW2Z-200K		
Conversion only		XW2Z-300K		
		Cable length: 5 m	XW2Z-500K	
	Servo Relay Unit for 1 axis		XW2B-20J6-8A	
Servo Relay Units	Servo Relay Unit for 2 axes		XW2B-40J6-9A	

CJ2M-CPU3□/-CPU1□/-MD21□

Product name		Specifications		Model	Standards
		Cable for Pulse I/O Modules	Cable length: 0.5 m	XW2Z-050J-A33	
	OMNUC G5/G Series		Cable length: 1 m	XW2Z-100J-A33	
		Servo Driver Connecting Cables	Cable length: 1 m	XW2Z-100J-B31	
			Cable length: 2 m	XW2Z-200J-B31	
	SMARTSTEP2	Cable for Pulse I/O Modules	Cable length: 0.5 m	XW2Z-050J-A33	
			Cable length: 1 m	XW2Z-100J-A33	
		Servo Driver Connecting Cables	Cable length: 1 m	XW2Z-100J-B32	
			Cable length: 2 m	XW2Z-200J-B32	
	SMARTSTEP Junior	Cable for Pulse I/O Modules	Cable length: 1 m	XW2Z-100J-A26	
Cables for Servo Relay Units		Servo Driver Connecting Cables	Cable length: 1 m	XW2Z-100J-B17	
			Cable length: 2 m	XW2Z-200J-B17	
		Cable for Pulse I/O Modules	Cable length: 1 m	XW2Z-100J-A26	
	SMARTSTEP A Series	Servo Driver Connecting Cables	Cable length: 1 m	XW2Z-100J-B5	
			Cable length: 2 m	XW2Z-200J-B5	
		Cable for Pulse I/O Modules	Cable length: 0.5 m	XW2Z-050J-A27	
			Cable length: 1 m	XW2Z-100J-A27	
	OMNUC W Series	Servo Driver Connecting Cables	Cable length: 1 m	XW2Z-100J-B4	
			Cable length: 2 m	XW2Z-200J-B4	

Accessories

The following accessories come with CPU Unit:

Item	Specification				
Battery CJ1W-BAT01					
End Cover CJ1W-TER01 (necessary to be mounted at the right end of CPU Rack)					
End Plate PFP-M (2 pcs)					
Serial Port (RS-232C) Connector (see note)	Connector set for serial port connection (D-SUB 9-pin male connector)				

Note: Connector is provided with CJ2M-CPU1 \square .

General Specifications

	u		CJ2M-				
	Item	CPU1□	CPU3□				
Enclosure		Mounted in a panel	Mounted in a panel				
Grounding		Less than 100 Ω					
CPU Unit Dimens	ions	90 mm × 75 mm × 31 mm	90 mm × 75 mm × 62 mm				
Weight		130 g or less	190 g or less (see note)				
Current Consump	otion	5 VDC, 0.5 A	5 VDC, 0.7 A				
	Ambient Operating Temperature	0 to 55°C	·				
	Ambient Operating Humidity	10% to 90% (with no condensation)					
	Atmosphere	Must be free from corrosive gases.	Must be free from corrosive gases.				
	Ambient Storage Temperature	-20 to 70°C (excluding battery)					
	Altitude	2,000 m or less					
	Pollution Degree	2 or less: Conforms to JIS B3502 and IEC	2 or less: Conforms to JIS B3502 and IEC 61131-2.				
Operation	Noise Immunity	2 kV on power supply line (Conforms to IEC 61000-4-4.)					
Environment	Overvoltage Category	Category II: Conforms to JIS B3502 and IE	Category II: Conforms to JIS B3502 and IEC 61131-2.				
	EMC Immunity Level	Zone B	Zone B				
	Vibration Resistance	Conforms to IEC60068-2-6 5 to 8.4 Hz with 3.5-mm amplitude, 8.4 to 150 Hz Acceleration of 9.8 m/s² for 100 min in X, Y, and Z directions (10 sweeps of 10 min each 100 min total)					
	Shock Resistance	Conforms to IEC60068-2-27 147 m/s², 3 times in X, Y, and Z directions	(100 m/s² for Relay Output Units)				
Pottory	Life	5 years at 25°C					
Battery	Model	CJ1W-BAT01					
Applicable Stand	ards	Conforms to cULus, NK, LR, and EC Direc	Conforms to cULus, NK, LR, and EC Directives.				

Note: Without a Serial Option Board.

Performance Specifications

Items			CDU14/24 CDU14/24 CDU14/24 CDU14/24 CDU14/24						
Ma			CPU11/31	CPU12/32	CPU13/33	CPU14/34	CPU15/35		
User Memory //O Bits			5K steps 2,560 bits	10K steps	20K steps	30K steps	60K steps		
/O Bits			Normal Mode:						
	Overhead F	Processing Time *1	CJ2M-CPU3□: 270) µs					
		ū	CJ2M-CPU1□: 160) μs					
	Execution Time		Basic Instructions	•					
Processing	LACCUION		Special Instructions	<u> </u>					
Speed		I/O Interrupts and	Interrupt task startu						
	Interrunte	External Interrupts	Return time to cycli	•	ma ingramenta)				
	Interrupts	Scheduled	Interrupt task startu	val : 0.4 ms (set in 0.1	ms increments)				
		Interrupts	Return time to cycli						
			•	k or Expansion Rack:	10 Units max.:				
Maximum Numl	ber of Conne	ctable Units	Total per PLC: 40 l		,				
Basic I/O Units		No limit							
	Special I/O Units			um of two CJ1W-INT0					
					ounted. (Unit numbers	run from 0 to 95. Units	are allocated between		
CPU Bus Units			1 and 8 unit numbe						
			CJ2M-CPU3□: 15 CJ2M-CPU1□: 16						
B 1. 16		2 Units max. *	oomax.						
	Pulse I/O Modules			y CJ2M CPU Units w	th unit version 2.0 or	ater. A Pulse I/O Mod	ule must be mounted.		
	Slots for wh	nich interrupts can be	Slots 0 to 4 on CPL	I Rack					
used) ndUK					
Maximum Numl		sion Racks	3 max.						
	I/O Area		, ,	ds): Words CIO 000					
	Link Area		3,200 bits (200 words) : Words CIO 1000 to CIO 1199						
	CPU Bus U		, ,	ds): Words CIO 150					
	Special I/O		, ,	ords): Words CIO 200					
CIO Area	Pulse I/O A			ts (CIO 2960 to CIO 2	,				
	Serial PLC		, ,	s) : Words CIO 310					
	DeviceNet A	Area	, ,	ds) : Words CIO 320					
	Internal I/O	Area				ot be used for external not be used for externa			
Work Area				ds): Words W000 to V			u 1/O.)		
WOIR AICU				ds): Words H000 to H		a for external 1/O.)			
Ualdina Avaa			Bits in this area maintain their ON/OFF status when PLC is turned OFF or operating mode is changed.						
Holding Area			Words H512 to H1535: These words can be used only for function blocks. They can be used only for function						
			,	<u> </u>	nly for internal variabl	es in function blocks).			
			Read-only: 31,744		4.4.7				
				vords): Words A0 to A 66 words): Words A10					
Auxiliary Area				bits (1,024 words) in		*			
						by CPU Bus Units, Spe	cial I/O Units, PTs, a		
			- ''	that do not specifical	ly support the CJ2 CF	² U Units.			
Temporary Area	a		16 bits: TR0 to TR1			\\\			
Fimer Area				rs (T0000 to T4095 (s	•	,,			
Counter Area				bers (C0000 to C4095	(separate from timer	5))			
			32k words * • DM Area words to	for Special I/O Units: I	020000 to D29599 (10	00 words × 96 Units)			
		DM Area words for Special I/O Units: D20000 to D29599 (100 words × 96 Units) DM Area words for CPU Bus Units: D30000 to D31599 (100 words × 16 Units)							
DM Area			 DM Area words t 	* Bits in the EM Area can be addressed either by bit or by word. These bits cannot be addressed by CPU But address					
DM Area			* Bits in the EM Are	ea can be addressed e	either by bit or by word	. These bits cannot be	addressed by CPU B		
DM Area			Bits in the EM Are Units, Special I/C	ea can be addressed of Units, PTs, and Sup	either by bit or by word port Software that do	not specifically suppor	addressed by CPU Br the CJ2 CPU Units.		
DM Area			Bits in the EM Are Units, Special I/O32k words/bank × 4	ea can be addressed of Units, PTs, and Sup banks max.: E00_00	either by bit or by word port Software that do 000 to E3_32767 max	not specifically suppor	the CJ2 CPÚ Units.		
			* Bits in the EM Are Units, Special I/C 32k words/bank × 4 * Bits in the EM Are	ea can be addressed of Units, PTs, and Supplements max.: E00_00 ea can be addressed of the control of the contr	either by bit or by word bort Software that do 000 to E3_32767 max bither by bit or by word	not specifically suppor	the CJ2 CPU Units.		
			* Bits in the EM Are Units, Special I/C 32k words/bank × 4 * Bits in the EM Are	ea can be addressed 6 D Units, PTs, and Supp D banks max.: E00_00 Dea can be addressed 6 D Units, PTs, and Supp	either by bit or by word bort Software that do 000 to E3_32767 max bither by bit or by word	not specifically suppor c. * . These bits cannot be	addressed by CPU B the CJ2 CPU Units.		
	Force-S/R I	Enabled Banks *2	* Bits in the EM Are Units, Special I/C 32k words/bank × 4 * Bits in the EM Are Units, Special I/C	ea can be addressed 6 D Units, PTs, and Supp D banks max.: E00_00 Dea can be addressed 6 D Units, PTs, and Supp	either by bit or by word bort Software that do 000 to E3_32767 max bither by bit or by word	not specifically suppor c. * These bits cannot be not specifically suppor	addressed by CPU B the CJ2 CPU Units.		
	Force-S/R I	Enabled Banks *2	 Bits in the EM Are Units, Special I/C 32k words/bank × 4 Bits in the EM Are Units, Special I/C 32K words × 1 bank 	ea can be addressed 6 D Units, PTs, and Supp D banks max.: E00_00 Dea can be addressed 6 D Units, PTs, and Supp	either by bit or by word bort Software that do 000 to E3_32767 max bither by bit or by word	not specifically suppor c. * . These bits cannot be not specifically suppor 32K words × 4 bar	addressed by CPU B the CJ2 CPU Units.		
EM Area		Enabled Banks *2	* Bits in the EM Ard Units, Special I/C 32k words/bank × 4 * Bits in the EM Ard Units, Special I/C 32K words × 1 banl Bank 0 hex IR0 to IR15 These are special r	ea can be addressed of Units, PTs, and Supple Danks max.: E00_00 ea can be addressed of Units, PTs, and Supple K	or by word or by word out Software that do on Software that do on the software that do on the software that do out Software that do on the software the software that do on th	not specifically suppor . * These bits cannot be not specifically suppor 32K words × 4 bar Bank 0 to 3 hex for indirect addressin	the CJ2 CPU Units. addressed by CPU Brithe CJ2 CPU Units. iks		
EM Area ndex Registers	3	Enabled Banks *2	* Bits in the EM Ard Units, Special I/C 32k words/bank × 4 * Bits in the EM Ard Units, Special I/C 32K words × 1 banl Bank 0 hex IR0 to IR15 These are special r be set so that they	ea can be addressed of Units, PTs, and Supple Banks max.: E00_00 ea can be addressed of Units, PTs, and Supple k	or by word or by word out Software that do on Software that do on the software that do on the software that do out Software that do on the software the software that do on th	not specifically suppor . * These bits cannot be not specifically suppor 32K words × 4 bar Bank 0 to 3 hex for indirect addressin	the CJ2 CPU Units. addressed by CPU B the CJ2 CPU Units. iks		
EM Area ndex Registers Cyclic Task Fla	3	Enabled Banks *2	* Bits in the EM Ard Units, Special I/C 32k words/bank × 4 * Bits in the EM Ard Units, Special I/C 32K words × 1 banl Bank 0 hex IR0 to IR15 These are special r be set so that they 128 flags	ea can be addressed of Units, PTs, and Supple banks max.: E00_00 ea can be addressed of Units, PTs, and Supple k	or by word or by word out Software that do on Software that do on the software that do on the software that do out Software that do on the software the software that do on th	not specifically suppor . * These bits cannot be not specifically suppor 32K words × 4 bar Bank 0 to 3 hex for indirect addressin	the CJ2 CPU Units. addressed by CPU Brithe CJ2 CPU Units. iks		
DM Area EM Area Index Registers Cyclic Task Fla Memory Card	3	Enabled Banks *2	* Bits in the EM Ard Units, Special I/C 32k words/bank × 4 * Bits in the EM Ard Units, Special I/C 32K words × 1 banl Bank 0 hex IR0 to IR15 These are special ribe set so that they 128 flags 128 MB, 256 MB, o	ea can be addressed of Units, PTs, and Supple banks max.: E00_00 ea can be addressed of Units, PTs, and Supple k	or by word or by word or software that do on the softw	not specifically suppor . * These bits cannot be not specifically suppor 32K words × 4 bar Bank 0 to 3 hex for indirect addressin shared by all tasks.)	the CJ2 CPU Units. addressed by CPU Bithe CJ2 CPU Units. iks g. (Index Registers ca		
EM Area Index Registers Cyclic Task Fla	3	Enabled Banks *2	* Bits in the EM Ard Units, Special I/C 32k words/bank × 4 * Bits in the EM Art Units, Special I/C 32K words × 1 banl Bank 0 hex IR0 to IR15 These are special r be set so that they 128 flags 128 MB, 256 MB, o PROGRAM Mode:	ea can be addressed ea b Units, PTs, and Supple banks max.: E00_00 Units, PTs, and Supple banks max.: Endough the banks max.: Event and Supple banks max.: Even and Supple banks max	or by word or by word or software that do on the softw	not specifically suppor . * These bits cannot be not specifically suppor 32K words × 4 bar Bank 0 to 3 hex for indirect addressin	the CJ2 CPU Units. addressed by CPU Bithe CJ2 CPU Units. iks g. (Index Registers ca		
EM Area Index Registers Cyclic Task Flag Memory Card	g Area	Enabled Banks *2	* Bits in the EM Ard Units, Special I/C 32k words/bank × 4 * Bits in the EM Ard Units, Special I/C 32K words × 1 banl Bank 0 hex IR0 to IR15 These are special r be set so that they 128 flags 128 MB, 256 MB, of PROGRAM Mode:	ea can be addressed ea can be are supply to the supply to	word software that do to	not specifically suppor . * . These bits cannot be not specifically suppor 32K words × 4 bar Bank 0 to 3 hex for indirect addressin shared by all tasks.) an be executed prior to	addressed by CPU Bit the CJ2 CPU Units. addressed by CPU Bit the CJ2 CPU Units. ks g. (Index Registers can be program execution in		
EM Area Index Registers Cyclic Task Fla	g Area	Enabled Banks *2	* Bits in the EM Ard Units, Special I/C 32k words/bank × 4 * Bits in the EM Ard Units, Special I/C 32K words × 1 banl Bank 0 hex IR0 to IR15 These are special r be set so that they 128 flags 128 MB, 256 MB, of PROGRAM Mode:	ea can be addressed ea can be are supply to the supply to	word software that do to	not specifically suppor . * . These bits cannot be not specifically suppor 32K words × 4 bar Bank 0 to 3 hex for indirect addressin shared by all tasks.) an be executed prior to ons, such as online edi	addressed by CPU B the CJ2 CPU Units. addressed by CPU B the CJ2 CPU Units. ks		

^{*1.} The following time must be added when using EtherNet/IP tag data links for the CJ2M-CPU3□.

100 μs + (Number of words transferred × 1.8 μs)

The following time must be added when using Pulse I/O Modules with a CJ2M CPU Unit:

10 μs × Number of Pulse I/O Modules

^{*2.} Force-setting/resetting bits in the EM Area is possible only for banks specified for the EM Area force-set/reset function.

			CJ2M-						
	Items		CPU11/31	CPU12/32	CPU13/33	CPU14/34	CPU15/35		
Execution	Mode		Normal Mode						
Programm	ing Languages		Ladder Logic (LD), Sequential Function Charts (SFC), Structured Text (ST), and Instruction Lists (IL)						
Function	Maximum numbe	r of definitions	256			2,048			
Blocks	Maximum numbe	r of instances	256			2,048			
FB Progra	m Area		20K steps			*			
	Type of Tasks		Cyclic tasks Interrupt tasks (Powe tasks, and input inter		scheduled interrupt ta	asks, I/O interrupt tasks,	, and external interrupt		
Tasks	Number of Tasks		Cyclic tasks: 128 Interrupt tasks: 256 (Interrupt tasks can be tasks is actually 384		ks to create extra cyc	lic tasks. Therefore, the	total number of cyclic		
Type of Symbols			Global symbols: C	ameter settings.	s in the PLC.	PLC. e externally accessed u	using symbols,		
Data Type of Symbols Symbols (Variables)		UDINT BCD (two- ULINT BCD (four- REAL (two-word f LREAL (four-word CHANNEL (word) NUMBER (consta WORD (one-word DWORD (two-word LWORD (four-word STRING (1 to 255 TIMER (timer) *5	unsigned binary) unsigned binary) ined binary) igned binary) igned binary) igned binary) ord unsigned BCD) * word unsigned BCD) idealing-point) if loating-point) *4 nt or number) *4 hexadecimal) id hexadecimal) id hexadecimal) id ASCII characters)	*4					
	Maximum Size of	Symbol	32k words	71 (,				
	Array Symbols (A	•	One-dimensional arr	avs					
	Number of Array	• '	32,000 elements ma	-					
	Number of Regist Symbols (Tags) *	trable Network	2,000 max.	Δ.					
	Length of Networ	k Symbol (Tag)	255 bytes max.						
	Encoding of Netwo	rk Symbols (Tags) *6	UTF-8						
	Memory Capacity	T	8,000 words (Up to 3	32k words × 4 banks w	hen EM is specified i	n CX-Programmer)			
	Number of Sample	lings	Bits = 31, one-word data =16, two-word data = 8, four-word data = 4						
	Sampling Cycle		1 to 2,550 ms (Unit: 1 ms)						
Data Tracing	Trigger Condition	ns	ON/OFF of specified bit Data comparison of specified word Data size: 1 word, 2 words, 4 words Comparison Method: Equals (=), Greater Than (>), Greater Than or Equals (≥), Less Than (<), Less Than or Equals (≤), Not Equal (≠)						
	Delay Value		-32,768 to +32,767	ms					
File Memo	ry			256, or 512 Mbytes) (l t of the EM Area can I		ls provided by OMRON as file memory.)	l.)		
Source/ Comment Memory	Function block processing comment file, processymbol tables		Capacity: 1 Mbytes						
	Legical Desta f	Logical Ports	8 ports (Used for SE	ND, RECV, CMND, P	MCR, TXDU, and RX	DU instructions.)			
Commi	Logical Ports for Communications	Extended Logical Ports	64 ports (Used for S	END2, RECV2, CMNI	02, and PMCR2 instru	uctions.)			
Commu- nications	CIP Communications	Class 3 (Connection Type)	Number of connection	ons: 64					
	Specification	UCMM (Non-		clients that can comn					
		connection Type)	Maximum number of	servers that can com	municate at the same	e time: 40			

^{*3.} Supported only by CJ2M CPU Units with unit version 2.0 or later. A Pulse I/O Module must be mounted. *4. Cannot be used in Function blocks. *5. Can be used only in Function blocks. *6. Supported only by the CJ2M-CPU3.

						CJ2M-				
			Item	CPU11/31	CPU12/32	CPU13/33	CPU14/34	CPU15/35		
	Perip	her	al (USB) Port	USB 2.0-compliant	B-type connector					
	Ва	aud	Rate	12 Mbps max.						
	Tr	ans	smission Distance	5 m max.						
	Seria	l Po	ort	CJ2M-CPU1□ interface: Conforms to EIA RS-232C. CJ2M-CPU3□: No serial ports with default system One of the following Serial Option Boards can be mounted. CP1W-CIF01 RS-232C Option Board CP1W-CIF11 RS-422A/485 Option Board (not isolated, max. transmission distance: 50 m) CP1W-CIF12 RS-422A/485 Option Board (isolated, max. transmission distance: 500 m)						
	Co	omi	munications Method	Half-duplex	Half-duplex					
	Sy	nc	hronization Method	Start-stop						
	Ва	aud	Rate	0.3, 0.6, 1.2, 2.4, 4	.8, 9.6, 19.2, 38.4, 57	'.6, or 115.2 (kbps)				
	Tr	ans	smission Distance	15 m max.						
	Ether	Ne	t/IP Port *7							
	Su	M	ledia Access Method	CSMA/CD						
	Specifications	M	lodulation	Baseband						
	ij	Т	ransmission Paths	Star						
	bec	В.	aud Rate	100 Mbps (100Bas	e-TX)					
	l la	Т	ransmission Media	Shielded twisted-pa	air (STP) cable; Cate	gories: 5, 5e				
	ssic	Т	ransmission Distance	100 m (between et	hernet switch and no	de)				
	Transmission	N	lumber of Cascade Connections	No restrictions if ethernet switch is used.						
		С	IP Communications: Tag Data Links							
			Number of Connections	32						
			Packet Interval (Refresh period)	1 to 10,000 ms (Un Can be set for each of nodes.)	it: 0.5 ms) n connection. (Data w	vill be refreshed at the	e set interval, regard	less of the number		
ommu-			Permissible Communications Band	3,000 packets per	second *8					
cations			Number of Registerable Tag	32						
			Type of Tags	CIO, DM, EM, HR,	WR, and Network sy	mboles				
			Number of Tags per Connection	8 (Seven tags if PLC status is included in the segment.)						
			Maximum Link Data Size per Node	640 words						
			Maximum Data Size per Connection	20 words (Data is synchronized within each connection.)						
	S		Number of Registrable Tag Set	32 (1 connection = 1 segment)						
	l lig		Maximum Tag Set Size	20 words (One wor	d is used when PLC	status is included in t	the segment.)			
	Specifications		Maximum Number of Tags Refreshable in a Single Cycle of CPU Unit *9		Unit to EtherNet/IP): rNet/IP to CPU Unit):					
			Data Size Refreshable in a Single Cycle of CPU Unit *9		to EtherNet/IP) : 640 rNet/IP to CPU): 640					
	Communications		Change of Tag Data Link Parameter Settings during Operation	OK *10						
	E	L	Multi-cast Packet Filter *11	ОК						
	ပိ		IP Communications: Explicit lessages	-						
			Class 3 (Connection Type)	Number of connect						
			UCMM (Non-connection Type)	Maximum number	of clients that can cor of servers that can co					
			CIP Routing	OK (CIP routing is enal CPU3□, and CS1V	bled for the following V-EIP21.)	remote Units: CJ1W-	-EIP21, CJ2H-CPU6	□-EIP, CJ2M-		
		F	INS Communications	-						
			FINS/UDP	ОК						
			FINS/TCP	16 connections ma	x.					
		_	•	Conforms to A5.						
		E	therNet/IP Conformance Test	Conforms to A5.						

^{*7.} The EtherNet/IP port is built into CJ2M-CPU3□ only.

^{*8. &}quot;Packets per second" is the number of communications packets that can be processed per second.

^{*9.} If the maximum number is exceeded, refreshing will require more than one CPU Unit cycle.
*10. When changing parameters, however, the EtherNet/IP port where the change is made will be restarted. In addition, a timeout will temporarily occur at the other node that was communicating with that port, and it will then recover automatically.
*11. The EtherNet/IP port supports an IGMP client, so unnecessary multicast packets are filtered by using an Ethernet switch that supports IGMP

snooping.

Function Specifications

	Fu	ınctions		Description			
	Minimum Cycle	Time		A minimum cycle time can be set. (0.2 to 32,000 ms; Unit: 0.1 ms) The minimum cycle time setting can be changed in MONITOR mode.			
Cycle Time Management	Cycle Time Mon	itoring		The cycle time is monitored. (0.01 to 40,000 ms; Unit: 0.01 ms)			
	Background Pro	cessing		Instructions with long execution times can be executed over multiple cycles to prevent fluctuations in the cycle time.			
			Cyclic Refreshing	Cyclic refreshing of Basic I/O Units, Special I/O Units, and CPU Bus Units			
	Basic I/O Units, Special I/O	I/O Refreshing	Immediate Refreshing	I/O refreshing by immediate refreshing instructions			
	Units, and CPU Bus Units		Refreshing by IORF	I/O refreshing by IORF instruction			
		Unit Recognitio	n at Startup	The number of units recognized when the power is turned ON is displayed.			
		Input Response Time Setting		The input response times can be set for Basic I/O Units. The response time can be increased to reduce the effects of chattering and noise at input contacts. The response time can be decreased to enable detecting shorter input pulses.			
Unit (I/O)	Basic I/O Units	/O Units Load OFF Function		All of the outputs on Basic I/O Units can be turned OFF when an error occurs in RUN MONITOR mode.			
Management		Basic I/O Unit S	tatus Monitoring	Alarm information can be read from Basic I/O Units and the number of Units recognized can be read.			
Į.		Reading/writing instructions for		Special instructions can be used to read/write required data for specific Units at high speed.			
	Special I/O Units and CPU Bus Units	Unit Restart Bits	s to Restart Units	A Special I/O Unit or CPU Bus Unit can be restarted.			
		Automatic I/O Allocation at Startup		I/O words can be automatically allocated to the Basic I/O Units that are connected in the PLC to start operation automatically without registering Units into I/O tables.			
	Configuration Management			The current unit configuration can be registered in I/O tables to prevent it from being changed, to reserve words, and to set words.			
		Rack/Slot First Word Settings		The first words allocated to a Units on the Racks can be set.			
	Holding I/O Mem	nory when Changi	ng Operating Modes	The status of I/O memory can be held when the operating mode is changed or power is turned ON. The forced-set/reset status can be held when the operating mode is changed or power is turned ON.			
	File Memory			Files (such as program files, data files, and symbol table files) can be stored in Memory Card, EM File Memory, or Comment Memory.			
Memory Management	Built-in Flash Me	emory		The user program and Parameter Area can be backed up to an internal flash memory when they are transferred to the CPU Unit.			
	EM File Function	1		Parts of the EM Area can be treated as file memory.			
	Storing Commer	nts		I/O comments can be stored as symbol table files in a Memory Card, EM file memory, or comment memory.			
	EM Configuration			EM Area can be set as trace memory or EM file memory.			
	Automatic File T	ransfer at Startup		A program file and parameter files can be read from a Memory Card when the power is turned ON.			
Memory Cards	Program Replac	ement during PLO	Operation	User programs can be transferred from a Memory Card to CPU Unit during operation.			
-3.40	Function for Reading and Writing Data from a Memory Card			Data in I/O memory in the CPU Unit can be written to a Memory Card in CSV/TXT format. Data in CSV/TXT format in the Memory Card can be read to I/O memory in the CPU Unit.			

		Funct	ion	Description						
Communication	ons			-						
	Pe Po	ripheral (USB) rt	Peripheral Bus	Bus for communications with various kinds of Support Software running on a personal computer. High-speed communications are supported.						
	Se	rial Port (Option) *11	Application is possible when a Serial Communications Option Board is mounted.						
	Host Link (SYSWAY) Communications			Host Link commands or FINS commands placed between Host Link headers and terminators can be sent from a host computer or PT to read/write I/O memory, read/control the operating mode, and perform other operations for PLC.						
		No-protocol Communications NT Link Communications		I/O instructions for communications ports (such as TXD/RXD instructions) can be used for data transfer with peripheral devices such as bar code readers and printers. I/O memory in the PLC can be allocated and directly linked to various PT functions, include status control areas, status notification areas, touch switches, lamps, memory tables, and other objects.						
		Peripheral Bus		Bus for communications with various kinds of Support Software running on a personal computer. High-speed communications are supported.						
		Serial Gateway		This gateway enables receiving and automatically converting FINS to the CompoWay/F.						
		Serial PLC Link	s	This gateway enables receiving and automatically converting FINS to the CompoWay/F. Data is exchanged between CPU Units using serial ports without communications programming. PTs set to the 1:N NT Link protocol can be included in the network. 100Base-TX/10Base-T Protocols: TCP/IP, UDP, ARP, ICMP (ping only), BOOTP Applications: FINS, CIP, SNTP, DNS (Client), FTP (Server) Programless cyclic data exchanges with the devices on the EtherNet/IP network.						
	EtherNet/IP Port *12		12	Protocols: TCP/IP, UDP, ARP, ICMP (ping only), BOOTP						
		CIP	Tag Data Links	Programless cyclic data exchanges with the devices on the EtherNet/IP network.						
		Communications Service	Message Communications	Any CIP commands can be received from the devices on the EtherNet/IP network.						
		FINS Communications Service	Message Communications	Any FINS commands can be transferred with the devices on the EtherNet/IP network.						
	Sc	heduled Interrup	ots	Tasks can be executed at a specified interval (minimum of 0.2 ms, Unit: 0.1 ms).						
		Resetting and r	estarting with MSKS(690)	When MSKS(690) is executed, the internal timer is restarted and the time to first interrupt is set to a fixed value.						
Interrupt		Reading presen MSKS(690)	t value of internal timer with	MSKS(690) can be used to read the time that has elapsed until the schedule interrupt is started or since the previous scheduled interrupt.						
	Ро	wer OFF Interru	ots	A task can be executed when CPU Unit's power turns OFF.						
	I/O	Interrupt Tasks		A task can be executed when an input signal is input to an Interrupt Input Unit.						
	Ex	ternal Interrupt	Tasks	A task can be executed when interrupts are requested from a Special I/O Unit or a CPU Bus Unit						
	Clo	ock Function		Clock data is stored in memory. Accuracy (Accuracy depends on the temperature.) Ambient temperature of 55°C: -3.5 to +0.5 min error per month Ambient temperature of 25°C: -1.5 to +1.5 min error per month Ambient temperature of 0°C: -3 to +1 min error per month						
	Ор	eration Start Tin	ne Storage	The time when operating mode was last changed to RUN mode or MONITOR mode is stored						
Clock	Ор	eration Stop Tin	ne Storage	The last time a fatal error occurred or the last time the operating mode was changed to PROGRAM mode is stored.						
		artup Time Stora	<u>~</u>	The time when the power was turned ON is stored.						
	Ро	wer Interruption	Time Storage	The time when the power is turned OFF is stored.						
	-	tal Power ON Tir		The total time that the PLC has been ON is stored in increments of 10 hours.						
		wer ON Clock Da		A history of the times when the power was turned ON is stored.						
			rwritten Time Storage	The time that the user program was last overwritten is stored.						
	Ра	rameter Date Sto	orage	The time when the Parameter Area was overwritten is stored.						
Power	Ме	mory Protection	1	Holding Area data, DM Area data, EM Area data, Counter Completion Flags, and counter present values are held even when power is turned OFF. CIO Area, Work Area, some Auxiliary Area data, and Timer Completion Flags, timer present values, index registers, and data registers can be protected by turning ON the IOM Hold Bit in the Auxiliary Area, and by also setting the IOM Hold Bit to "Hold" in the PLC Setup.						
Supply Management	Ро	wer OFF Detecti	on Time Setting	The detection time for power interruptions can be set. AC power supply: 10 to 25 ms (variable) DC power supply: 2 to 5 ms (CJ1W-PD022) or 2 to 20 ms (CJ1W-PD025)						
	Ро	wer OFF Detecti	on Delay Time	The detection of power interruptions can be delayed: 0 to 10 ms (Not supported by the CJ1W-PD022.)						
			nterruptions Counter	The number of times power has been interrupted is counted.						

^{*12.}A Serial Option Board is required to use a serial port for the CJ2M-CPU3 CJ2M CPU Unit. *13.Supported only by the CJ2M-CPU3.

	Funct	ion	Description Standard programming can be encapsulated as function blocks.					
Function Blo	cks		Standard programming can be encapsulated as function blocks.					
	Languages in Fund	ction Block Definitions	Ladder programming or structured text					
	Online Editing		The program can be changed during operation (in MONITOR or PROGRAM mode), except for block programming areas.					
	Force-Set/Reset		Specified bits can be set or reset. Force-set/reset to the EM Area is enabled by specifying a start bank in parameter setting.					
	Differentiate Monit	oring	ON/OFF changes in specified bits can be monitored.					
	Data Tracing		The specified I/O memory data can be stored in the trace memory in the CPU Unit. The triggers can be set.					
Debugging	Continuous Tra	acing	The trace data can be uploaded during data tracing using CX-Programmer, which enables continuously logging the data by constantly uploading the trace data.					
	Automatically s starts	starting tracing when operation	Data tracing can be automatically started when operation is started (i.e., when the operating mode is changed from PROGRAM mode to MONITOR or RUN mode).					
	Storing Location o	f Error when an Error Occurs	The location and task number where execution stopped for a program error is recorded.					
	Program Check		The programs can be checked for items such as no END instruction and FALS/FAL errors at startup.					
	Error Log		A function is provided to store predefined error codes in CPU Unit, error information, and time at which the error occurred.					
	CPU Error Detection	on	CPU Unit WDT errors are detected.					
	User-defined Failu	re Diagnosis	Errors can be generated for user-specified conditions: Non-fatal errors (FAL) and fatal errors (FALS). Program section time diagnosis and program section logic diagnosis are supported (FPD instruction).					
	Load OFF Function	n	This function turns OFF all outputs from Output Units when an error occurs.					
	RUN Output		The RUN output from the CJ1W-PA205R turns ON while CPU Unit is in RUN mode or MONITOR mode.					
	Basic I/O Load Sho	ort-circuit Detection	This function provides alarm information from Basic I/O Units that have load short-circuit protection.					
	Failure Point Detec	ction	The time and logic of an instruction block can be analyzes using the FPD instruction.					
	CPU Standby Dete	ction	This function indicates when the CPU Unit is on standby because all Special I/O Units and CPU Bus Units have not been recognized at the startup in RUN or MONITOR mode.					
		System FAL Error Detection (User-defined non-fatal error)	This function generates a non-fatal (FAL) error when the user-defined conditions are met in program.					
		Duplicate Refreshing Error Detection	This function detects an error when an immediate refreshing Instruction in an interrupt task is competing with I/O refreshing of a cyclic task.					
		Basic I/O Unit Error Detection	This function detects the errors in Basic I/O Units.					
		Backup Memory Error Detection	This function detects errors in the memory backup of the user programs and parameter area (backup memory).					
Self- diagnosis		PLC Setup Error Detection	This function detects setting errors in the PLC Setup.					
and Restoration	Non-fatal Error	CPU Bus Unit Error Detection	This function detects an error when there is an error in data exchange between the CPU Unit and a CPU Bus Unit.					
	Detection	Special I/O Unit Error Detection	This function detects an error when there is an error in data exchange between the CPU Unit and a Special I/O Unit.					
		Tag Memory Error Detection *13	This function detects errors in tag memory.					
		Battery Error Detection	This function detects an error when a battery is not connected to the CPU Unit or when the battery voltage drops.					
		CPU Bus Unit Setting Error Detection	This function detects an error when the model of a CPU Bus Unit in the registered I/O tables does not agree with the model that is actually mounted in the PLC.					
		Special I/O Unit Setting Error Detection	This function detects an error when the model of a Special I/O Unit in the registered I/O tables does not agree with the model of Unit that is actually mounted.					
		Option Board Error Detection *13	This function detects the errors in Serial Option Board mounting status.					
		Memory Error Detection	This function detects errors that occur in memory of the CPU Unit.					
		I/O Bus Error Detection	This function detects when an error occurs in data transfers between the Units mounted in Rack slots and the CPU Unit and detects when the End Cover is not connected to the CPU Rack or an Expansion Rack.					
	Fatal Error Detection	Unit/Rack Number Duplication Error	This function detects an error when the same unit number is set for two or more Units, the same word is allocated to two or more Basic I/O Units, or the same rack number is set for two or more Racks.					
		Too Many I/O Points Error Detection	This function detects an error when the total number of I/O points set in the I/O tables or the number of Units per Rack exceeds the specified range.					
		I/O Setting Error Detection	This function detects an error when the number of Units in the registered I/O tables does not agree with the actual number of Units that is mounted, or an Interrupt Unit has been connected in the wrong position, i.e., not in slot 0 to 4.					

^{*14.}Supported only by the CJ2M-CPU3□.

	Funct	ion			Description					
		Pro	ogram Error D	etection	This function detects errors in programs.					
			Instruction Processing Error Detection Indirect DM/EM BCD Error Detection Illegal Area Access Error Detection		This function detects an error when the given data value is invalid when executing an instruction, or execution of instruction between tasks was attempted.					
					This function detects an error when an indirect DM/EM address in BCD mode is not BCD.					
					This function detects an error when an attempt is made to access an illegal area with an instruction operand.					
			No END Erro	r Detection	This function detects an error when there is no END instruction at the end of the program.					
0-14	Fatal Error Detection		Task Error D	etection	This function detects an error when there are no tasks that can be executed in a cycle, there is no program for a task, or the execution condition for an interrupt task was met but there is no interrupt task with the specified number.					
Self- diagnosis and			Differentiation Error Detection		This function detects an error when too many differentiated instructions are entered or deleted during online editing (131,072 times or more).					
Restoration			Invalid Instru Detection	iction Error	This function detects an error when an attempt is made to execute an instruction that is not defined in the system.					
			User Program Area Overflow Error Detection		This function detects an error when instruction data is stored after the last address in user program area.					
			Cycle Time Exceeded Error Detection		This function monitors the cycle time (10 to $40,000 \text{ ms}$) and stops the operation when the svalue is exceeded.					
	Fatal Error		System FALS Error Detection (User-defined Fatal Error)		This function generates a fatal (FALS) error when the user-defined conditions are met in program.					
	Detection (Continued from	Ve	rsion Error De	etection	This function detects an error when a user program includes a function that is not supported by the current unit version.					
	previous page)		mory Card Tratection	ansfer Error	This function detects an error when the automatic file transfer from Memory Card fails at startup.					
	Simple Backup Fur	nctio	on		This function collectively backs up the data in CPU Unit (user programs, parameters, and memory) and internal backup data in the I/O Units.					
	Unsolicited Comm	unic	ations		A function that allows the PLC to use Network Communications Instruction to send required FINS commands to a computer connected via a Host Link					
Maintenance	Remote Programm	ing	and Monitorin	g	Host Link communications can be used for remote programming and remote monitoring through a Controller Link, Ethernet, DeviceNet, or SYSMAC LINK Network. Communications across network layers can be performed. Controller Link or Ethernet : 8 layers DeviceNet or SYSMAC LINK: 3 layers					
	Automatic Online (Conr	nection via	Direct Serial Connection	This function enables automatically connecting to the PLC online when the CX-Programmer is directly connected by a serial connection (peripheral (USB) port or serial port).					
	HOLWOIR			Via Networks	This function enables connecting the CX-Programmer online to a PLC that is connected via an EtherNet/IP network.					
	Read Protection using Password		Password		This function protects reading and displaying programs and tasks using passwords. Write protection: Set using the DIP switch. Read protection: Set a password using the CX-Programmer.					
Security	FINS Write Protecti	ion			This function prohibits writing by using FINS commands sent over the network.					
Cocurity	Unit Name Functio	n			This function allows the users to give any names to the Units. Names are verified at online connection to prevent wrong connection					
	Hardware ID Using Lot Numbers				This function sets operation protection by identifying hardware using the user programs according to lot numbers stored in the Auxiliary Area.					

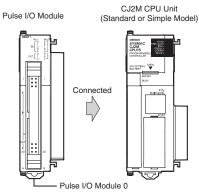
Module 0 and the other one is Pulse I/O Module 1.

Specifications for Pulse I/O Functions

The following functions of CJ2M can be used by installing one or two Pulse I/O Modules. Each module has 10 high-speed inputs and 6 high-speed outputs. Pulse I/O Modules can be installed on CJ2M CPU Units with Unit Version 2.0 or Later.

- The inputs can be used as general-purpose inputs, interrupt inputs, quick-response inputs, high-speed counters, or origin search inputs.
- The outputs can be used as general-purpose outputs, pulse outputs, origin search outputs, or PWM outputs.

One Pulse I/O Module can be mounted



Two Pulse I/O Modules can be mounted CJ2M CPU Unit (Standard or Simple Model) Connected Connected Note: The Pulse I/O Module closest to the CPU Unit is Pulse I/O

- Pulse I/O Module 0

Performance Specifications

	Item	Description
	Model of Pulse I/O Modules	CJ2M-MD211 (Sinking-type) CJ2M-MD212 (Sourcing-type)
	External Interface	40-pin MIL connector
	Pulse Inputs	Can be used as normal inputs, interrupt inputs, quick-response inputs, or high-speed counter inputs. (Function of each input must be selected in the PLC Setup.) Input method: Line-driver input or 24-VDC input (selected by wiring)
	Normal Inputs	20 max. (10 per Pulse I/O Module) Input constants: Set in the PLC Setup (0, 0.5, 1, 2, 4, 8, 16, or 32 ms). Default: 8 ms
	Interrupt Inputs and Quick-response Inputs	8 max. (4 per Pulse I/O Module) Input signal minimum ON pulse width: 30 μs
Pulse I/O	High-speed Counter Inputs	4 max. (2 per Pulse I/O Module) Input method: Differential-phase (x4) pulses, pulse + direction, up/down pulses, or increment pulse Maximum response frequency: 50 kHz for differential phases or 100 kHz for single phase Counting mode: Linear mode or circular (ring) mode Count value: 32 bits Counter reset: Phase Z + software reset or software reset Control method: Target-value comparison or range comparison Gate function: Supported
	Pulse Outputs	Can be used as normal outputs, pulse outputs, or PWM outputs. (Function of each output must be selected in the PLC Setup.) Output method: Sinking or sourcing transistor outputs (The method is determined by Pulse I/O Module model.)
	Normal Outputs	12 max. (6 per Pulse I/O Module)
	Pulse Outputs	4 max. (2 per Pulse I/O Module) Output method: CW/CCW or pulse + direction (The method is determined by the I/O wiring and the instructions used in the ladder program.) Output frequency: 1 pps to 100 kpps (in increments of 1 pps) Output Mode: Continuous mode (for speed control) or independent mode (for position control) Output pulses: Relative coordinates: 0000 0000 to 7FFF FFFF hex (0 to 2,147,483,647 pulses) Absolute coordinates: 8000 0000 to 7FFF FFFF hex (-2,147,483,648 to 2,147,483,647) Acceleration/deceleration curves: Linear or S-curve Origin search function: Supported
	PWM Outputs	4 max. (2 per Pulse I/O Module) Output frequency: 0.1 to 6,553.5 Hz (in 0.1-Hz increments) or 1 to 32,800 Hz (in 1-Hz increments) Duty ratio: 0.0% to 100.0% (in 0.1% increments)

Pulse I/O Module 1

Function Specifications

	Func	tions	Description				
		Normal Inputs	Input signals are read during I/O refreshing and stored in I/O memory.				
	Pulse Input	Interrupt Inputs	An interrupt task can be started when an input signal turns ON or turns OFF.				
	Functions	Quick-response Inputs	Input signals that are shorter than the cycle time are read and stored in I/O memory.				
		High-speed Counter Inputs	High-speed pulse signals are counted. Interrupt tasks can also be started.				
Pulse I/O	Pulse	Normal Outputs	The status of I/O memory is output during I/O refreshing.				
Functions	Output	Pulse Outputs	A pulse signal is output with the specified frequency and number of pulses at a fixed duty ratio (50%)				
	Functions	PWM Outputs	A pulse signal is output at the specified duty ratio.				
	Origin Searc	hes	The origin point of the machine is determined according to the specified origin search parameters while actually outputting pulses and using the origin and origin proximity input signals as conditions. (Pulse inputs and outputs are also used for this function.)				
	Input Interru	pt Function	A task is started for an interrupt input from a Pulse I/O Module or for a high-speed counter input.				
Interrupt	Input Interrupts High-speed Counter Interrupts		Interrupt tasks are executed when the interrupt input turns ON or turns OFF. Direct Mode: An interrupt task is executed each time an input signal changes. Counter Mode: Changes in the input signal are counted up or down and the interrupt task is executed when the counter counts out. (The maximum response frequency is 3 kHz.)				
			An interrupt task is executed when preset comparison conditions for a high-speed counter are met. Target-value comparison: The interrupt task is executed when the count matches a specified value. Range comparison: The interrupt task is executed when the count enters or leaves a specified range of values.				

Allocating Functions I/O signals Pulse I/O Module 0 (on the right)

Tor	minal s	wmbal	IN 00	IN 01	IN 02	IN 03	IN 04	IN 05	IN 06	IN 07	IN 08	IN 09	OUT 00	OUT 01	OUT 02	OUT0 3	OUT 04	OUT 05
_		Syllibol		IN UT	IIV UZ	114 03	IN U4	IIV US	114 00	IN U7	114 00	114 09		00101	001 02	00103	001 04	001 05
Addres	SS		2960		1_	_		1_	1_	I _	_	1_	2961		_	_		I _
Bit			0	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5
	Norma	al inputs	Normal input 0	Normal input 1	Normal input 2	Normal input 3	Normal input 4	Normal input 5	Normal input 6	Normal input 7	Normal input 8	Normal input 9						
	(Direc	upt inputs t Mode/ er Mode)	Interrupt input 0	Interrupt input 1	Interrupt input 2	Interrupt input 3												
Inputs	Quick	response	Quick response input 0	Quick response input 1	Quick response input 2	Quick response input 3												
	High-speed counters				Highspeed counter 1 (phase- Z/ reset)	Highspeed counter 0 (phase- Z/ reset)			High- speed counter 1 (phase-A, increment, or count input)	High- speed counter 1 (phase-B, decrement, or direction input)	High- speed counter 0 (phase-A, increment, or count input)	High- speed counter 0 (phase-B, decrement, or direction input)						
	Norma	al outputs											Normal output 0	Normal output 1	Normal output 2	Normal output 3	Normal output 4	Normal output 5
		CW/CCW outputs											Pulse output 0 (CW)	Pulse output 0 (CCW)	Pulse output 1 (CW)	Pulse output 1 (CCW)		
Out puts	Pulse out puts	Pulse + direction outputs											Pulse output 0 pulse)	Pulse output 1 (pulse)	Pulse output 0 (direction)	Pulse output 1 (direction)		
	puto	Variable duty ratio outputs															PWM output 0	PWM output 1
Origin :	search		Origin search 0 (Origin Input Signal)	Origin search 0 (Origin Proximity Input Signal)	Origin search 1 (Origin Input Signal)	Origin search 1 (Origin Proximity Input Signal)	Origin search 0 (Positio ning Complet ed Signal)	Origin search 1 (Positio ning Complet ed Signal)									Pulse output 0 error counter reset output (operatio n modes 1 and 2)	Pulse output 1 error counter reset output (operatio n modes 1 and 2)

Pulse I/O Module 1 (on the left)

Ter	minal s	symbol	IN 10	IN 11	IN 12	IN 13	IN 14	IN 15	IN 16	IN 17	IN 18	IN 19	OUT 10	OUT 11	OUT 12	OUT 13	OUT 14	OUT 15
Addres	s		2962	1									2963					
Bit			0	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5
	Norma	al inputs	Normal input 10	Normal input 11	Normal input 12	Normal input 13	Normal input 14	Normal input 15	Normal input 16	Normal input 17	Normal input 18	Normal input 19						
	(Direct	upt inputs t Mode/ er Mode)	Interrupt input 4	Interrupt input 5	Interrupt input 6	Interrupt input 7												
Inputs	Quick inputs	response	Quick response input 4	Quick response input 5	Quick response input 6	Quick response input 7												
	High-speed counters				Highspeed counter 3 (phase- Z/ reset)	Highspeed counter 2 (phase- Z/ reset)			High- speed counter 3 (phase-A, increment, or count input)	High- speed counter 3 (phase-B, decrement, or direction input)	High- speed counter 2 (phase-A, increment, or count input)	High- speed counter 2 (phase-B, decrement, or direction input)						
	Norma	al outputs											Normal output 6	Normal output 7	Normal output 8	Normal output 9	Normal output 10	Normal output 11
		CW/CCW outputs											Pulse output 2 (CW)	Pulse output 2 (CCW)	Pulse output 3 (CW)	Pulse output 3 (CCW)		
Out puts	Pulse out puts	Pulse + direction outputs											Pulse output 2 pulse)	Pulse output 3 (pulse)	Pulse output 2 (direction)	Pulse output 3 (direction)		
	paid	Variable duty ratio outputs															PWM output 2	PWM output 3
Origin :	search		Origin search 2 (Origin Input Signal)	Origin search 2 (Origin Proximity Input Signal)	Origin search 3 (Origin Input Signal)	Origin search 3 (Origin Proximity Input Signal)	Origin search2 (Positio ning Complet ed Signal)	Origin search3 (Positio ning Complet ed Signal)									Pulse output 2 error counter reset output (operatio n modes 1 and 2)	Pulse output 3 error counter reset output (operatio n modes 1 and 2)

Specifications of Pulse Input Functions Interrupt Inputs

Item	Direct Mode	Counter Mode
Number of interrupt inputs	Max. 8 inputs	
Allocated bit	CIO 2960 and CIO 2962, bits 00 to 03	
Interrupt detection method	ON-to-OFF or OFF-to-ON transitions	
Interrupt task numbers	140 to 147 (fixed)	
Counting method		Incrimenting or decrementing (Set with the MSKS(690) instruction.)
Counting range		0001 to FFFF hex (16 bits) (Set in A532 to A535 and A544 to A547.)
Response frequency		Single-phase: 3 kHz x 8 inputs
Storage locations for PVs for interrupt inputs in Counter Mode		A536 to A539 and A548 to A551

Quick-response inputs

Item	Specifications
Number of Quick-response inputs	Max. 8 inputs
Quick-response inputs	Signals that are shorter than the cycle time are latched for one PLC cycle, so they can be detected in the PLC program. Minimum detectable pulse width is 30 µs.

High-speed Counter Inputs

	Item	Description							
Number of High-	speed Counter Inputs	Max. 4 inputs							
Pulse input meth	od (counting mode)	Incremental pulse inputs	ncremental pulse inputs Differential phase input (4x) Up/down inputs						
		Increment pulse	ncrement pulse Phase A Up pulse Pulse						
Input signals			Phase B Down pulse Direction						
			Phase Z	Reset	Reset				
Frequency and r	number of high-speed	100 kHz, 2 inputs × 2 I/O Modules	50 kHz, 2 inputs × 2 I/O Modules	100 kHz, 2 inputs × 2 I/O Modules	100 kHz, 2 inputs × 2 I/O Modules				
Counting mode		Linear mode or ring mode							
		Linear mode:	8000 0000 to 7FF						
Count value		0000 0000 to FFFF FFFF hex (for increment pulse) Ring mode: 0000 0000 to Max. ring value							
High-speed cour	nter PV storage locations	High-speed counter 0: A271 (upper 4 digits) and A270 (lower 4 digits) High-speed counter 1: A273 (upper 4 digits) and A272 (lower 4 digits) High-speed counter 2: A317 (upper 4 digits) and A316 (lower 4 digits) High-speed counter 3: A319 (upper 4 digits) and A318 (lower 4 digits) Refreshed during overseeing processing. Use PRV(881) to read the most recent PVs.							
	ū	Data format: 8 digit hexadecimal • Linear mode: 8000 0000 to 7FFF FFFF hex 0000 0000 to FFFF FFFF hex (for increment pulse) • Ring mode: 0000 0000 to Max. ring value							
	Target value comparison	Up to 48 target values and corresponding interrupt task numbers can be registered.							
Control method	Range Comparison	Up to 8 or up to 32 ranges can be registered, with a separate upper limit, lower limit, and interrupt task number for each range.							
Counter reset method		Software reset The counter is reset whe	et n the phase-Z input goes ON w n the Reset Bit (A531.00 to A53 stop or continue the comparisor	31.03) is turned ON.	,				

Specifications of Pulse Output Functions Position Control and Speed Control

Item	Specifications
Number of Pulse Outputs	Max. 4 outputs (Pulse Output 00 to 03)
Output mode	Continuous mode (for speed control) or independent mode (for position control)
Positioning (independent mode) instructions	PULS (886) and SPED (885), PULS (886) and ACC (888), or PULS2 (887) instruction
Speed control (continuous mode) instructions	SPED (885) and ACC (888) instructions
Origin (origin search and origin return) instructions	ORG (889) instruction
Interrupt feeding instruction	IFEED (892) instruction
Output frequency	1 pps to 100 kpps (1 pps units), two pulse outputs × 2 Pulse I/O Modules
Frequency acceleration and deceleration rates	Set in increments of 1 pps for acceleration/deceleration rates from 1 to 65,535 pps (every 4 ms). The acceleration and deceleration rates can be set independently only with the PLS2 (887) instruction.
Changing SVs during instruction execution	The target frequency, acceleration/deceleration rate, and target position can be changed.
Pulse output method	CW/CCW or pulse + direction
Number of output pulses	Relative coordinates: 0000 0000 to 7FFF FFFF hex (Accelerating or decelerating in either direction: 2,147,483,647) Absolute coordinates: 8000 0000 to 7FFF FFFF hex (-2,147,483,648 to 2,147,483,647)
Relative/absolute coordinate specifications for pulse output PVs	Absolute coordinates are specified automatically when the origin location has been defined by changing the pulse output PV with the INI (880) instruction or performing an origin search with the ORG(889) instruction. Relative coordinates must be used when the origin is undefined.
Relative pulse/absolute pulse specifications	The pulse type can be specified with an operand in the PULS (886) or PLS2 (887) instruction. Absolute pulses can be used when absolute coordinates are specified for the pulse output PV, i.e. the origin location has been defined. Absolute pulse cannot be used when relative coordinates are specified, i.e., when the origin location is undefined. An instruction error will occur.
Pulse output PV's storage location	The following Auxiliary Area words contain the pulse output PVs Pulse output 0: A277 (leftmost 4 digits) and A276 (rightmost 4 digits) Pulse output 1: A279 (leftmost 4 digits) and A278 (rightmost 4 digits) Pulse output 2: A323 (leftmost 4 digits) and A322 (rightmost 4 digits) Pulse output 3: A325 (leftmost 4 digits) and A324 (rightmost 4 digits) The PVs are refreshed during regular I/O refreshing.

Variable-duty Pulse Outputs (PWM)

Item	Specifications
Number of PWM Outputs	Max. 4 outputs (PWM Output 00 to 03)
Duty ratio	0.0% to 100.0% in 0.1% increments
Frequency	0.1 Hz to 6,553.5 Hz (Set in 0.1-Hz increments.) 1 Hz to 32,800 Hz (Set in 1-Hz increments.)
Output mode	Continuous Mode
Instruction	PWM (891) instruction

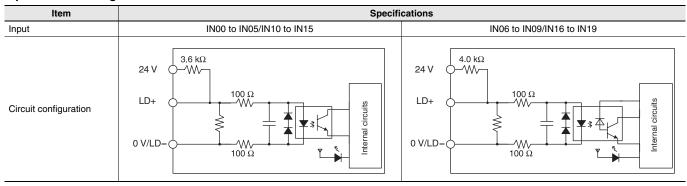
Specifications of Pulse I/O Modules

Input Specifications (IN00 to IN09/IN10 to IN19)

Normal Inputs

Inputs	IN00 to IN05 and IN10 to IN15	IN06 to IN09 and IN16 to IN19	IN00 to IN05 and IN10 to IN15	IN06 to IN09 and IN16 to IN19	
Input form	24 VDC inputs	24 VDC inputs			
Input current	6.0 mA typical	5.5 mA typical	13 mA typical	10 mA typical	
Input voltage range	24 VDC +10%/-15%			RS-422A or RS-422 line driver (conforming to AM26LS31), Power supply voltage of 5 V $\pm 5\%$	
Input impedance	3.6 kΩ	4.0 kΩ			
Number of circuits	1 common, 1 circuit	1 common, 1 circuit			
ON voltage/current	17.4 VDC min., 3 mA min				
OFF voltage/current	1 mA max. at 5 VDC max				
ON response time	8 ms max. (The input time constant can be set to 0, 0.5, 1, 2, 4, 8, 16, or 32 ms in the PLC Setup.)		_C Setup.)		
OFF response time	8 ms max. (The input time constant can be set to 0, 0.5, 1, 2, 4, 8, 16, or 32 ms in the PLC Setup.)				

Input Circuit Configuration



Interrupt Input and Quick-response Input Specifications (IN00 to IN03 and IN10 to IN13)

Item	Specifications
ON response time	30 μs max.
OFF response time	150 μs max.
Response pulse	ON 150 μs min.

High-speed Counter Input Specifications (IN06 to IN09 and IN16 to IN19)

angir speed country	24 VDC input		
	24-VDC input	Line driver input	
	Phase-A/Phase-B encoder input, Single-phase 60-kHz pulse input with 50% duty ratio	Encoder input phase A or B, single-phase 60-kHz pulse input with 50% duty ratio	
	Rise time and fall time: 3.0 μs max. 16.6 μs min.	16.6 μs min.	
	+ 10.0 μ3 11	8.3 μs min. 8.3 μs min.	
	8.3 μs min. 8.3 μs min.	ON	
	ON ON	50%	
	50%		
	OFF — \ \	OFF —	
	 		
	3 μs max. 3 μs max.		
	Phase-A/Phase-B encoder inputs, Differential phases, 30 kHz	Phase-A/Phase-B encoder inputs, Differential phases, 30 kHz	
Set to 60 kHz	Changes in phases A and B must be	Changes in phases A and B must be separated by at least	
	separated by at least 4.0 μs. 33.3 μs min.	4.0 μs.	
	ON CONTRACTOR	33.3 μs min.	
		ON	
	50%	50%	
	OFF/ \	OFF	
	ON /	ON	
	50%	50%	
	OFF \	OFF	
	T1 T2 T3 T4	 	
	T1, T2, T3, T4: 4.0 μs min.	T1 T2 T3 T4 T1, T2, T3, T4: 4.0 µs min.	
	DI 4/DI D I : 10: 1 I	Single-phase 100-kHz pulse input with	
	Phase-A/Phase-B encoder input, Single-phase 100-kHz pulse input with 50% duty ratio	50% duty ratio	
	Rise time and fall time: 2.5 µs max.	10.0 μs min.	
	10.0 μs min.	5.0 μs min. 5.0 μs min.	
	5.0 μs min. 5.0 μs min.	ON	
	ON 3.0 με πιπτ.	50%	
	50%	055	
		OFF —	
	OFF -		
	2.5 μs max. 2.5 μs max.		
	Phase-A/Phase-B encoder inputs, Differential phases, 50 kHz	Differential-phase 50-kHz pulse input Changes in phases A and B must be separated	
Set to 100 kHz	Changes in phases A and B must be	by at least 2.5 μs.	
	separated by at least 2.5 μs. 20.0 μs min.	20.0 μs min.	
	ON CONTRACTOR OF THE PROPERTY	ON	
		50%	
	50%	OFF	
	OFF/	ON	
	ON 50%	50%	
	50%	OFF —	
	OFF	 	
	T1 T2 T3 T4	T1 T2 T3 T4 T1, T2, T3, T4: 2.5 μs min.	
	T1, T2, T3, T4: 2.5 μs min.		
	Encoder input phase Z (IN02/IN03 or IN12/IN13)	Encoder input phase Z (IN02/IN03 or IN12/IN13)	
	Maintain an ON time of 30 μs min.	Maintain an ON time of 30 μs min. and an OFF time of 150 μs min.	
Phase Z/reset input	and an OFF time of 150 µs min. ON 150 µs min.	ON 30 μs min. 150 μs min.	
i nase Zireset input	50%		
	30 μs min.	50%	
	OFF —/	OFF	
	•		

Output Specifications (OUT00 to OUT05 and OUT10 to OUT15)

Item	Specifications		
Output Specifications	Sinking-type (CJ2M-MD211) Sourcing-type (CJ2M-MD212)		
Rated voltage	5 to 24 VDC		
Allowable voltage range	4.75 to 26.4 VDC		
Maximum switching current	0.3 A/output, 1.8 A/Unit		
Number of circuits	6 outputs (6 outputs/common)		
Maximum inrush current	3.0 A/output, 10 ms max. 2.0 A/output, 10 ms max.		
Leakage current	0.1 mA max.		
Residual voltage	0.6 V max.		
ON response time	0.1 ms max.		
OFF response time	0.1 ms max.		
Fuse	None		
External supply power (power supply input for outputs)	10.2 to 26.4 VDC, 20 mA min.		
Circuit configuration	Rated voltage circuit Voltag		

Pulse Outputs (OUT00 to OUT03 and OUT10 to OUT13)

Item	Specifi	cations
Output Specifications	Sinking-type (CJ2M-MD211)	Sourcing-type (CJ2M-MD212)
Rated voltage	5 to 24 VDC	
Allowable voltage range	4.75 to 26.4 VDC	
Maximum switching capacity	30 mA	
Minimum switching capacity	7 mA	
Maximum output frequency	100 kHz	
Output waveform	OFF 90% ON 10% 2 μs min. 4 μs min.	ON 90% OFF 10% 4 μs min. 2 μs min.

PWM Outputs (OUT04, OUT05, OUT14, and OUT15)

Item	Specifications		
Output Specifications	Sinking-type (CJ2M-MD211)	Sourcing-type (CJ2M-MD212)	
Rated voltage	5 to 24 VDC	•	
Allowable voltage range	4.75 to 26.4 VDC		
Maximum switching capacity	6.5535 kHz or less: 300 mA, 6.5535 to 32.8 kHz: 100 mA		
Maximum output frequency	32,800 Hz		
PWM output accuracy (for ON pulse width of 2 μs or longer)	ON duty at 6.5535 kHz or less: -0.2% to +1%, ON duty at 32.8 kHz: -1% to +5% (at switching current of 30 mA)	ON duty at 6.5535 kHz or less: ±0.5%, ON duty at 32.8 kHz: +2.5% (at switching current of 30 mA)	
Output waveform	OFF 50% ON ON duty = $\frac{t_{ON}}{T}$ × 100%	ON t_{ON} ON duty = $\frac{t_{ON}}{T}$ X 100%	

Unit Versions

Units	Models	Unit version	
CJ2M CPU Units	CJ2M-CPU3□	CPU: Unit version 1.0 (Built-in EtherNet/IP section: Unit version 2.0)	
		CPU: Unit version 2.0 (Built-in EtherNet/IP section: Unit version 2.0)	
		CPU: Unit version 1.0	
	CJ2M-CPU1□	CPU: Unit version 2.0	

Function Support by Unit Version

Functions Supported for Unit Version 2.0 or Later

The following tables show the relationship between unit versions and CX-Programmer versions.

CPU Unit	CJ2M CPU Unit		
Model	CJ2M-CPU□□		
Unit Version Function	Unit version 2.0 or higher	Unit version 1.0	
	ОК	_	

^{*} A Pulse I/O Module must be mounted for CJ2M CPU Units with unit version 2.0 or later.

Unit Versions and Programming Devices

The following tables show the relationship between unit versions and CX-Programmer versions.

Unit Versions and Programming Devices

CPU Unit Functions		Required Programming Device				
		CX-Programmer			Programming	
			Ver. 9.0 or lower	Ver. 9.1 or higher	Ver. 9.12 or higher	Console
CJ2M-CPU□□ Unit version 1.0	Functions for ur	Functions for unit version 1.0		OK *1	ОК	
CJ2M-CPU□□	Functions	Using new functions	-	_	OK	- * 3
Unit version 2.0	added for unit version 2.0	Not using new functions	_	OK * 2	ок	

^{*1.} CX-Programmer version 9.1 or higher is required to use CJ2M CPU Units.

Device Type Setting

The unit version does not affect the setting made for the device type on the CX-Programmer. Select the device type as shown in the following table regardless of the unit version of the CPU Unit.

Series	CPU Unit group	CPU Unit model	Device type setting on CX-Programmer Ver. 9.1 or higher
CJ Series	CJ2M CPU Units	CJ2M-CPU3□ CJ2M-CPU1□	CJ2M

^{*2.} It is not necessary to upgrade the version of the CX-Programmer if functionality that was enhanced for the upgrade of the CPU Unit will not be

^{*3.} A Programming Console cannot be used with a CJ2M CPU Unit.

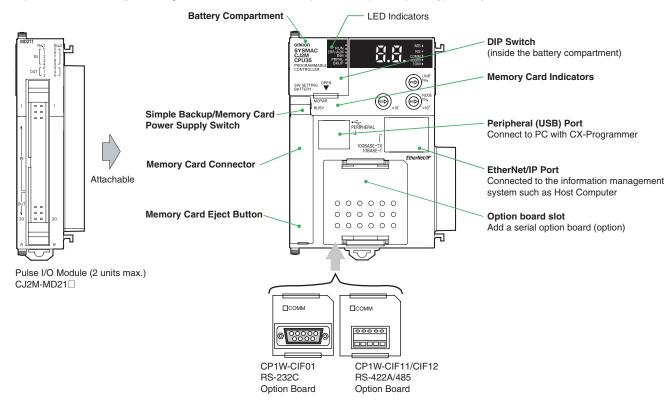
External Interface

CJ2M-CPU3□ (CJ2M with Built-in EtherNet/IP)

A CJ2M-CPU3 provides two communications ports for external interfaces: a peripheral (USB) port and an EtherNet/IP port.

The Pulse I/O functions of the CJ2M can be used by mounting a Pulse I/O Module. Up to two Pulse I/O Modules can be connected to the left side of a CJ2M CPU Unit.

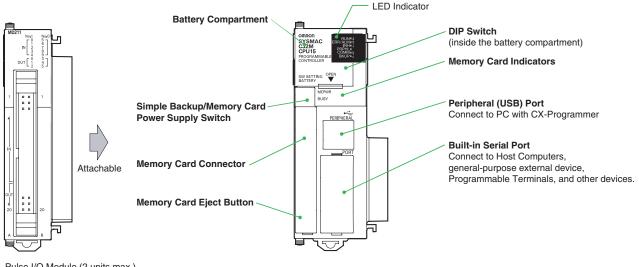
Serial ports can be added by mounting a Serial Communications Option Board (sold separately) in an option slot.



CJ2M-CPU1□

A CJ2M-CPU1□ provides two communications ports for external interfaces: a peripheral (USB) port and a serial port.

The Pulse I/O of the CJ2M can be used by mounting a Pulse I/O Module. Up to two Pulse I/O Modules can be connected to the left side of a CJ2M CPU Unit.



Pulse I/O Module (2 units max.) CJ2M-MD21 \square

Peripheral (USB) Port

Item	Specification	
Baud Rate	12 Mbps max.	
Transmission Distance	5 m max.	
Interface	USB 2.0-compliant B-type connector	
Protocol	Peripheral Bus	

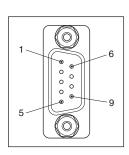
EtherNet/IP Port

Item	Specification	
Media Access Method	CSMA/CD	
Modulation	Baseband	
Transmission Paths	Star	
Baud Rate	100 Mbps (100Base-TX)	
Transmission Media	Shielded twisted-pair (STP) cable; Categories: 5, 5e	
Transmission Distance	100 m (between ethernet switch and node)	
Number of Cascade Connections	No restrictions if ethernet switch is used.	
Communications	CIP Communications (tag data links, Explicit Messages). FINS communications	

Built-in Serial Port (Only CJ2M-CPU1□)

Item	Specification			
Communications method	Half duplex			
Synchronization	Start-stop			
Baud rate	.3/0.6/1.2/2.4/4.8/9.6/19.2/38.4/57.6/115.2 kbps *			
Transmission distance	15 m max.			
Interface	EIA RS-232C			
Protocol	Host Link, NT Link, 1:N, No-protocol, or Peripheral Bus			

^{*}Baud rates for the RS-232C are specified only up to 19.2 kbps. The CJ Series supports serial communications from 38.4 kbps to 115.2 kbps, but some computers cannot support these speeds. Lower the baud rate if necessary.



Pin No.	Signal	Name	Direction
1	FG	Protection earth	-
2	SD (TXD)	Send data	Output
3	RD (RXD)	Receive data	Input
4	RS (RTS)	Request to send	Output
5	CS (CTS)	Clear to send	Input
6	5 V	Power supply	_
7	DR (DSR)	Data set ready	Input
8	ER (DTR)	Data terminal ready	Output
9	SG (0 V)	Signal ground	-
Connector hood	FG	Protection earth	-

Note: Do not use the 5-V power from pin 6 of the RS-232C port for anything but CJ1W-CIF11 RS-422A Conversion Adapter, NT-AL001 RS-232C/RS-422A Conversion Adapter and NV3W-M_20L Programmable Terminal. The external device or the CPU Unit may be damaged.

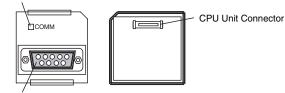
Serial Option Board (Only CJ2M-CPU3□)
A Serial Option Board can be used with a CJ2M-CPU3□ CJ2M CPU Unit.

Model	Port	Maximum transmission distance	Connection method
CP1W-CIF01	One RS-232C port	15 m	Connector: D-sub, 9-pin female
CP1W-CIF11	One RS-422A/485 port (not isolated)	50 m	Terminal block: Using ferrules
CP1W-CIF12	One RS-422A/485 port (isolated)	500 m	Terminal block: Using ferrules

CP1W-CIF01 RS-232C Option Board

Back Front

Communications Status Indicator



●RS-232C Connector

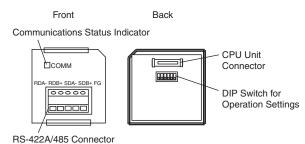
RS-232 Connector



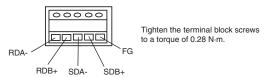
Pin No.	Signal	Name	Direction
1	FG	Protection earth	-
2	SD (TXD)	Send data	Output
3	RD (RXD)	Receive data	Input
4	RS (RTS)	Request to send	Output
5	CS (CTS)	Clear to send	Input
6	5 V	Power supply	_
7	DR (DSR)	Data set ready	Input
8	ER (DTR)	Data terminal ready	Output
9	SG (0 V)	Signal ground	-
Connector hood	FG	Protection earth	_

Note: Do not use the 5-V power from pin 6 of the RS-232C port for anything but CJ1W-CIF11 RS-422A Conversion Adapter, NT-AL001 RS-232C/ RS-422A Conversion Adapter and NV3W-M_20L Programmable Terminal. The external device or the CPU Unit may be damaged.

CP1W-CIF11/CIF12 RS-422A/485 Option Board



●RS-422A/485 Terminal Block



Connector Pin Allocations of Pulse I/O Module (40 pins)

Connector on Sinking-type I/O Module (CJ2M-MD211)

Pin layout	Terminal symbol	Input signal type	Pin	*	Terminal symbol	Input signal type	Pin	*
	IN00/IN10	24 VDC	1	A1		24 VDC	2	B1
		LD+	3	A2	IN01/IN11	LD+	4	B2
		0 V/LD-	5	А3		0 V/LD-	6	В3
		24 VDC	7	A4		24 VDC	8	B4
	IN02/IN12	LD+	9	A5	IN03/IN13	LD+	10	B5
1 2 2		0 V/LD-	11	A6		0 V/LD-	12	B6
56		24 VDC	13	A7		24 VDC	14	B7
7 8 9 10	IN04/IN14	LD+	15	A8	IN05/IN15	LD+	16	B8
11 - 12 13 - 14		0 V/LD-	17	A9		0 V/LD-	18	B9
15 16	IN06/IN16	24 VDC	19	A10	IN07/IN17	24 VDC	20	B10
19 20 20 22		LD+	21	A11		LD+	22	B11
21		0 V/LD-	23	A12		0 V/LD-	24	B12
27 28 30	IN08/IN18	24 VDC	25	A13	IN09/IN19	24 VDC	26	B13
31 32 32		LD+	27	A14		LD+	28	B14
35 36 37 38 39 40		0 V/LD-	29	A15		0 V/LD-	30	B15
39 40	OUT00/OUT10		31	A16	OUT01/OUT11		32	B16
	OUT02/OUT12		33	A17	OUT03/OUT13		34	B17
	OUT04/OUT14		35	A18	OUT05/OUT15		36	B18
	Power supply input +V for outputs		37	A19	Power supply input +V for outputs		38	B19
	СОМ		39	A20	COM		40	B20

^{*}Terminals numbers on the XW2D-_G_ Connector-Terminal Block Conversion Unit.

Sourcing-type I/O Module (CJ2M-MD212)

Pin layout	Terminal symbol	Input signal type	Pin	*	Terminal symbol	Input signal type	Pin	*
		24 VDC	1	A1		24 VDC	2	B1
	IN00/IN10	LD+	3	A2	IN01/IN11	LD+	4	B2
		0 V/LD-	5	А3		0 V/LD-	6	В3
		24 VDC	7	A4		24 VDC	8	B4
	IN02/IN12	LD+	9	A5	IN03/IN13	LD+	10	B5
1 2 2		0 V/LD-	11	A6		0 V/LD-	12	B6
5 — # • • # 6		24 VDC	13	A7		24 VDC	14	B7
7 8 10	IN04/IN14	LD+	15	A8	IN05/IN15	LD+	16	B8
11		0 V/LD-	17	A9		0 V/LD-	18	В9
15	IN06/IN16	24 VDC	19	A10		24 VDC	20	B10
19 - 20 21 - 22		LD+	21	A11	IN07/IN17	LD+	22	B11
23 - 24 25 - 26 27 - 28 29 - 30		0 V/LD-	23	A12		0 V/LD-	24	B12
27 - 28 30		24 VDC	25	A13	IN09/IN19	24 VDC	26	B13
31 - 32 33 - 34 35 - 36	IN08/IN18	LD+	27	A14		LD+	28	B14
37		0 V/LD-	29	A15		0 V/LD-	30	B15
39	OUT00/OUT10		31	A16	OUT01/OUT11		32	B16
	OUT02/OUT12		33	A17	OUT03/OUT13		34	B17
	OUT04/OUT14		35	A18	OUT05/OUT15		36	B18
	СОМ		37	A19	СОМ		38	B19
	Power supply input –V for outputs		39	A20	Power supply input -V for outputs		40	B20

^{*} Terminals numbers on the XW2D-□□G□ Connector-Terminal Block Conversion Unit.

Pulse I/O Module MIL connector Wiring Methods

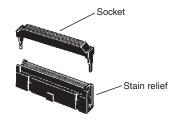
To connect to a Terminal Block, use an OMRON Cable preassembled with the special connector or attach the special connector (sold separately) to a cable yourself.

Using User-made Cables with Connector

Connector Models

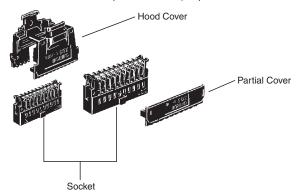
Compatible Connector Specifications

• MIL Flat Cable Connectors (40-pin Pressure-fitted Connectors)



Name	OMRON model number
Socket	XG4M-4030
Stain Relief	XG4M-4004
Set model number	XG4M-4030-T
Recommended Flat Cable	XY3A-200□

• MIL Loose Wire Crimp Connectors (40-pin Pressure-fitted Connectors)



	Name	OMRON model number
0	AWG24	XG5M-4032-N
Socket	AWG26 to AWG28	XG5M-4035-N
Spare Contacts (See note 1.)	AWG24	XG5W-0031-N
	AWG26 to AWG28	XG5W-0034-N
Hood Cover (See note 2.)		XG5S-4022
Partial Cover (See note 2.) (2 required for each socket)		XG5S-2001

Note: 1. Contacts are included with the Socket.

2. Select either the Hood Cover or the Partial Cover.

Wiring

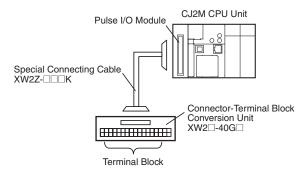
We recommend using a cable with wires sized between 28 and 24 AWG (0.2 to 0.08 mm²). Use a wire with an outer diameter of 1.61 mm max.

Compatible Terminal Blocks

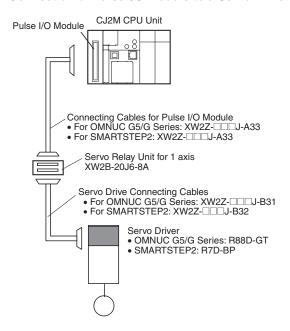
Recommended Cable	Compatible Terminal Block	Number of pins	Size	Temperature (°C)
XW2Z-□□□K	XW2D-40G6		Small	
	XW2B-40G5	40	Standard	0 to 55
	XW2B-40G4		Standard	

Using preassembled cables and terminal blocks.

Connection of Pulse I/O module to a General-purpose Terminal Block



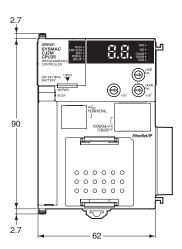
Connection of Pulse I/O module to a Servo Drive Terminal Block

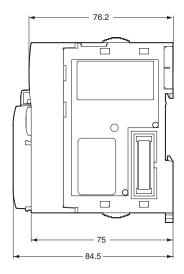


Dimensions (Unit: mm)

CJ2M-CPU3□

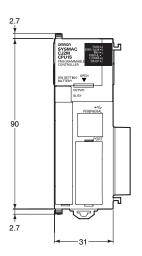


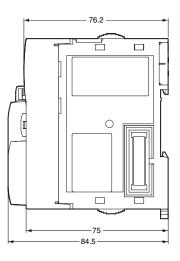




CJ2M-CPU1□

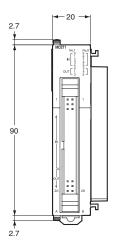


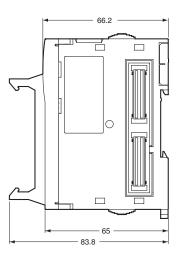




CJ2M-MD211/MD212







Related Manuals

Cat. No.	Model	Manual	Application	Description
W472	CJ2H-CPU6□-EIP CJ2H-CPU6□ CJ2M-CPU□□	CJ-series CJ2 CPU Unit Hardware User's Manual	Hardware specifications for CJ2 CPU Units	Describes the following for CJ2 CPU Units: Overview and features Basic system configuration Part nomenclature and functions Mounting and setting procedure Remedies for errors Also refer to the Software User's Manual (W473).
W473	CJ2H-CPU6□-EIP CJ2H-CPU6□ CJ2M-CPU□□	CJ-series CJ2 CPU Unit Software User's Manual	Software specifications for CJ2 CPU Units	Describes the following for CJ2 CPU Units: • CPU Unit operation • Internal memory • Programming • Settings • Functions built into the CPU Unit Also refer to the Hardware User's Manual (W472)
W486	CJ2M-CPU□□ + CJ2M-MD21□	CJ-series CJ2M CPU Unit Pulse I/O Module User's Manual	Information on using Pulse I/O on CJ2M CPU Units	Provides the following information on the CJ2M CPU Units: • Specifications and wiring methods • I/O functions • Quick-response inputs • Interrupt functions • High-speed counters • Pulse outputs • PWM outputs When programming, use this manual together with the Instructions Reference Manual (Cat. No. W474).
W474	CJ2H-CPU6 - EIP CJ2H-CPU6 - CJ2H-CPU3 - CJ2M-CPU1 - CS1G/H-CPU - H CS1G/H-CPU - H CS1G/H-CPU - H CJ1G/H-CPU - H CJ1G-CPU - CJ1M-CPU - NSJ (D)	CS/CJ/NSJ-series Instructions Reference Manual	Information on instructions	Describes each programming instruction in detail. Also refer to the <i>Software User's Manual</i> (W473) when you do programming.
W342	CJ2H-CPU6 -EIP CJ2H-CPU6 - CJ2M-CPU CS1G/H-CPU CS1G/H-CPU CS1D-CPU CS1D-CPU CS1W-SCU V1 CS1W-SCB V1 CJ1H-CPU CJ1G-CPU CJ1M-CPU CJ1M-CPU CJ1M-CPU CJ1M-CPU CJ1M-CPU CJ1M-CPU CJ1M-CPU CJ1M-SCU V1 CP1H-X CP1H-X CP1E-E/N NSJ	CS/CJ/CP/NSJ-series Communications Command Reference Manual	Information on communications for CS/CJ/CP-series CPU Units and NSJ-series Controllers	Describes C-mode commands and FINS commands Refer to this manual for a detailed description of commands for communications with the CPU Unit using C mode commands or FINS commands. Note: This manual describes the communications commands that are addressed to CPU Units. The communications path that is used is not relevant and can include any of the following: serial ports on CPU Units, communications ports on Serial Communications Units/Boards, and Communications Units. For communications commands addressed to Special I/O Units or CPU Bus Units, refer to the operation manual for the related Unit.
W465	CJ2H-CPU6□-EIP CJ2M-CPU3□ CS1W-EIP21 CJ1W-EIP21	CS and CJ Series EtherNet/IP Units CS1W-EIP21, CJ1W- EIP21, CJ2H-CPU6□- EIP, CJ2M-CPU3□ Operation Manual	Information for EtherNet/IP function of CJ2M built-in Ethernet port	Describes EtherNet/IP port/units. A basic setting, a tag data link, FINS communication, and other function are described.
W463	CXONE-AL□□C-V□/ AL□□D-V□	CX-One Setup Manual	Installing software from the CX- One	Provides an overview of the CX-One FA Integrated Tool Package and describes the installation procedure.
W446		CX-Programmer Operation Manual		
W447	WS02-CXPC□-V□	CX-Programmer Operation Manual Functions Blocks/ Structured Text	Support Software for Windows computers CX-Programmer operating procedure	Describes operating procedures for the CX-Programmer. Also refer to the <i>Software User's Manual</i> (W473) and <i>Instructions Reference Manual</i> (W474) when you do programming.
W469		CX-Programmer Operation Manual SFC Programming	p. 555641.0	
W366	WS02-SIMC1-E	CS/CJ/CP/NSJ-series CX-Simulator Operation Manual	Operating procedures for CX- Simulator Simulation Support Software for Windows computers Using simulation in the CX- Programmer with CX- Programmer version 6.1 or higher	Describes the operating procedures for the CX-Simulator. When you do simulation, also refer to the CX-Programmer Operation Manual (W446), Software User's Manual (W473), and CS/CJ/NSJ series Instructions Reference Manual (W474).
W464	CXONE-AL C-V CXONE-AL CD-V	CS/CJ/CP/NSJ-series CX-Integrator Network Configuration Software Operation Manual	Network setup and monitoring	Describes the operating procedures for the CX-Integrator.

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