**Product data sheet** 

### 1. General description

The 74LV4066 is a low-voltage Si-gate CMOS device that is pin and function compatible with the 74HC4066 and 74HCT4066.

The 74LV4066 has four independent switches. Each switch has two input/output pins (nY, nZ) and an active HIGH enable input pin (nE). When nE is LOW the corresponding analog switch is turned off.

The 74LV4066 has a ON-resistance which is reduced in comparison with the 74HCT4066.

### 2. Features and benefits

- Optimized for low-voltage applications: 1.0 V to 3.6 V
- Typical V<sub>OLP</sub> (output ground bounce): < 0.8 V at V<sub>CC</sub> = 3.3 V and T<sub>amb</sub> = 25 °C
- Accepts TTL input levels between V<sub>CC</sub> = 2.7 V and V<sub>CC</sub> = 3.6 V
- Very low ON-resistance:
  - 60  $\Omega$  (typical) at V<sub>CC</sub> = 2.0 V
  - 35  $\Omega$  (typical) at V<sub>CC</sub> = 3.0 V
  - 25  $\Omega$  (typical) at V<sub>CC</sub> = 4.5 V
- ESD protection:
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +80 °C and from -40 °C to +125 °C

### 3. Ordering information

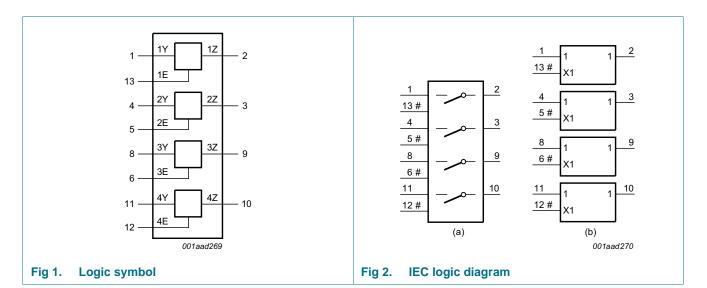
#### Table 1.Ordering information

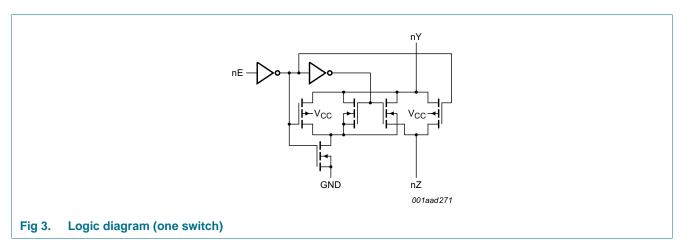
Type number	Package	kage									
	Temperature range	Femperature range         Name         Description									
74LV4066D	–40 °C to +125 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	SOT108-1							
74LV4066DB	–40 °C to +125 °C	SSOP14	plastic shrink small outline package; 14 leads; body width 5.3 mm	SOT337-1							
74LV4066PW	–40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1							

# nexperia

74LV4066 Quad bilateral switches

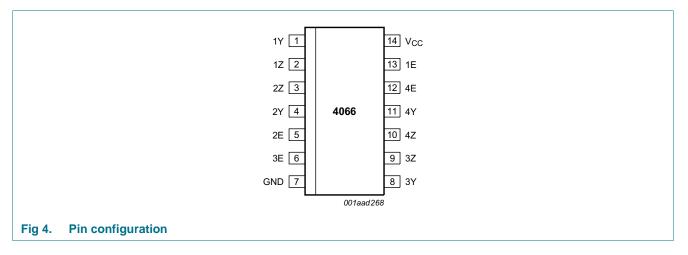
### 4. Functional diagram





### 5. Pinning information

### 5.1 Pinning



### 5.2 Pin description

#### Table 2. Pin description

Symbol	Pin	Description
1Y, 2Y, 3Y, 4Y	1, 4, 8, 11	independent input or output
1Z, 2Z, 3Z, 4Z	2, 3, 9, 10	independent output or input
GND	7	ground (0 V)
1E, 2E, 3E, 4E	13, 5, 6, 12	enable input
V <sub>CC</sub>	14	supply voltage

### 6. Functional description

#### 6.1 Function table

#### Table 3.Function table

Input nE	Switch
LOW	off
HIGH	on

### 7. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	$V_{I}$ < -0.5 V or $V_{I}$ > $V_{CC}$ + 0.5 V		-	±20	mA
I <sub>ОК</sub>	output clamping current	$V_O < -0.5$ V or $~V_O > V_{CC}$ + 0.5 V		-	±50	mA
I <sub>SW</sub>	switch current	$V_{O} = -0.5 \text{ V}$ to ( $V_{CC} + 0.5 \text{ V}$ )	<u>[1]</u>	-	±25	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$				
		SO14 package	[2]	-	500	mW
		(T)SSOP14 package	<u>[3]</u>		400	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] SO14 package:  $P_{tot}$  derates linearly with 8 mW/K above 70  $^\circ C.$ 

[3] (T)SSOP14 package:  $P_{tot}$  derates linearly with 5.5 mW/K above 60  $^\circ C.$ 

### 8. Recommended operating conditions

#### Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage	[1]	1.0	3.3	6	V
VI	input voltage		0	-	V <sub>CC</sub>	V
Vo	output voltage		0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature	in free air	-40	-	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 1.0 \text{ V} \text{ to } 2.0 \text{ V}$	-	-	500	ns/V
		$V_{CC}$ = 2.0 V to 2.7 V	-	-	200	ns/V
		V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	100	ns/V
		$V_{CC} = 3.6 \text{ V to } 5.5 \text{ V}$	-	-	50	ns/V

[1] The static characteristics are guaranteed from  $V_{CC}$  = 1.2 V to  $V_{CC}$  = 5.5 V, but LV devices are guaranteed to function down to  $V_{CC}$  = 1.0 V (with input levels GND or  $V_{CC}$ ).

### 9. Static characteristics

#### Table 6. Static characteristics

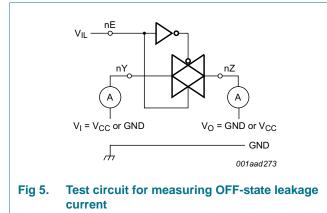
At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

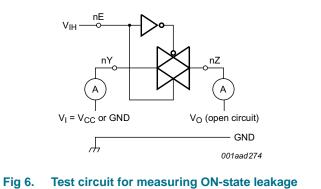
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -40	0 °C to +85 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 1.2 V	0.90	-	-	V
		$V_{CC} = 2.0 V$	1.40	-	-	V
		$V_{CC} = 2.7 V \text{ to } 3.6 V$	2.00	-	-	V
		$V_{CC} = 4.5 V$	3.15	-	-	V
		$V_{CC} = 6.0 V$	4.20	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 1.2 V	-	-	0.30	V
		$V_{CC} = 2.0 V$	-	-	0.60	V
		$V_{CC} = 2.7 V \text{ to } 3.6 V$	-	-	0.80	V
		$V_{CC} = 4.5 V$	-	-	1.35	V
		$V_{CC} = 6.0 V$	-	-	1.80	V
I	input leakage current	$V_I = V_{CC}$ or GND				
		$V_{CC} = 3.6 V$	-	-	1.0	μA
		$V_{CC} = 6.0 V$	-	-	2.0	μΑ
I <sub>S(OFF)</sub>	OFF-state leakage current	$V_{I} = V_{IH} \text{ or } V_{IL}; \text{ see } \frac{Figure 5}{Figure 5}$				
		V <sub>CC</sub> = 3.6 V	-	-	1.0	μA
		V <sub>CC</sub> = 6.0 V	-	-	2.0	μA
I <sub>S(ON)</sub>	ON-state leakage current	$V_I = V_{IH} \text{ or } V_{IL}; \text{ see } \frac{Figure 6}{Figure 6}$				
		V <sub>CC</sub> = 3.6 V	-	-	1.0	μA
		V <sub>CC</sub> = 6.0 V	-	-	2.0	μA
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A				
		V <sub>CC</sub> = 3.6 V	-	-	20	μA
		V <sub>CC</sub> = 6.0 V	-	-	40	μA
Δl <sub>CC</sub>	additional supply current	per input; V <sub>I</sub> = V <sub>CC</sub> – 0.6 V; V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	500	μA
Cı	input capacitance		-	3.5	-	pF
T <sub>amb</sub> = -40	0 °C to +125 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 1.2 V	0.90	-	-	V
		V <sub>CC</sub> = 2.0 V	1.40	-	-	V
		$V_{CC}$ = 2.7 V to 3.6 V	2.00	-	-	V
		V <sub>CC</sub> = 4.5 V	3.15	-	-	V
		V <sub>CC</sub> = 6.0 V	4.20			V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 1.2 V	-	-	0.30	V
		V <sub>CC</sub> = 2.0 V	-	-	0.60	V
		$V_{CC} = 2.7 \text{ V to } 3.6 \text{ V}$	-	-	0.80	V
		V <sub>CC</sub> = 4.5 V	-	-	1.35	V
		$V_{CC} = 6.0 V$	-	-	1.80	V

#### Table 6. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
lı	input leakage current	$V_I = V_{CC}$ or GND				
		V <sub>CC</sub> = 3.6 V	-	-	1.0	μA
		$V_{CC} = 6.0 V$	-	-	2.0	μA
S(OFF)	OFF-state leakage current	$V_I = V_{IH} \text{ or } V_{IL}; \text{ see } \frac{\text{Figure 5}}{1}$				
		V <sub>CC</sub> = 3.6 V	-	-	1.0	μA
		$V_{CC} = 6.0 V$	-	-	2.0	μA
I <sub>S(ON)</sub>	ON-state leakage current	$V_I = V_{IH} \text{ or } V_{IL}; \text{ see } \frac{\text{Figure 6}}{1}$				
		V <sub>CC</sub> = 3.6 V	-	-	1.0	μA
		$V_{CC} = 6.0 V$	-	-	2.0	μA
I <sub>CC</sub>	supply current	$V_{I} = V_{CC}$ or GND; $I_{O} = 0$ A				
		V <sub>CC</sub> = 3.6 V	-	-	40	μA
		$V_{CC} = 6.0 V$	-	-	80	μA
$\Delta I_{CC}$	additional supply current	per input; $V_I = V_{CC} - 0.6 V$ ; $V_{CC} = 2.7 V$ to 3.6 V	-	-	850	μA







#### Table 7.ON-resistance

At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for test circuit see Figure 7.

Symbol	Parameter	Conditions		Min	Typ[1]	Max	Unit
T <sub>amb</sub> = -40 °	C to +85 °C; see Figure 8						
R <sub>ON(peak)</sub>	ON resistance (peak)	$V_{I} = V_{IH} \text{ or } V_{IL}$					
		V <sub>CC</sub> = 1.2 V	[2]	-	300	-	Ω
		$V_{CC} = 2.0 V$		-	60	130	Ω
		$V_{CC} = 2.7 V$		-	41	60	Ω
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		-	37	72	Ω
		$V_{CC} = 4.5 V$		-	25	52	Ω
		$V_{CC} = 6.0 V$		-	23	47	Ω
R <sub>ON(rail)</sub>	ON resistance (rail)	$V_I = V_{IH} \text{ or } V_{IL}; V_{is} = GND$					
		V <sub>CC</sub> = 1.2 V	[2]	-	75	-	Ω
		V <sub>CC</sub> = 2.0 V		-	35	98	Ω
		V <sub>CC</sub> = 2.7 V		-	26	60	Ω
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		-	24	52	Ω
		$V_{CC} = 4.5 V$		-	15	40	Ω
		$V_{CC} = 6.0 V$		-	13	35	Ω
		$V_I = V_{IH} \text{ or } V_{IL}; V_{is} = V_{CC}$					
		V <sub>CC</sub> = 1.2 V	[2]	-	75	-	Ω
		V <sub>CC</sub> = 2.0 V		-	40	110	Ω
		$V_{CC} = 2.7 V$		-	35	72	Ω
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		-	30	65	Ω
		$V_{CC} = 4.5 V$		-	22	47	Ω
		$V_{CC} = 6.0 V$		-	20	40	Ω
R <sub>ON(flat)</sub>	ON resistance (flatness)	$V_I = V_{IH} \text{ or } V_{IL}; V_{is} = V_{CC}$					
		$V_{CC} = 2.0 V$		-	5	-	Ω
		V <sub>CC</sub> = 2.7 V		-	4	-	Ω
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		-	4	-	Ω
		$V_{CC} = 4.5 V$		-	3	-	Ω
		$V_{CC} = 6.0 V$		-	2	-	Ω
T <sub>amb</sub> = -40 °	C to +125 °C						
R <sub>ON(peak)</sub>	ON resistance (peak)	$V_{I} = V_{IH} \text{ or } V_{IL}$					
		V <sub>CC</sub> = 2.0 V		-	-	150	Ω
		V <sub>CC</sub> = 2.7 V		-	-	90	Ω
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		-	-	83	Ω
		$V_{CC} = 4.5 V$		-	-	60	Ω
		$V_{CC} = 6.0 V$		-	-	54	Ω

# 74LV4066

**Quad bilateral switches** 

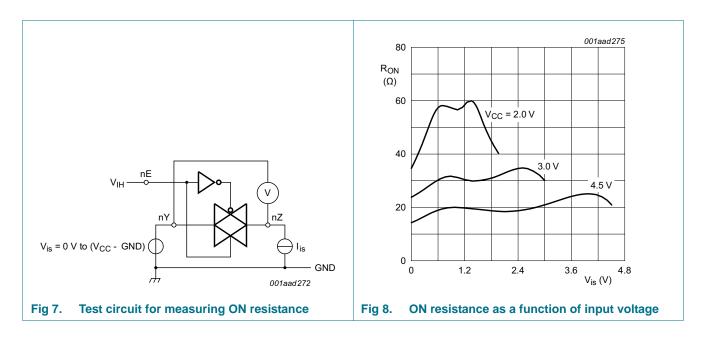
#### Table 7. ON-resistance ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for test circuit see Figure 7.

Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
R <sub>ON(rail)</sub>	ON resistance (rail)	$V_I = V_{IH} \text{ or } V_{IL}; V_{is} = GND$				
		$V_{CC} = 2.0 V$	-	-	115	Ω
		$V_{CC} = 2.7 V$	-	-	68	Ω
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	-	-	60	Ω
		$V_{CC} = 4.5 V$	-	-	45	Ω
		V <sub>CC</sub> = 6.0 V	-	-	40	Ω
		$V_I = V_{IH} \text{ or } V_{IL}; V_{is} = V_{CC}$				
		$V_{CC} = 2.0 V$	-	-	130	Ω
		V <sub>CC</sub> = 2.7 V	-	-	85	Ω
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	-	-	75	Ω
		V <sub>CC</sub> = 4.5 V	-	-	55	Ω
		V <sub>CC</sub> = 6.0 V	-	-	47	Ω

[1] All typical values are measured at  $T_{amb} = 25 \text{ °C}$ .

[2] At supply voltage approaching 1.2 V, the analog switch ON-resistance becomes extremely non-linear. Therefore it is recommended that these devices be used to transmit digital signals only, when using these supply voltages.



### **10. Dynamic characteristics**

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 11.

Symbol	Parameter	Conditions		Min	Typ[1]	Max	Unit
T <sub>amb</sub> = -40	0 °C to +85 °C	· · · · · · · · · · · · · · · · · · ·					
t <sub>pd</sub>	propagation delay	V <sub>is</sub> to V <sub>os</sub> ; see <u>Figure 9</u>	[2]				
		V <sub>CC</sub> = 1.2 V		-	8	-	ns
		$V_{CC} = 2.0 V$		-	5	26	ns
		$V_{CC} = 2.7 V \text{ to } 3.6 V$		-	3	15	ns
		$V_{CC} = 4.5 V$		-	2	13	ns
		$V_{CC} = 6.0 V$		-	2	10	ns
t <sub>on</sub>	turn-on time	nE to V <sub>os</sub> ; see Figure 9	[3]				
		V <sub>CC</sub> = 1.2 V		-	40	-	ns
		$V_{CC} = 2.0 V$		-	22	43	ns
		V <sub>CC</sub> = 2.7 V to 3.6 V		-	12	25	ns
		$V_{CC} = 3.3 \text{ V}; \text{ C}_{L} = 15 \text{ pF}$		-	10	-	ns
		$V_{CC} = 4.5 V$		-	10	21	ns
		$V_{CC} = 6.0 V$		-	8	16	ns
t <sub>off</sub>	turn-off time	nE to V <sub>os</sub> ; see Figure 9	[4]				
		V <sub>CC</sub> = 1.2 V		-	50	-	ns
		$V_{CC} = 2.0 V$		-	27	65	ns
		V <sub>CC</sub> = 2.7 V to 3.6 V		-	15	38	ns
		$V_{CC} = 3.3 \text{ V}; \text{ C}_{L} = 15 \text{ pF}$		-	13	-	ns
		$V_{CC} = 4.5 V$		-	13	32	ns
		$V_{CC} = 6.0 V$		-	12	28	ns
C <sub>PD</sub>	power dissipation capacitance	per switch; $V_{CC}$ = 3.3 V; V <sub>I</sub> = GND to V <sub>CC</sub> ; C <sub>L</sub> = 15 pF	<u>[5]</u>	-	11	-	pF
T <sub>amb</sub> = -40	0 °C to +125 °C					1	1
t <sub>pd</sub>	propagation delay	V <sub>is</sub> to V <sub>os</sub> ; see <u>Figure 9</u>	[2]				
		V <sub>CC</sub> = 2.0 V		-	-	31	ns
		$V_{CC} = 2.7 V \text{ to } 3.6 V$		-	-	18	ns
		$V_{CC} = 4.5 V$		-	-	15	ns
		$V_{CC} = 6.0 V$		-	-	12	ns
t <sub>on</sub>	turn-on time	nE to V <sub>os</sub> ; see Figure 9	[3]				
		$V_{CC} = 2.0 V$		-	-	51	ns
		$V_{CC} = 2.7 V \text{ to } 3.6 V$		-	-	30	ns
		$V_{CC} = 4.5 V$		-	-	26	ns
		V <sub>CC</sub> = 6.0 V		-	-	20	ns

74LV4066 Product data sheet

#### Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see <u>Figure 11</u>.

Symbol	Parameter	Conditions	Min	Typ <mark>[1]</mark>	Max	Unit
t <sub>off</sub>	turn-off time	nE to V <sub>os</sub> ; see <u>Figure 9</u> [4]				
		V <sub>CC</sub> = 2.0 V	-	-	81	ns
		V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	47	ns
		V <sub>CC</sub> = 4.5 V	-	-	40	ns
		V <sub>CC</sub> = 6.0 V	-	-	34	ns

[1] Typical values are measured at nominal V\_CC and T\_amb = 25 °C.

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

[3]  $t_{on}$  is the same as  $t_{PZH}$  and  $t_{PZL}$ .

[4]  $t_{off}$  is the same as  $t_{PHZ}$  and  $t_{PLZ}$ .

[5]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu W$ ).

 $\mathsf{P}_{\mathsf{D}} = \mathsf{C}_{\mathsf{P}\mathsf{D}} \times \mathsf{V}_{\mathsf{C}\mathsf{C}}^2 \times \mathsf{f}_i \times \mathsf{N} + \Sigma[(\mathsf{C}_{\mathsf{L}} + \mathsf{C}_{\mathsf{S}}) \times \mathsf{V}_{\mathsf{C}\mathsf{C}}^2 \times \mathsf{f}_o] \text{ where:}$ 

 $f_i$  = input frequency in MHz;

 $f_o$  = output frequency in MHz;

 $C_L$  = output load capacitance in pF;

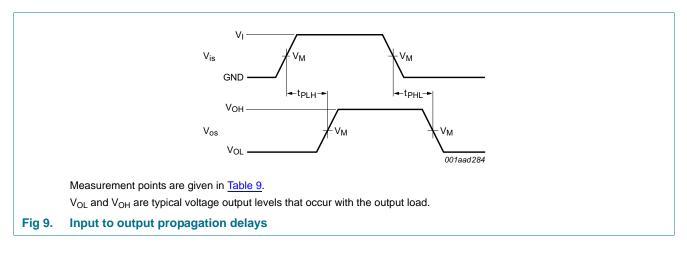
 $C_S$  = maximum switch capacitance in pF;

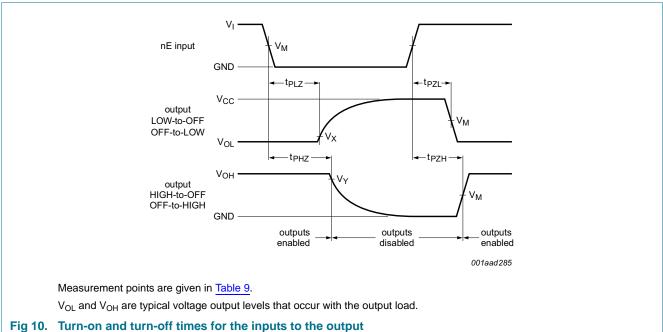
 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;

 $\Sigma[(C_L + C_S) \times V_{CC}^2 \times f_o]$  = sum of the outputs.

### 11. Waveforms





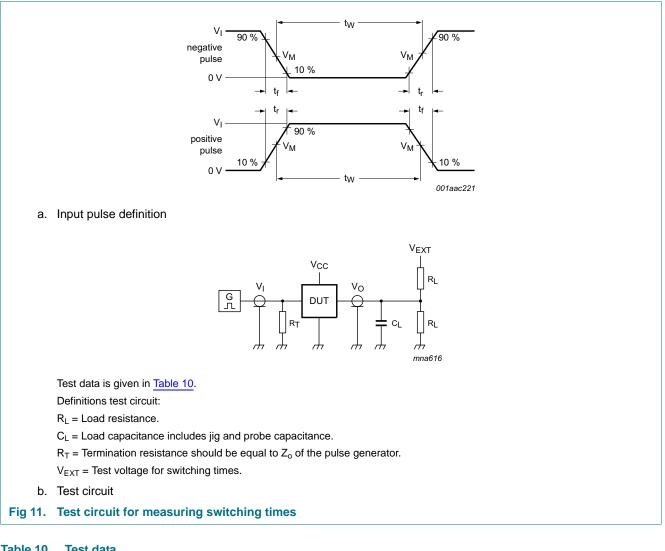
#### Table 9. Measurement points

Supply voltage	Input	Output				
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>		
≥ 2.7 V	1.5 V	1.5 V	V <sub>OL</sub> + 0.3	V <sub>OH</sub> – 0.3 V		
< 2.7 V	$0.5  imes V_{CC}$	$0.5  imes V_{CC}$	V <sub>OL</sub> + 0.15	V <sub>OH</sub> – 0.15 V		

#### Nexperia

# 74LV4066

#### **Quad bilateral switches**



#### Table 10. Test data

Supply voltage	Input		Load		V <sub>EXT</sub>			
V <sub>CC</sub>	VI	t <sub>r</sub> , t <sub>f</sub>	CL	RL <sup>[1]</sup>	t <sub>PHZ</sub> , t <sub>PZH</sub>	t <sub>PLZ</sub> , t <sub>PZL</sub>	t <sub>PLH</sub> , t <sub>PHL</sub>	
< 2.7 V	V <sub>CC</sub>	≤ 2.5 ns	50 pF	1 kΩ	GND	$2 \times V_{CC}$	open	
2.7 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	1 kΩ	GND	$2 \times V_{CC}$	open	
≥ 4.5 V	V <sub>CC</sub>	≤ 2.5 ns	50 pF	1 kΩ	GND	$2 \times V_{CC}$	open	

[1]  $R_L = \infty \Omega$  for measuring the propagation delays  $t_{PLH}$  and  $t_{PHL}$ .

### 12. Additional dynamic characteristics

#### Table 11. Additional dynamic characteristics

Voltages are referenced to GND (ground = 0 V);  $V_{is}$  is the input voltage at pin nY or nZ, whichever is assigned as an input;  $V_{os}$  is the output voltage at pin nY or nZ, whichever is assigned as an output.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
d <sub>sin</sub> sir	sine-wave distortion	$R_L$ = 10 kΩ; f = 1 kHz; $C_L$ = 50 pF; see <u>Figure 12</u>				
		V <sub>CC</sub> = 3.0 V; V <sub>is</sub> = 2.75 V (p-p)	-	0.04	-	%
		V <sub>CC</sub> = 6.0 V; V <sub>is</sub> = 5.50 V (p-p)	-	0.02	-	%
		$R_L$ = 10 kΩ; f = 10 kHz; $C_L$ = 50 pF; see <u>Figure 12</u>				
		V <sub>CC</sub> = 3.0 V; V <sub>is</sub> = 2.75 V (p-p)	-	0.12	-	%
		V <sub>CC</sub> = 6.0 V; V <sub>is</sub> = 5.50 V (p-p)	-	0.06	-	%
$lpha_{OFF(feedthru)}$	switch OFF-state signal feed-through attenuation	$R_L = 600 \text{ k}\Omega; \text{ f} = 1 \text{ MHz}; C_L = 50 \text{ pF};$ see Figure 13 and Figure 14	1]			
		V <sub>CC</sub> = 3.0 V	-	-50	-	dB
		V <sub>CC</sub> = 6.0 V	-	-50	-	dB
α <sub>ct(S)</sub>	crosstalk between switches	$R_L = 600 \text{ k}\Omega; \text{ f} = 1 \text{ MHz}; C_L = 50 \text{ pF};$ see Figure 15	[1]			
		V <sub>CC</sub> = 3.0 V	-	-60	-	dB
		V <sub>CC</sub> = 6.0 V	-	-60	-	dB
	crosstalk voltage between enable input to any switch (peak-to-peak value)	$R_L = 600 \text{ k}\Omega; \text{ f} = 1 \text{ MHz}; C_L = 50 \text{ pF};$ see Figure 16 and Figure 17	[2]			
		V <sub>CC</sub> = 3.0 V	-	110	-	mV
		V <sub>CC</sub> = 6.0 V	-	220	-	mV
f <sub>max</sub>	minimum frequency response (-3 dB)	$R_L = 50 \text{ k}\Omega; C_L = 50 \text{ pF}; \text{ see } \frac{\text{Figure 18}}{\text{Figure 19}}$	[3]			
		V <sub>CC</sub> = 3.0 V	-	180	-	MHz
		V <sub>CC</sub> = 6.0 V	-	200	-	MHz
C <sub>S</sub>	maximum switch capacitance		-	8	-	pF

[1] Adjust input voltage  $V_{is}$  is 0 dBm level (0 dBm = 1 mW into 600  $\Omega$ ).

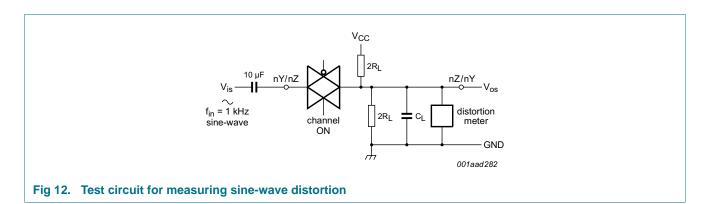
[2] Pin nE: square wave between  $V_{CC}$  and GND,  $t_r = t_f = 6$  ns.

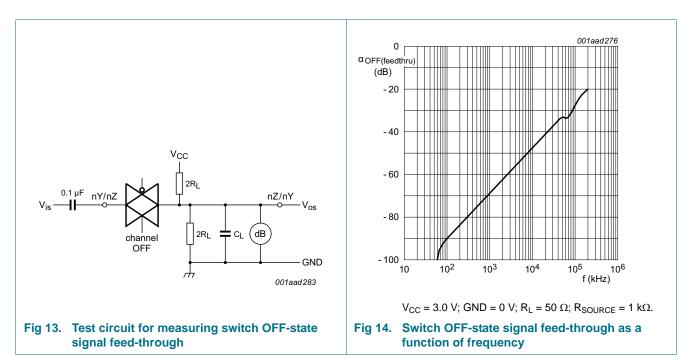
[3] Adjust input voltage V<sub>is</sub> is 0 dBm level at V<sub>os</sub> for 1 MHz (0 dBm = 1 mW into 50  $\Omega$ ).

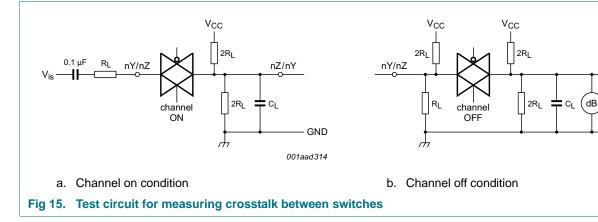
#### Nexperia

# 74LV4066

#### **Quad bilateral switches**







nZ/nY

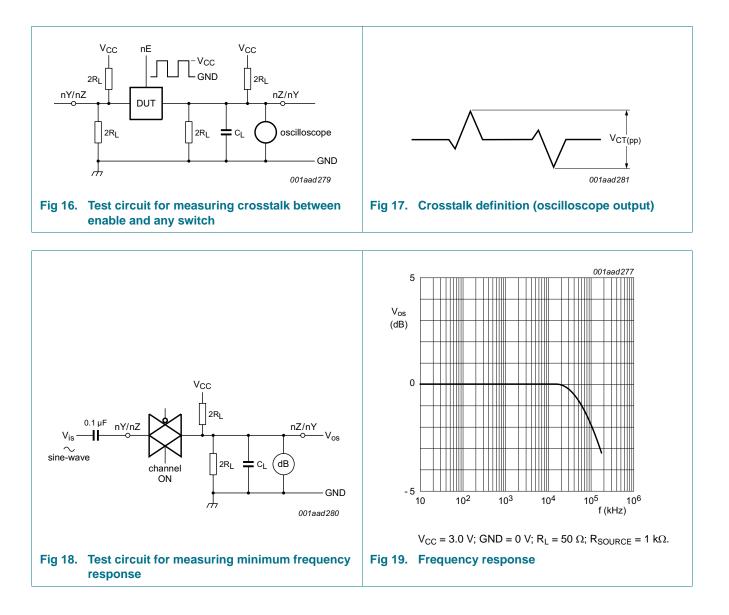
GND

001aad278

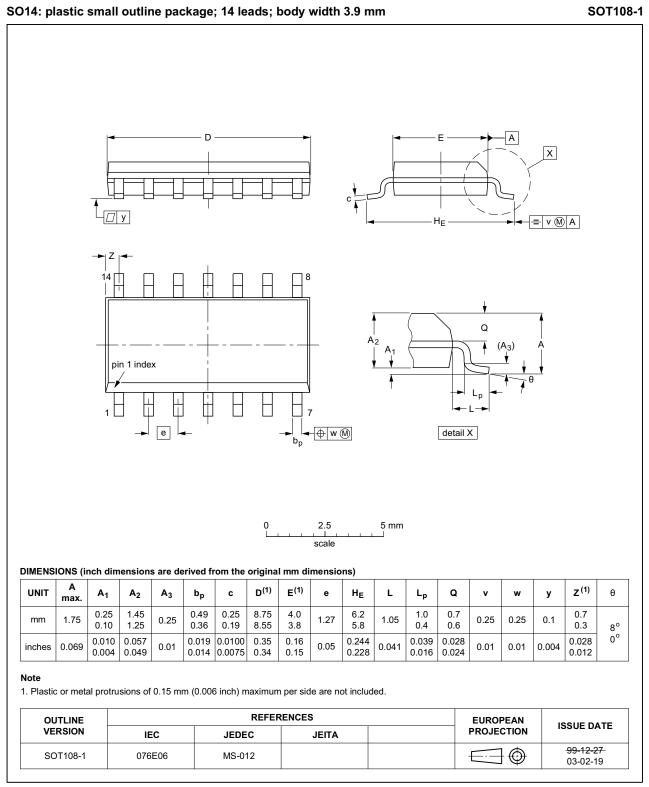
#### Nexperia

# 74LV4066

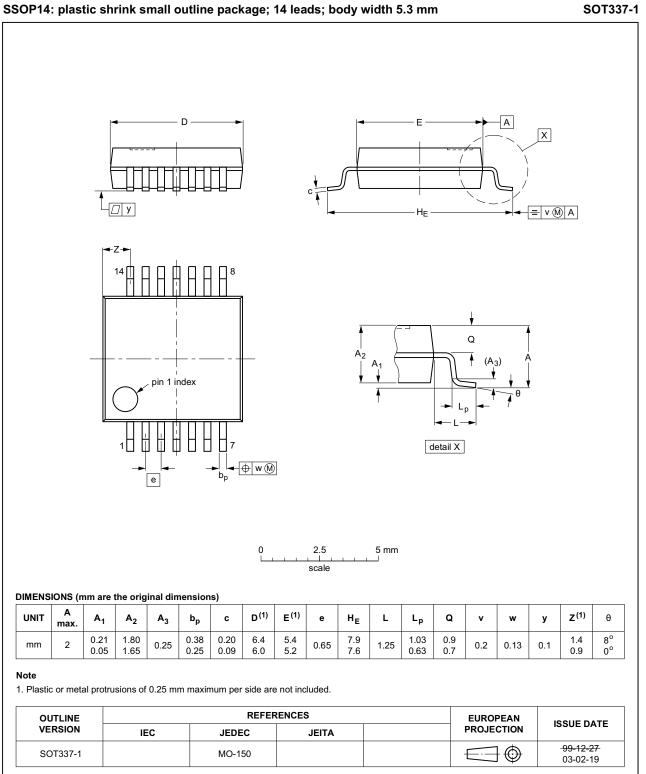
#### **Quad bilateral switches**



### 13. Package outline

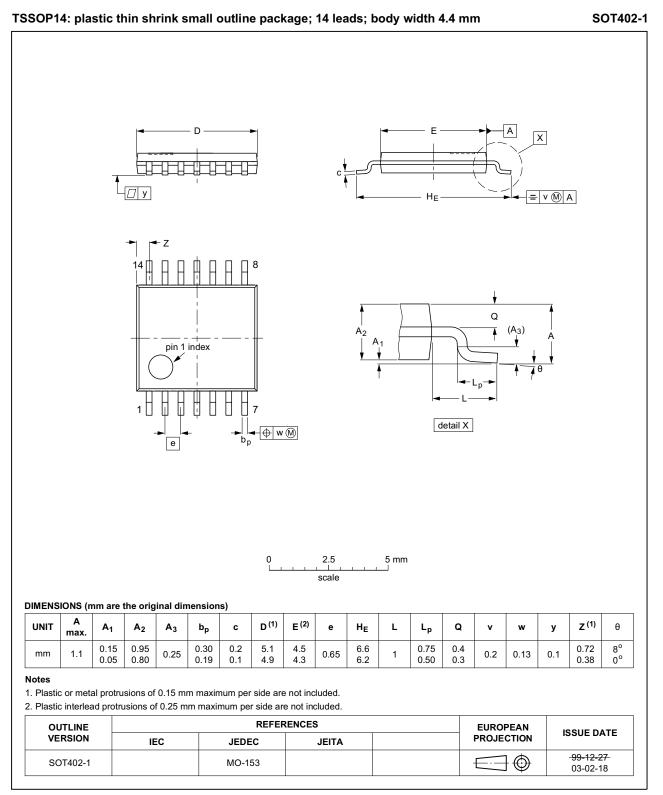


#### Fig 20. Package outline SOT108-1 (SO14)



#### SSOP14: plastic shrink small outline package; 14 leads; body width 5.3 mm

Fig 21. Package outline SOT337-1 (SSOP14)



#### Fig 22. Package outline SOT402-1 (TSSOP14)

All information provided in this document is subject to legal disclaimers.

### 14. Abbreviations

Table 12. Abbreviations				
Acronym	Description			
CMOS	Complementary Metal Oxide Semiconductor			
DUT	Device Under Test			
ESD	ElectroStatic Discharge			
НВМ	Human Body Model			
MM	Machine Model			
TTL	Transistor-Transistor Logic			

### 15. Revision history

#### Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74LV4066 v.4	20151209	Product data sheet	-	74LV4066 v.3	
Modifications:	• Type number 74LV4066N (SOT27-1) removed.			Modifications:	
74LV4066 v.3	20050704	Product data sheet	-	74LV4066 v.2	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the new presentation and information standard of Philips Semiconductors.</li> <li>Table 1: corrected package names.</li> </ul>				
74LV4066 v.2	19980623	Product specification	-	-	

### **16. Legal information**

#### 16.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.nexperia.com">http://www.nexperia.com</a>.

#### 16.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any

representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and

customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

#### 16.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

**Right to make changes** — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof. Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia

products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

#### **Nexperia**

# 74LV4066

#### **Quad bilateral switches**

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Non-automotive qualified products** — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of

non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

#### 16.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

### 17. Contact information

For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to: salesaddresses@nexperia.com

# 74LV4066

### **18. Contents**

1	General description 1
2	Features and benefits 1
3	Ordering information 1
4	Functional diagram 2
5	Pinning information 3
5.1	Pinning 3
5.2	Pin description 3
6	Functional description 3
6.1	Function table 3
7	Limiting values 4
8	Recommended operating conditions 4
9	Static characteristics 5
10	Dynamic characteristics 9
11	Waveforms 11
12	Additional dynamic characteristics 13
13	Package outline 16
14	Abbreviations 19
15	Revision history 19
16	Legal information
16.1	Data sheet status
16.2	Definitions
16.3	Disclaimers 20
16.4	Trademarks 21
17	Contact information 21
18	Contents





Общество с ограниченной ответственностью «МосЧип» ИНН 7719860671 / КПП 771901001 Адрес: 105318, г.Москва, ул.Щербаковская д.З, офис 1107

#### Данный компонент на территории Российской Федерации

Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

#### http://moschip.ru/get-element

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

#### Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж: moschip.ru moschip.ru\_4

moschip.ru\_6 moschip.ru\_9