

# **Power Management Switch ICs for PCs and Digital Consumer Products**



2.7V to 5.5V 110mΩ(Typ.)

0.01µA (Typ.)

0.2A min., 0.4A max.

# 1ch Small Package **High Side Switch ICs** for USB Devices and Memory Cards

# **BD2248G**

#### Description

BD2248G is low on-resistance N-channel MOSFET high-side power switches, optimized for Universal Serial Bus (USB) applications. BD2248G is equipped with the function of over-current detection, thermal shutdown, under-voltage lockout and soft-start.

#### Features

- Reverse Current Protection when Power Switch Off
- **Output Discharge Function**
- **Over-Current Detection**
- Thermal Shutdown
- **Open-Drain Fault Flag Output**
- Under-Voltage Lockout
- Soft-Start Circuit
- Control Input Logic Active-High
- ESD protection

#### Applications

USB hub in consumer appliances, Car accessory, PC, PC peripheral equipment, and so forth

## Key Specifications

- Input voltage range:
- ON resistance: (VIN=5V)
- Over current threshold:
- Standby current:
- Operating temperature range: -40°C to +85°C



## Typical Application Circuit



Figure 1. Typical Application Circuit

# Block Diagram



Figure 2. Block Diagram

## Pin Configuration



Figure 3. Pin Configuration (TOP VIEW)

## Pin Descriptions

Pin No.	Symbol	I/O	Function
1	VIN	-	Switch input and the supply voltage for the IC.
2	GND	-	Ground.
3	EN	I	Enable input. High level input turns on the switch.
4	/OC	0	Over-current notification terminal. Low level output during over-current or over-temperature condition. Open-drain fault flag output.
5	VOUT	0	Switch output.

## ● Absolute Maximum Ratings(Ta=25°C)

Parameter	Symbol	Ratings	Unit
VIN supply voltage	Vin	-0.3 to 6.0	V
EN input voltage	Ven	-0.3 to 6.0	V
/OC voltage	V/oc	-0.3 to 6.0	V
/OC sink current	I/oc	5	mA
VOUT voltage	Vout	-0.3 to 6.0	V
Storage temperature	Tstg	-55 to 150	°C
Power dissipation	Pd	675 <sup>*1</sup>	mW

\*1 Mounted on 70mm x 70mm x 1.6mm glass epoxy board. Reduce 5.4mW per 1°C above 25°C

#### Recommended Operating Ratings

Deremeter	Symbol		Lloit			
Falameter	Symbol	Min.	Тур.	Max.	Unit	
VIN operating voltage	Vin	2.7	5.0	5.5	V	
Operating temperature	TOPR	-40	-	85	C°	

## ●Electrical Characteristics (VIN= 5V, Ta= 25°C, unless otherwise specified.)

DC Characteristics

Doromotor	Sumbol	Limits		Linit	Conditions		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Operating ourrant	Idd	•	130	175	μA	VEN = 5V, VOUT = open, VIN = 5V	
Operating current		•	90	120		VEN = 3.3V, VOUT = open, VIN = 3.3V	
Standby current	Isтв	•	0.01	5	μA	VEN = 0V, VOUT = open, VIN = 5V	
	Venh	2.0	-	-	V	High input, $VIN = 3.3$ to 5V	
EN input voltage		-	-	0.8	V	Low input, VIN = 5V	
	VENL	-	-	0.6		Low input, VIN = 3.3V	
EN input leakage	IEN	-1	0.01	1	μA	VEN = 0V or 5V	
On registeres	Ron	-	110	155	mΩ	IOUT = 100mA, VIN = 5V	
On-resistance		-	130	180		IOUT = 100mA, VIN = 3.3V	
Reverse leak current	IREV	-	-	1	μA	VOUT = 5V, VIN = 0V	
Over eurrent threshold	Ітн	200	300	400	~^^	VIN = 5V	
Over-current threshold		190	290	390	ШA	VIN = 3.3V	
Short circuit output current	Isc	100	200	300	mA	VOUT = $0V$ , RMS, VIN = $3.3$ to $5V$	
Output discharge resistance	RDISC	30	60	120	Ω	IDISC = 1mA, VIN = 5V	
Output discharge resistance		50	100	200		IDISC = 1mA, VIN = 3.3V	
/OC output low voltage	V/oc	-	-	0.4	V	I/OC = 0.5mA, VIN = 3.3 to 5V	
LIV/LO threaded	ντυνη	2.1	2.3	2.5	V	Vin increasing	
UVLU threshold	VTUVL	2.0	2.2	2.4	V	Vin decreasing	

## AC Characteristics

Devenueter	Symbol	Limits			1.1	Conditions	
Parameter		Min.	Тур.	Max.	Unit	Conditions	
Output rise time	Ton1	-	1	6	ms	$RL = 500\Omega$ , $VIN = 3.3$ to $5V$	
Output turn-on time	Ton2	-	1.5	10	ms	$R_L = 500\Omega$ , $V_{IN} = 3.3$ to $5V$	
Output fall time	TOFF1	-	1	20	μs	$R_L = 500\Omega$ , $V_{IN} = 3.3$ to $5V$	
Output turn-off time	TOFF2	-	3	40	μs	$R_L = 500\Omega$ , $V_{IN} = 3.3$ to $5V$	
100 dalay time	T/oc	10	15	20	ms	VIN = 5V	
		11	16	21		VIN = 3.3V	

## Measurement Circuit



Operating current



On-resistance, Over-current detection



EN, Input voltage, Output rise/fall time



/OC Output low voltage



# Timing Diagram



Figure 5. Output rise/fall time

## Typical Performance Curves















## Typical Wave Forms







## Typical Wave Forms - continued



## Typical Wave Forms - continued



## Typical Application Circuit



Figure 41. Typical application circuit

#### • Application Information

When excessive current flows owing to output shortcircuit or so, ringing occurs by inductance of power source line to IC, and may cause bad influences upon IC actions. In order to avoid this case, connect a bypath capacitor CIN by VIN terminal and GND terminal of IC. 1 $\mu$ F or higher is recommended. In order to decrease voltage fluctuations of power source line to IC, connect a low ESR capacitor in parallel with CIN. 10 $\mu$ F to 100 $\mu$ F or higher is effective.

Pull up /OC output by resistance  $10k\Omega$  to  $100k\Omega$ .

Set up value, which satisfies the application as CL.

This system connection diagram doesn't guarantee operating as the application.

When using the circuit with changes to the external circuit constants, make sure to leave an adequate margin for external components including static and transitional characteristics as well as dispersion of the IC.

#### Functional Description

1. Switch Operation

VIN terminal and VOUT terminal are connected to the drain and the source of switch MOSFET respectively. And the VIN terminal is used also as power source input to internal control circuit.

When the switch is turned on from EN control input, VIN terminal and VOUT terminal are connected by an  $110m\Omega(Typ.)$  switch. In on status, the switch is bidirectional. Therefore, when the potential of VOUT terminal is higher than that of VIN terminal, current flows from VOUT terminal to VIN terminal.

Since a parasitic diode between the drain and the source of switch MOSFET is canceled, in the off status, it is possible to prevent current from flowing reversely from VOUT to VIN.

2. Thermal Shutdown Circuit (TSD)

If over-current would continue, the temperature of the IC would increase drastically. If the junction temperature were beyond 135°C(Typ.) in the condition of over-current detection, thermal shutdown circuit operates and makes power switch turn off and outputs fault flag (/OC). Then, when the junction temperature decreases lower than 115°C(Typ.), power switch is turned on and fault flag (/OC) is cancelled. Unless the fact of the increasing chips temperature is removed or the output of power switch is turned off, this operation repeats.

The thermal shutdown circuit operates when the switch is on (EN signal is active).

3. Over-Current Detection (OCD)

The over-current detection circuit limits current ( $I_{SC}$ ) and outputs fault flag (/OC) when current flowing in each switch MOSFET exceeds a specified value. There are three types of response against over-current. The over-current detection circuit works when the switch is on (EN signal is active).

3-1. When the switch is turned on while the output is in shortcircuit status

When the switch is turned on while the output is in shortcircuit status or so, the switch gets in current limit status soon.

- 3-2. When the output shortcircuits while the switch is on When the output shortcircuits or large capacity is connected while the switch is on, very large current flows until the over-current limit circuit reacts. When the current detection, limit circuit works, current limitation is carried out.
- 3-3. When the output current increases gradually When the output current increases gradually, current limitation does not work until the output current exceeds the over-current detection value. When it exceeds the detection value, current limitation is carried out.
- 4. Under-Voltage Lockout (UVLO)

UVLO circuit prevents the switch from turning on until the VIN exceeds 2.3V(Typ.). If the VIN drops below 2.2V(Typ.) while the switch turns on, then UVLO shuts off the power switch. UVLO has hysteresis of a 100mV(Typ). Under-voltage lockout circuit works when the switch is on (EN signal is active).

#### 5. Fault Flag (/OC) Output

Fault flag output is N-MOS open drain output. At detection of over-current, thermal shutdown, low level is output.

Over-current detection has delay filter. This delay filter prevents instantaneous current detection such as inrush current at switch on, hot plug from being informed to outside.



Figure 42. Over-current detection



Figure 43. Over-current detection, Thermal shutdown timing

# BD2248G

#### •Power Dissipation

(SSOP5 package)





## ●I/O Equivalence Circuit



## Operational Notes

#### (1) Absolute maximum ratings

An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down devices, thus making impossible to identify breaking mode such as a short circuit or an open circuit. If any special mode exceeding the absolute maximum ratings is assumed, consideration should be given to take physical safety measures including the use of fuses, etc.

#### (2) Operating conditions

These conditions represent a range within which characteristics can be provided approximately as expected. The electrical characteristics are guaranteed under the conditions of each parameter.

#### (3) Reverse connection of power supply connector

The reverse connection of power supply connector can break down ICs. Take protective measures against the breakdown due to the reverse connection, such as mounting an external diode between the power supply and the IC's power supply terminal.

(4) Power supply line

Design PCB pattern to provide low impedance for the wiring between the power supply and the GND lines. In this regard, for the digital block power supply and the analog block power supply, even though these power supplies has the same level of potential, separate the power supply pattern for the digital block from that for the analog block, thus suppressing the diffraction of digital noises to the analog block power supply resulting from impedance common to the wiring patterns. For the GND line, give consideration to design the patterns in a similar manner.

Furthermore, for all power supply terminals to ICs, mount a capacitor between the power supply and the GND terminal. At the same time, in order to use an electrolytic capacitor, thoroughly check to be sure the characteristics of the capacitor to be used present no problem including the occurrence of capacity dropout at a low temperature, thus determining the constant.

#### (5) GND voltage

Make setting of the potential of the GND terminal so that it will be maintained at the minimum in any operating state. Furthermore, check to be sure no terminals are at a potential lower than the GND voltage including an actual electric transient.

#### (6) Short circuit between terminals and erroneous mounting

In order to mount ICs on a set PCB, pay thorough attention to the direction and offset of the ICs. Erroneous mounting can break down the ICs. Furthermore, if a short circuit occurs due to foreign matters entering between terminals or between the terminal and the power supply or the GND terminal, the ICs can break down.

#### (7) Operation in strong electromagnetic field

Be noted that using ICs in the strong electromagnetic field can malfunction them.

(8) Inspection with set PCB

On the inspection with the set PCB, if a capacitor is connected to a low-impedance IC terminal, the IC can suffer stress. Therefore, be sure to discharge from the set PCB by each process. Furthermore, in order to mount or dismount the set PCB to/from the jig for the inspection process, be sure to turn OFF the power supply and then mount the set PCB to the jig. After the completion of the inspection, be sure to turn OFF the power supply and then dismount it from the jig. In addition, for protection against static electricity, establish a ground for the assembly process and pay thorough attention to the transportation and the storage of the set PCB.

(9) Input terminals

In terms of the construction of IC, parasitic elements are inevitably formed in relation to potential. The operation of the parasitic element can cause interference with circuit operation, thus resulting in a malfunction and then breakdown of the input terminal. Therefore, pay thorough attention not to handle the input terminals, such as to apply to the input terminals a voltage lower than the GND respectively, so that any parasitic element will operate. Furthermore, do not apply a voltage to the input terminals when no power supply voltage is applied to the IC. In addition, even if the power supply voltage is applied, apply to the input terminals a voltage lower than the power supply voltage or within the guaranteed value of electrical characteristics.

(10) Ground wiring pattern

If small-signal GND and large-current GND are provided, It will be recommended to separate the large-current GND pattern from the small-signal GND pattern and establish a single ground at the reference point of the set PCB so that resistance to the wiring pattern and voltage fluctuations due to a large current will cause no fluctuations in voltages of the small-signal GND. Pay attention not to cause fluctuations in the GND wiring pattern of external parts as well.

(11) External capacitor

In order to use a ceramic capacitor as the external capacitor, determine the constant with consideration given to a degradation in the nominal capacitance due to DC bias and changes in the capacitance due to temperature, etc.

(12) Thermal shutdown circuit (TSD)

When junction temperatures become detected temperatures or higher, the thermal shutdown circuit operates and turns a switch OFF. The thermal shutdown circuit is aimed at isolating the LSI from thermal runaway as much as possible. Do not continuously use the LSI with this circuit operating or use the LSI assuming its operation.

#### (13) Thermal design

Perform thermal design in which there are adequate margins by taking into account the power dissipation (Pd) in actual states of use.

## Ordering Information



## Physical Dimension Tape and Reel Information

SSOP5



#### Marking Diagram



Status of this document

The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority.

## Revision History

Date	Revision	Changes
25.JUL.2012	001	New Release

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