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March 2011

### FDS86140

# N-Channel PowerTrench<sup>®</sup> MOSFET 100 V, 11.2 A, 9.8 m $\Omega$

#### **Features**

- Max  $r_{DS(on)} = 9.8 \text{ m}\Omega$  at  $V_{GS} = 10 \text{ V}$ ,  $I_D = 11.2 \text{ A}$
- Max  $r_{DS(on)} = 16 \text{ m}\Omega$  at  $V_{GS} = 6 \text{ V}$ ,  $I_D = 9 \text{ A}$
- High performance trench technologh for extremely low r<sub>DS(on)</sub>
- High power and current handing capability in a widely used surface mount package
- 100% UIL Tested
- RoHS Compliant

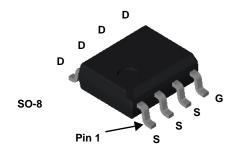


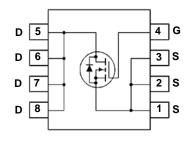
#### **General Description**

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced Power Trench® process that has been optimized for  $r_{DS(on)}$ , switching performance and ruggedness.

#### **Applications**

- DC/DC Converters and Off-Line UPS
- Distributed Power Architectures and VRMs
- Primary Swith for 24 V and 48 V Systems
- High Voltage Synchronous Rectifier





#### MOSFET Maximum Ratings T<sub>A</sub> = 25 °C unless otherwise noted

Symbol	Parameter			Ratings	Units
$V_{DS}$	Drain to Source Voltage			100	V
$V_{GS}$	Gate to Source Voltage			±20	V
I <sub>D</sub>	Drain Current -Continuous			11.2	۸
	-Pulsed			50	A
E <sub>AS</sub>	Single Pulse Avalanche Energy		(Note 3)	264	mJ
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25 °C	(Note 1)	5.0	W
	Power Dissipation $T_A = 25 ^{\circ}\text{C}$ (Note 1a)			2.5	VV
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperation		-55 to +150	°C	

#### **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Note 1)	25	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	50	C/VV

#### **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDS86140	FDS86140	SO-8	13"	12 mm	2500 units

#### **Electrical Characteristics** T<sub>J</sub> = 25 °C unless otherwise noted

Symbol	Parameter Test Conditions		Min	Тур	Max	Units
Off Chara	acteristics					
$BV_{DSS}$	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	100			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, referenced to 25 °C		70		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V			1	μΑ
I <sub>GSS</sub>	Gate to Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

#### On Characteristics

V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2	2.7	4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, referenced to 25 °C		-11		mV/°C
r <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 11.2 A		8.1	9.8	mΩ
		$V_{GS} = 6 \text{ V}, I_D = 9 \text{ A}$		10.8	16	
		$V_{GS} = 10 \text{ V, } I_{D} = 11.2 \text{ A,}$ $T_{J} = 125 ^{\circ}\text{C}$		13.1	17	
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 10 \text{ V}, I_D = 11.2 \text{ A}$		35		S

#### **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V 50.V/V 0.V/	1940	2580	pF
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V},$ $f = 1 \text{ MHz}$	440	585	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 - 1 1/1/12	20	30	pF
R <sub>a</sub>	Gate Resistance		0.9		Ω

#### **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time		13.7	25	ns
t <sub>r</sub>	Rise Time	V <sub>DD</sub> = 50 V, I <sub>D</sub> = 11.2 A,	5.6	11	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$	23	38	ns
t <sub>f</sub>	Fall Time		4.8	10	ns
Qg	Total Gate Charge	V <sub>GS</sub> = 0 V to 10 V	29	41	nC
$Q_g$	Total Gate Charge	$V_{GS} = 0 \ V \text{ to 5 } V V_{DD} = 50 \ V,$	16.5	23	nC
$Q_{gs}$	Gate to Source Charge	I <sub>D</sub> = 11.2 A	8.0		nC
$Q_{gd}$	Gate to Drain "Miller" Charge		6.5		nC

#### **Drain-Source Diode Characteristics**

$V_{SD}$	Source-Drain Dioge Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 11.2 \text{ A}$ (Note 2)	0.8	1.3	\/
		$V_{GS} = 0 \text{ V, } I_{S} = 2 \text{ A}$ (Note 2)	0.7	1.2	v
t <sub>rr</sub>	Reverse Recovery Time	$I_{\rm F} = 11.2 \text{ A, di/dt} = 100 \text{ A/µs}$	53	85	ns
Q <sub>rr</sub>	Reverse Recovery Charge	iF = 11.2 A, αί/αι = 100 A/μS	59	94	nC

<sup>1.</sup> R<sub>0,1A</sub> is determined with the device mounted on a 1 in<sup>2</sup> pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R<sub>0,1C</sub> is guaranteed by design while R<sub>0,CA</sub> is determined by the user's board design.



a) 50 °C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper.



b) 125 °C/W when mounted on a minimum pad.

<sup>2.</sup> Pulse Test: Pulse Width < 300  $\mu s$ , Duty cycle < 2.0%. 3. Starting T  $_J$  = 25 °C,  $\,$  L = 1 mH,  $I_{AS}$  = 23 A,  $V_{DD}$  = 90 V,  $V_{GS}$  = 10 V.

#### Typical Characteristics T<sub>J</sub> = 25 °C unless otherwise noted

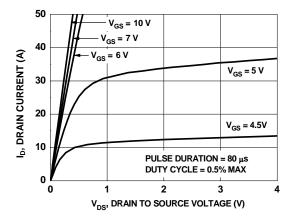


Figure 1. On Region Characteristics

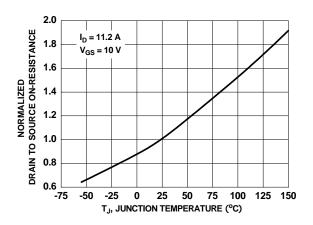


Figure 3. Normalized On Resistance vs Junction Temperature

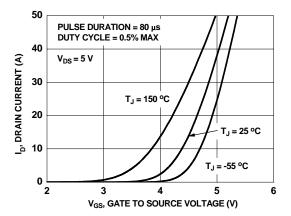


Figure 5. Transfer Characteristics

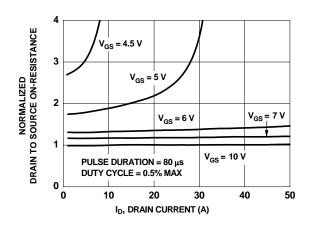


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

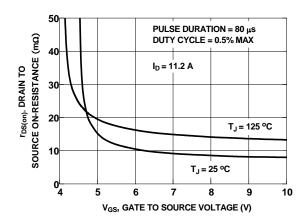


Figure 4. On-Resistance vs Gate to Source Voltage

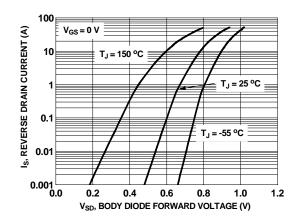


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

#### Typical Characteristics T<sub>J</sub> = 25 °C unless otherwise noted

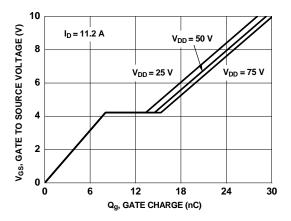


Figure 7. Gate Charge Characteristics

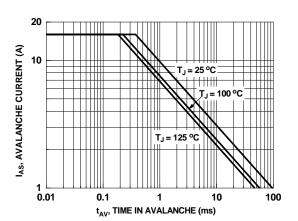


Figure 9. Unclamped Inductive Switching Capability

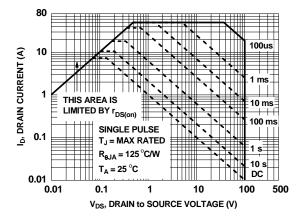


Figure 11. Forward Bias Safe Operating Area

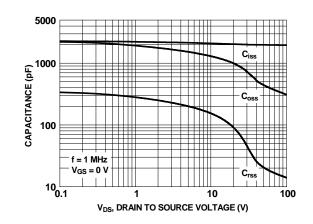


Figure 8. Capacitance vs Drain to Source Voltage

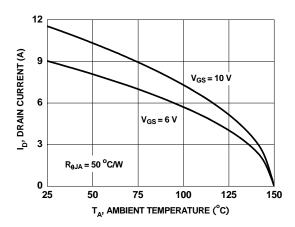


Figure 10. Maximum Continuous Drain Current vs Ambient Temperature

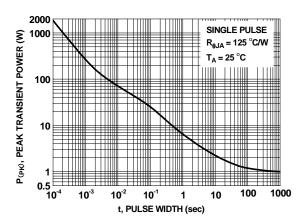


Figure 12. Single Pulse Maximum Power Dissipation

### **Typical Characteristics** $T_J = 25$ °C unless otherwise noted

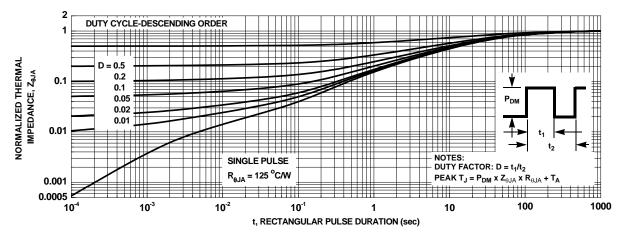
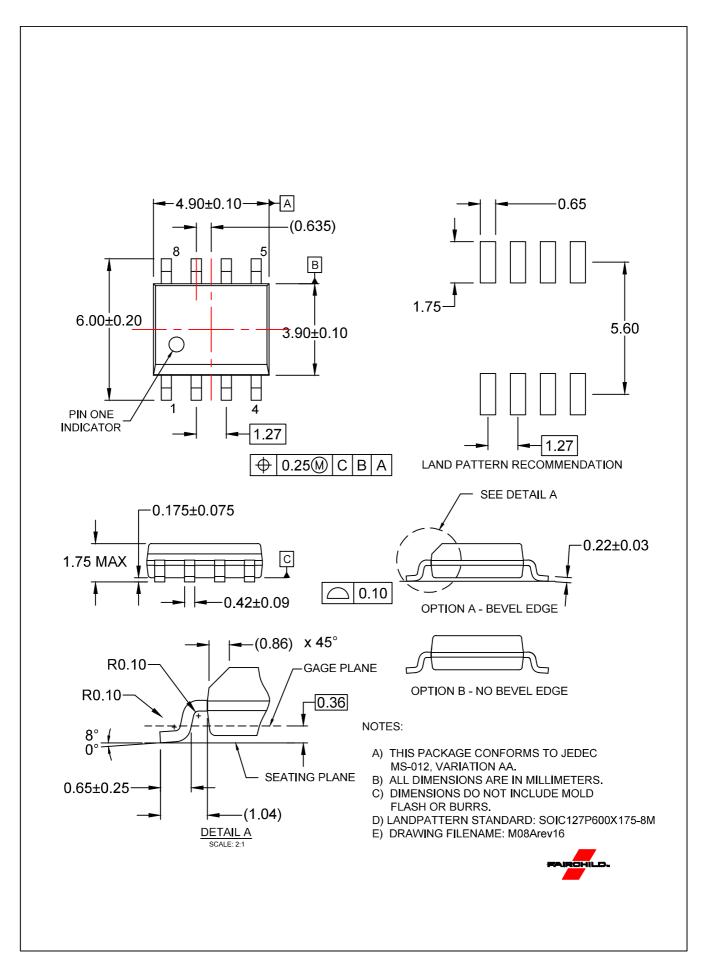


Figure 13. Junction-to-Ambient Transient Thermal Response Curve



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