

## Off-line Controllers for LED Lighting

## ■ GENERAL DESCRIPTION

The XC9401 series are off-line controller ICs for LED lighting. Through optimization of the external components, these ICs can be made to operate in a range from 85VAC to 270VAC, as well as by DC input, and a diversity of specifications can be achieved by selecting components appropriate for the circuit configuration. Fixed off-time control is used for the basic control method, and by detecting the current that flows to the external power MOSFET, the current that flows to the LED is monitored to provide a stable power supply for LED lighting. Three product series differing by function type are available, the XC9401A type, XC9401B type and the XC9401C type.

The circuit configuration of type A is designed for the power factor, achieving a high power factor by synchronizing the LED current to the input current (sine wave).

Type B and C holds the peak current due to switching that flows to the external power MOSFET constant, enabling the LED current to be kept constant.

With the type C, dimming is possible by inputting a PWM signal into the EN/DIM pin to set the LED current within a range of 1% to 100%.

## ■ APPLICATIONS

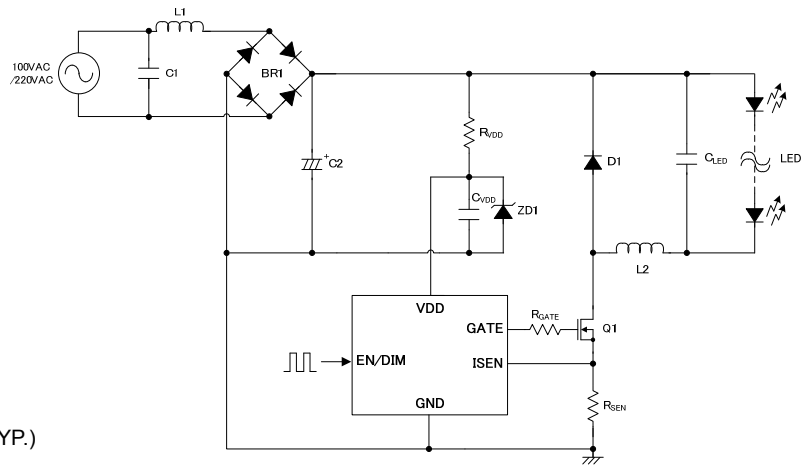
- LED bulb (GU-10, MR-16, Other)
- LED tube lights
- LED down lights
- LED spot lights
- LED stands

## ■ FEATURES

Operating Voltage	: 85VAC ~ 270VAC
	DC Input
I <sub>SEN</sub> Voltage	: 0.34V ±2.5% (Type C)
Fixed Off-time	: 6.0μs
Protection Circuits	: Thermal Shutdown 150°C (TYP.)
	V <sub>DD</sub> Over voltage protection, V <sub>DD</sub> =18V (TYP.)
	UVLO, V <sub>DD</sub> =6.5V (TYP.)
	Over current protection, V <sub>ISEN</sub> =0.7V (TYP.)
Dimming	: PWM Dimming (LED Current:1%~100%@1kHz, Type C)
Packages	: SOT-26 (Type A, Type B) SOT-25 (Type C)
Operating Ambient Temperature	: -40°C ~ +85°C
Environmentally Friendly	: EU RoHS Compliant, Pb Free

## ■ TYPICAL APPLICATION CIRCUITS

(XC9401C60CSR-G Non-isolation buck Type)



## ■ SOLUTION EXAMPLES

Input Voltage	Product	Isolation/ Non-Isolation	Topology	Reference designs			
				Output Power	LED Voltage/ LED Current	Efficiency/ Power Factor	Option
100VAC/110VAC	XC9401C60CSR-G	Non-Isolation	Buck	7.2W	60V/120mA	91%/0.5~0.65	-
220VAC/240VAC	XC9401C60CSR-G	Non-Isolation	Buck	7.2W	60V/120mA	85%/0.5~0.65	-
	XC9401C60CSR-G	Isolation	Flyback	7.0W	19.2V/360mA	83%/0.5~0.65	-
	XC9401A605MR-G	Isolation	Flyback	7.2W	20V/360mA	82%/0.9~	-
400VDC	XC9401C60CSR-G	Non-Isolation	Buck	104W	200V/520mA	97%	With PFC circuit
15VDC	XC9401C60CSR-G	-	Buck	3.5W	9.6V/350mA	92%	-
12VAC	XC9401C60CSR-G	-	Buck	2.7W	9V/300mA	87%	For MR16

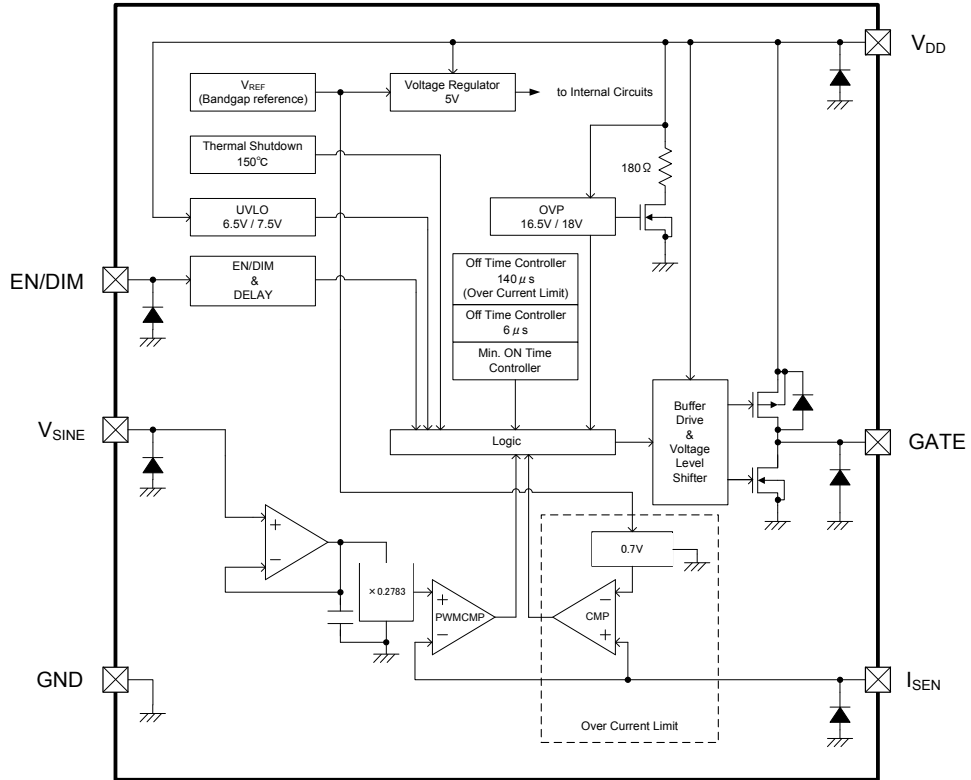
\*Due to dispersion of constant values of external components, the above values may be deviated.

Please understand that the above are typical values. For details, refer to the XC9401 Series Application Notes.

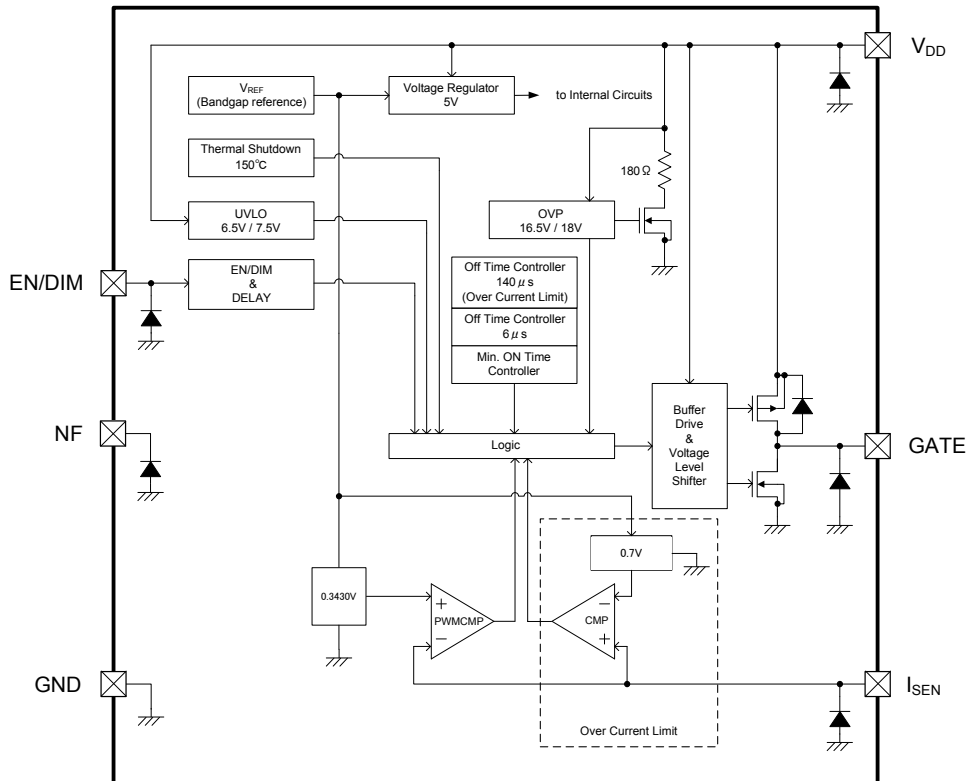
# XC9401 Series

## ■ BLOCK DIAGRAM

### 1) XC9401 Series, Type A



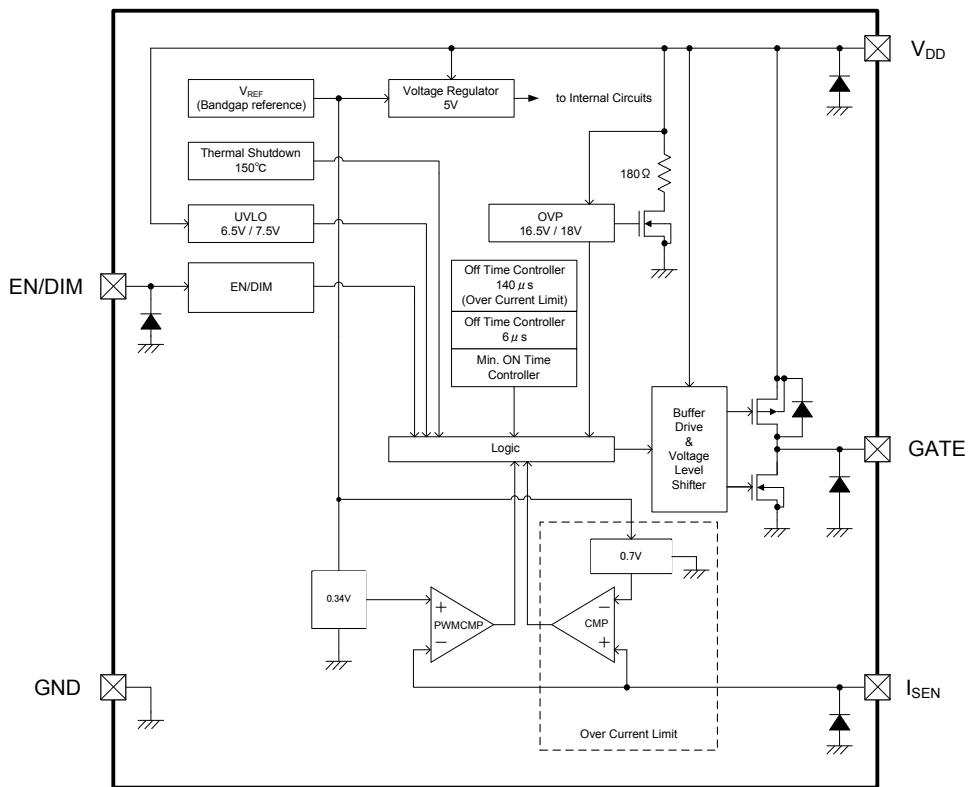
### 2) XC9401 Series, Type B



\* Diodes inside the circuit are an ESD protection diode and a parasitic diode.

**■ BLOCK DIAGRAM**

3) XC9401 Series, Type C



\* Diodes inside the circuit are an ESD protection diode and a parasitic diode.

## PRODUCT CLASSIFICATION

### Ordering Information

XC9401①②③④⑤⑥-⑦<sup>(\*)</sup>

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
①	Type	A	Refer to Selection Guide
		B	
		C	
②	OFF Time	6	OFF Time is fixed in 6μs
③④	Accuracy	05	Type A, Type B : I <sub>SEN</sub> Voltage Accuracy is ±5%
		0C	Type C : I <sub>SEN</sub> Voltage Accuracy is ±2.5%
⑤⑥-⑦	Packages (Order Unit)	MR-G	SOT-26 (Type A, Type B) (3,000pcs/Reel)
		SR-G	SOT-25 (Type C) (3,000pcs /Reel)

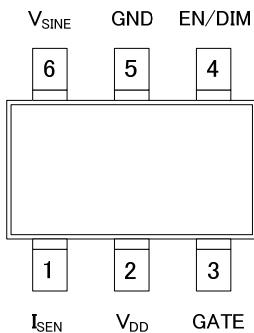
(\*) The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

### Selection Guide

TYPE	COMPARISON WITH I <sub>SEN</sub>	PWM DIMMING	DESCRIPTION
A	"V <sub>SINE</sub> " × 0.2783	Yes	Type A is suitable for PFC circuit.
B	0.3430V	Yes	Type B is suitable for constant LED current circuit.
C	0.3400V	Yes	Type C is suitable for constant LED current circuit and for PWM DIMMING control.

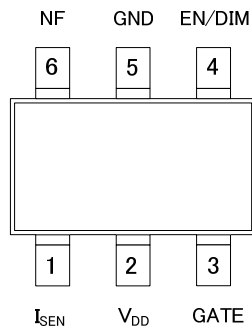
## PIN CONFIGURATION

\*Type A



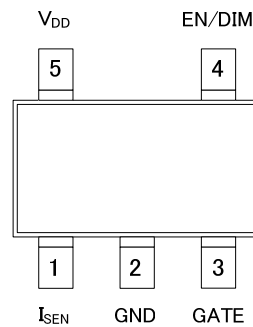
SOT-26  
(TOP VIEW)

\*Type B



SOT-26  
(TOP VIEW)

\*Type C



SOT-25  
(TOP VIEW)

## PIN ASSIGNMENT

PIN NUMBER		PIN NAME	FUNCTIONS
SOT-26	SOT-25		
1	1	I <sub>SEN</sub>	Current Feedback
2	5	V <sub>DD</sub>	Power Input
3	3	GATE	External Power MOSFET Drive
4	4	EN/DIM	ON/OFF / PWM Dimming
5	2	GND	Ground
6	-	Type A : V <sub>SINE</sub>	V <sub>SINE</sub> Pin : Current Feedback Reference Voltage Input.
		Type B : NF	NF Pin : No Function. Please connect to GND.

## ■ FUNCTION

PIN NAME	EN/DIM	STATUS
EN/DIM	L	Stand-by Mode
	H	Active Mode
	OPEN	Undefined State <sup>(*)</sup>

<sup>(\*)</sup> Prohibited in the XC9401 series due to undefined operation.

## ■ ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMETER	SYMBOL	RATINGS	UNITS	
V <sub>DD</sub> Pin Voltage	V <sub>DD</sub>	-0.3 ~ +19.4	V	
EN/DIM Pin Voltage	V <sub>EN/DIM</sub>	-0.3 ~ +19.4	V	
GATE Pin Voltage	V <sub>GATE</sub>	-0.3 ~ V <sub>DD</sub> +0.3 or +19.4 <sup>(*)</sup>	V	
I <sub>SEN</sub> Pin Voltage	V <sub>ISEN</sub>	-0.3 ~ 5.5	V	
V <sub>SINE</sub> Pin Voltage	V <sub>SINE</sub>	-0.3 ~ 5.5	V	
NF Pin Voltage	V <sub>NF</sub>			
Power Dissipation	SOT-26	P <sub>d</sub>	250	mW
			600 (PCB mounted) <sup>(*)</sup>	
	SOT-25		250	
			600 (PCB mounted) <sup>(*)</sup>	
Operating Ambient Temperature	T <sub>opr</sub>	-40 ~ +85	°C	
Storage Temperature	T <sub>stg</sub>	-55 ~ +125	°C	

<sup>(\*)</sup> All voltages are described based on GND.

<sup>(\*)</sup> The maximum value should be either V<sub>DD</sub>+0.3 or +19.4V in the lowest.

<sup>(\*)</sup> The power dissipation figure shown is PCB mounted. Please refer to pages 21 ~ 22 for details.

## ELECTRICAL CHARACTERISTICS

XC9401 Series, Type A

Ta=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
V <sub>DD</sub> Voltage Range	V <sub>DD</sub>		9	-	15	V	①
I <sub>SEN</sub> Voltage	V <sub>ISEN</sub>	V <sub>ISEN</sub> =SWEEP <sup>(*)</sup> , V <sub>SINE</sub> =1V	0.2644	0.2783	0.2922	V	①
V <sub>SINE</sub> Voltage Range <sup>(**)</sup>	V <sub>SINE</sub>		GND	-	1.8	V	①
UVLO Detect Voltage	V <sub>UVLO</sub>	V <sub>DD</sub> =SWEEP <sup>(***)</sup>	5.5	6.5	7.5	V	①
UVLO Release Voltage	V <sub>UVLOR</sub>	V <sub>DD</sub> =SWEEP <sup>(****)</sup>	6.5	7.5	8.5	V	①
UVLO Hysteresis Width	V <sub>UVLOH</sub>	V <sub>UVLOH</sub> =V <sub>UVLOR</sub> - V <sub>UVLO</sub>	-	1.1	-	V	①
Supply Current <sup>(***)</sup>	I <sub>DD</sub>	V <sub>DD</sub> =V <sub>ENDIM</sub> =15V	-	250	300	μA	①
Stand-by Current <sup>(****)</sup>	I <sub>STB</sub>	V <sub>DD</sub> =15V, V <sub>ENDIM</sub> =GND, V <sub>SINE</sub> =GND	-	225	280	μA	①
V <sub>DD</sub> Overvoltage Protection Voltage	V <sub>OVPR</sub>	V <sub>DD</sub> =SWEEP <sup>(***)</sup> , V <sub>ENDIM</sub> =V <sub>DD</sub>	17	18	19	V	①
V <sub>DD</sub> Overvoltage Protection Release Voltage	V <sub>OVPR</sub>	V <sub>DD</sub> =SWEEP <sup>(****)</sup> , V <sub>ENDIM</sub> =V <sub>DD</sub>	15.5	16.5	17.5	V	①
V <sub>DD</sub> Overvoltage Protection Hysteresis Width	V <sub>OVPH</sub>	V <sub>OVPH</sub> =V <sub>OVPR</sub> - V <sub>OVPR</sub>	-	1.5	-	V	①
V <sub>DD</sub> Overvoltage Protection Discharge Current <sup>(****)</sup>	I <sub>OVPR</sub>	V <sub>DD</sub> =19V	-	30	-	mA	①
GATE "H" ON Resistance	R <sub>GATEH</sub>	I <sub>GATE</sub> = -10mA R <sub>GATEH</sub> = (V <sub>DD</sub> -V <sub>GATE</sub> ) / I <sub>GATE</sub>	2	5	8	Ω	③
GATE "L" ON Resistance	R <sub>GATEL</sub>	V <sub>ISEN</sub> =1V, R <sub>1</sub> =300Ω R <sub>GATEL</sub> = V <sub>GATE</sub> / I <sub>R1</sub> <sup>(***)</sup>	-	5	-	Ω	④
OFF Time <sup>(***)</sup>	t <sub>OFF</sub>	V <sub>ISEN</sub> =0.4V, V <sub>SINE</sub> =1V	-	6	-	μs	①
Minimum ON Time	t <sub>ONMIN</sub>	V <sub>ISEN</sub> =1V	-	0.2	-	μs	①
Current Limit Voltage <sup>(****)</sup>	V <sub>LIM</sub>	V <sub>ISEN</sub> =SWEEP, V <sub>SINE</sub> =1.4V	0.65	0.70	0.95	V	①
Thermal Shutdown Temperature <sup>(****)</sup>	T <sub>TSD</sub>		-	150	-	°C	①
Thermal Shutdown Release Temperature	T <sub>TSDR</sub>		-	130	-	°C	①
Thermal Shutdown Hysteresis Width	T <sub>HYS</sub>		-	20	-	°C	①
PWM Dimmer Delay Time1 <sup>(****)</sup>	t <sub>PWMDIM1</sub>	V <sub>ENDIM</sub> =2.2V to GND	-	0.3	4.0	μs	②
PWM Dimmer Delay Time2 <sup>(****)</sup>	t <sub>PWMDIM2</sub>	V <sub>ENDIM</sub> =GND to 2.2V	100	140	200	μs	②
EN/DIM "H" Voltage	V <sub>ENDIMH</sub>		2.2	-	15.0	V	①
EN/DIM "L" Voltage	V <sub>ENDIML</sub>		GND	-	0.4	V	①
EN/DIM Bias Current	I <sub>ENDIMH</sub>	V <sub>ENDIM</sub> =15V	-	-	32	μA	①

Unless otherwise stated, GND standard, V<sub>DD</sub>=13V, V<sub>ENDIM</sub>=V<sub>DD</sub>, V<sub>ISEN</sub>=GND, V<sub>SINE</sub>=5.5V

(\*) I<sub>SEN</sub> pin voltage measured at start of GATE pin switching.

(\*\*) Indicates V<sub>SINE</sub> pin voltage at which OFF TIME 6μs switching becomes possible.

(\*\*\*) V<sub>DD</sub> pin voltage measured when GATE pin=L occurs.

(\*\*\*\*) V<sub>DD</sub> pin voltage measured when GATE pin=H occurs.

(\*) Indicates internal supply current when "H" level is input into EN/DIM pin and all circuits are activated. (When not switching.)

(\*\*) Indicates internal supply current when "L" level is input into EN/DIM pin and the switching circuit is stopped.

(\*) Indicates the current that discharges the capacitance between the V<sub>DD</sub> and GND pins at V<sub>OVPR</sub>.

(\*) Please refer to P.7 "CIRCUIT④".

(\*) May not be fixed at 6μs when UVLO is detected or during DIM signal control.

(\*) When the current limit voltage V<sub>LIM</sub> is exceeded, off time is extended to about 140μs to prevent element damage.

For details, refer to the operation description.

(\*) To protect the IC from thermal destruction, thermal shutdown activates when the chip temperature reaches 150°C and forcibly sets the GATE pin voltage to "L". When the chip temperature falls to 130°C, operation resumes.

(\*) Time from attainment of EN/DIM "L" voltage until GATE pin=L.

(\*) Time from attainment of EN/DIM "H" voltage until GATE pin=H.

## ■ ELECTRICAL CHARACTERISTICS (Continued)

XC9401 Series, Type B

Ta=25 °C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
V <sub>DD</sub> Voltage Range	V <sub>DD</sub>		9	-	15	V	①
I <sub>SEN</sub> Voltage	V <sub>ISEN</sub>	V <sub>ISEN</sub> =SWEEP <sup>(1)</sup>	0.3259	0.3430	0.3602	V	①
UVLO Detect Voltage	V <sub>UVLO</sub>	V <sub>DD</sub> =SWEEP <sup>(2)</sup>	5.5	6.5	7.5	V	①
UVLO Release Voltage	V <sub>UVLOR</sub>	V <sub>DD</sub> =SWEEP <sup>(3)</sup>	6.5	7.5	8.5	V	①
UVLO Hysteresis Width	V <sub>UVLOH</sub>	V <sub>UVLOH</sub> =V <sub>UVLOR</sub> - V <sub>UVLO</sub>	-	1.1	-	V	①
Supply Current <sup>(4)</sup>	I <sub>DD</sub>	V <sub>DD</sub> =V <sub>EN/DIM</sub> =15V	-	250	300	μA	①
Stand-by Current <sup>(5)</sup>	I <sub>STB</sub>	V <sub>DD</sub> =15V, V <sub>EN/DIM</sub> =GND	-	225	280	μA	①
V <sub>DD</sub> Overvoltage Protection Voltage	V <sub>OVV</sub>	V <sub>DD</sub> =SWEEP <sup>(2)</sup>	17	18	19	V	①
V <sub>DD</sub> Overvoltage Protection Release Voltage	V <sub>OVPR</sub>	V <sub>DD</sub> =SWEEP <sup>(3)</sup>	15.5	16.5	17.5	V	①
V <sub>DD</sub> Overvoltage Protection Hysteresis Width	V <sub>OVPH</sub>	V <sub>OVPH</sub> =V <sub>OVV</sub> - V <sub>OVPR</sub>	-	1.5	-	V	①
V <sub>DD</sub> Overvoltage Protection Discharge Current <sup>(6)</sup>	I <sub>OVV</sub>	V <sub>DD</sub> =19V	-	30	-	mA	①
GATE "H" ON Resistance	R <sub>GATEH</sub>	I <sub>GATE</sub> = -10mA R <sub>GATEH</sub> = (V <sub>DD</sub> - V <sub>GATE</sub> ) / I <sub>GATE</sub>	2	5	8	Ω	③
GATE "L" ON Resistance	R <sub>GATEL</sub>	V <sub>ISEN</sub> =1V, R <sub>1</sub> =300Ω R <sub>GATEL</sub> = V <sub>GATE</sub> / I <sub>R1</sub> <sup>(7)</sup>	-	5	-	Ω	④
OFF Time <sup>(8)</sup>	t <sub>OFF</sub>	V <sub>ISEN</sub> =0.45V	-	6	-	μs	①
Minimum ON Time	t <sub>ONMIN</sub>	V <sub>ISEN</sub> =1V	-	0.2	-	μs	①
Current Limit Voltage <sup>(9)</sup>	V <sub>LIM</sub>	V <sub>ISEN</sub> =SWEEP	0.65	0.70	0.95	V	①
Thermal Shutdown Temperature <sup>(10)</sup>	T <sub>TSD</sub>		-	150	-	°C	①
Thermal Shutdown Release Temperature	T <sub>TSDR</sub>		-	130	-	°C	①
Thermal Shutdown Hysteresis Width	T <sub>HYS</sub>		-	20	-	°C	①
PWM Dimmer Delay Time1 <sup>(11)</sup>	t <sub>PWMDIM1</sub>	V <sub>EN/DIM</sub> =2.2V to GND	-	0.3	4.0	μs	①
PWM Dimmer Delay Time2 <sup>(12)</sup>	t <sub>PWMDIM2</sub>	V <sub>EN/DIM</sub> =GND to 2.2V	100	140	200	μs	②
EN/DIM "H" Voltage	V <sub>EN/DIMH</sub>		2.2	-	15.0	V	②
EN/DIM "L" Voltage	V <sub>EN/DIML</sub>		GND	-	0.4	V	①
EN/DIM Bias Current	I <sub>EN/DIMH</sub>	V <sub>EN/DIM</sub> =15V	-	-	32	μA	①

Unless otherwise stated, GND standard, V<sub>DD</sub>=13V, V<sub>EN/DIM</sub>=V<sub>DD</sub>, V<sub>ISEN</sub>=GND, V<sub>NF</sub>=GND

<sup>(1)</sup> I<sub>SEN</sub> pin voltage measured at start of GATE pin switching.

<sup>(2)</sup> V<sub>DD</sub> pin voltage measured when GATE pin=L occurs.

<sup>(3)</sup> V<sub>DD</sub> pin voltage measured when GATE pin=H occurs.

<sup>(4)</sup> Indicates internal supply current when "H" level is input into EN/DIM pin and all circuits are activated. (When not switching.)

<sup>(5)</sup> Indicates internal supply current when "L" level is input into EN/DIM pin and the switching circuit is stopped.

<sup>(6)</sup> Indicates the current that discharges the capacitance between the V<sub>DD</sub> and GND pins at V<sub>OVV</sub>.

<sup>(7)</sup> Please refer to "CIRCUIT④".

<sup>(8)</sup> May not be fixed at 6 μs when UVLO is detected or during DIM signal control.

<sup>(9)</sup> When the current limit voltage V<sub>LIM</sub> is exceeded, off time is extended to about 140 μs to prevent element damage.

For details, refer to the operation description.

<sup>(10)</sup> To protect the IC from thermal destruction, thermal shutdown activates when the chip temperature reaches 150 °C and forcibly sets the GATE pin voltage to "L". When the chip temperature falls to 130 °C, operation resumes.

<sup>(11)</sup> Time from attainment of EN/DIM "L" voltage until GATE pin=L.

<sup>(12)</sup> Time from attainment of EN/DIM "H" voltage until GATE pin=H.

## ELECTRICAL CHARACTERISTICS (Continued)

XC9401 Series, Type C

Ta=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
V <sub>DD</sub> Voltage Range	V <sub>DD</sub>		9	-	15	V	①
I <sub>SEN</sub> Voltage	V <sub>ISEN</sub>	V <sub>ISEN</sub> =SWEEP <sup>(1)</sup>	0.3315	0.3400	0.3485	V	①
UVLO Detect Voltage	V <sub>UVLO</sub>	V <sub>DD</sub> =SWEEP <sup>(2)</sup>	5.5	6.5	7.5	V	①
UVLO Release Voltage	V <sub>UVLOR</sub>	V <sub>DD</sub> =SWEEP <sup>(3)</sup>	6.5	7.5	8.5	V	①
UVLO Hysteresis Width	V <sub>UVLOH</sub>	V <sub>UVLOH</sub> =V <sub>UVLOR</sub> - V <sub>UVLO</sub>	-	1.1	-	V	①
Supply Current <sup>(4)</sup>	I <sub>DD</sub>	V <sub>DD</sub> =V <sub>EN/DIM</sub> =15V	-	250	300	μA	①
Stand-by Current <sup>(5)</sup>	I <sub>STB</sub>	V <sub>DD</sub> =15V, V <sub>EN/DIM</sub> =GND	-	225	280	μA	①
V <sub>DD</sub> Overvoltage Protection Voltage	V <sub>OVP</sub>	V <sub>DD</sub> =SWEEP <sup>(2)</sup>	17	18	19	V	①
V <sub>DD</sub> Overvoltage Protection Release Voltage	V <sub>OVPR</sub>	V <sub>DD</sub> =SWEEP <sup>(3)</sup>	15.5	16.5	17.5	V	①
V <sub>DD</sub> Overvoltage Protection Hysteresis Width	V <sub>OVPH</sub>	V <sub>OVPH</sub> =V <sub>OVP</sub> - V <sub>OVPR</sub>	-	1.5	-	V	①
V <sub>DD</sub> Overvoltage Protection Discharge Current <sup>(6)</sup>	I <sub>OVP</sub>	V <sub>DD</sub> =19V	-	30	-	mA	①
GATE "H" ON Resistance	R <sub>GATEH</sub>	I <sub>GATE</sub> = -10mA R <sub>GATEH</sub> = (V <sub>DD</sub> - V <sub>GATE</sub> ) / I <sub>GATE</sub>	2	5	8	Ω	③
GATE "L" ON Resistance	R <sub>GATEL</sub>	V <sub>ISEN</sub> =1V, R <sub>1</sub> =300Ω R <sub>GATEL</sub> = V <sub>GATE</sub> / I <sub>R1</sub> <sup>(7)</sup>	-	5	-	Ω	④
OFF Time <sup>(8)</sup>	t <sub>OFF</sub>	V <sub>ISEN</sub> =0.45V	-	6	-	μs	①
Minimum ON Time	t <sub>ONMIN</sub>	V <sub>ISEN</sub> =1V	-	0.2	-	μs	①
Current Limit Voltage <sup>(9)</sup>	V <sub>LIM</sub>	V <sub>ISEN</sub> =SWEEP	0.65	0.70	0.95	V	①
Thermal Shutdown Temperature <sup>(10)</sup>	T <sub>TSD</sub>		-	150	-	°C	①
Thermal Shutdown Release Temperature	T <sub>TSDR</sub>		-	130	-	°C	①
Thermal Shutdown Hysteresis Width	T <sub>HYS</sub>		-	20	-	°C	①
PWM Dimmer Delay Time1 <sup>(11)</sup>	t <sub>PWMDIM1</sub>	V <sub>EN/DIM</sub> =2.2V to GND	-	0.3	4.0	μs	①
PWM Dimmer Delay Time2 <sup>(12)</sup>	t <sub>PWMDIM2</sub>	V <sub>EN/DIM</sub> =GND to 2.2V, V <sub>ISEN</sub> =0.45V	-	6.0	10.0	μs	②
EN/DIM "H" Voltage	V <sub>EN/DIMH</sub>		2.2	-	15.0	V	②
EN/DIM "L" Voltage	V <sub>EN/DIML</sub>		GND	-	0.4	V	①
EN/DIM Bias Current	I <sub>EN/DIMH</sub>	V <sub>EN/DIM</sub> =15V	-	-	32	μA	①

Unless otherwise stated, GND standard, V<sub>DD</sub>=13V, V<sub>EN/DIM</sub>=V<sub>DD</sub>, V<sub>ISEN</sub>=GND

<sup>(1)</sup> I<sub>SEN</sub> pin voltage measured at start of GATE pin switching.

<sup>(2)</sup> V<sub>DD</sub> pin voltage measured when GATE pin=L occurs.

<sup>(3)</sup> V<sub>DD</sub> pin voltage measured when GATE pin=H occurs.

<sup>(4)</sup> Indicates internal supply current when "H" level is input into EN/DIM pin and all circuits are activated. (When not switching.)

<sup>(5)</sup> Indicates internal supply current when "L" level is input into EN/DIM pin and the switching circuit is stopped.

<sup>(6)</sup> Indicates the current that discharges the capacitance between the V<sub>DD</sub> and GND pins at V<sub>OVP</sub>.

<sup>(7)</sup> Please refer to "CIRCUIT ④".

<sup>(8)</sup> May not be fixed at 6μs when UVLO is detected or during DIM signal control.

<sup>(9)</sup> When the current limit voltage V<sub>LIM</sub> is exceeded, off time is extended to about 140μs to prevent element damage.

For details, refer to the operation description.

<sup>(10)</sup> To protect the IC from thermal destruction, thermal shutdown activates when the chip temperature reaches 150°C and forcibly sets the GATE pin voltage to "L". When the chip temperature falls to 130°C, operation resumes.

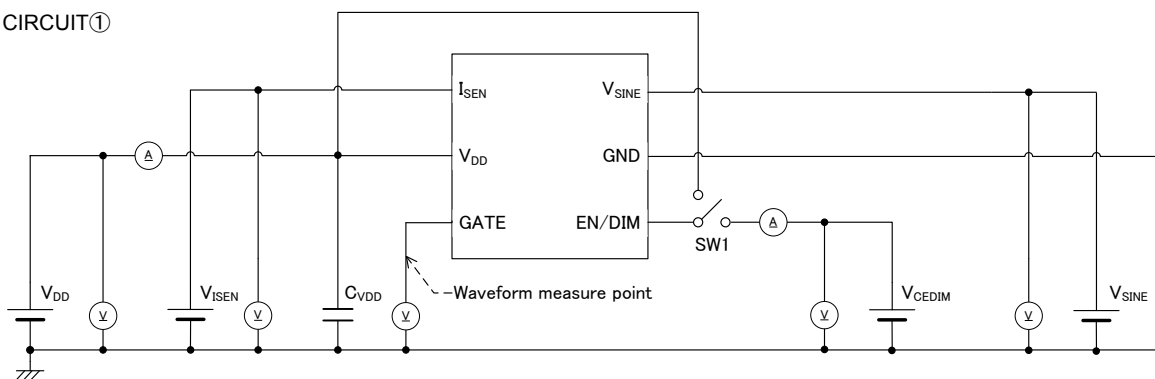
<sup>(11)</sup> Time from attainment of EN/DIM "L" voltage until GATE pin=L.

<sup>(12)</sup> Time from attainment of EN/DIM "H" voltage until GATE pin=H.

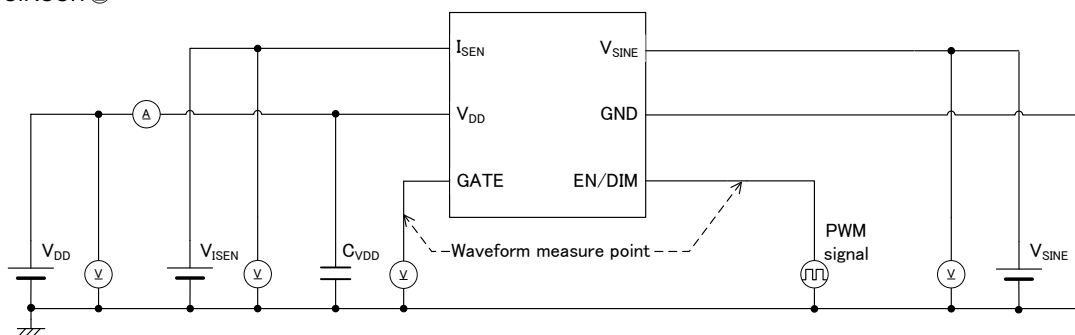


## TYPICAL APPLICATION CIRCUIT (Type A)

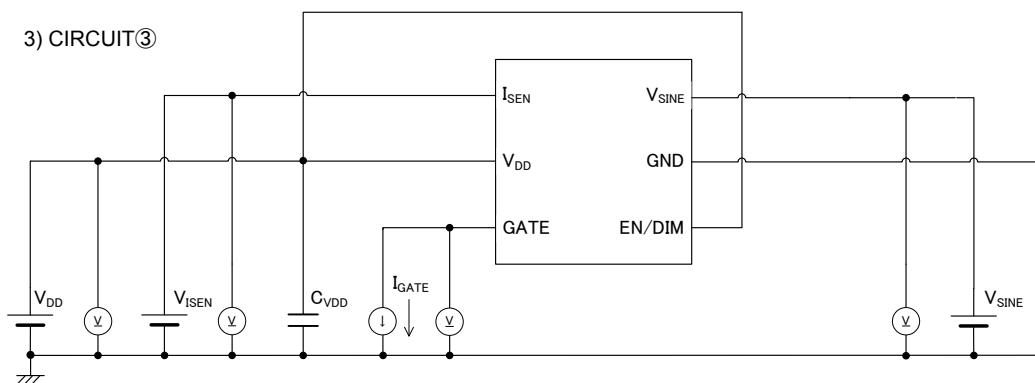
1) CIRCUIT①



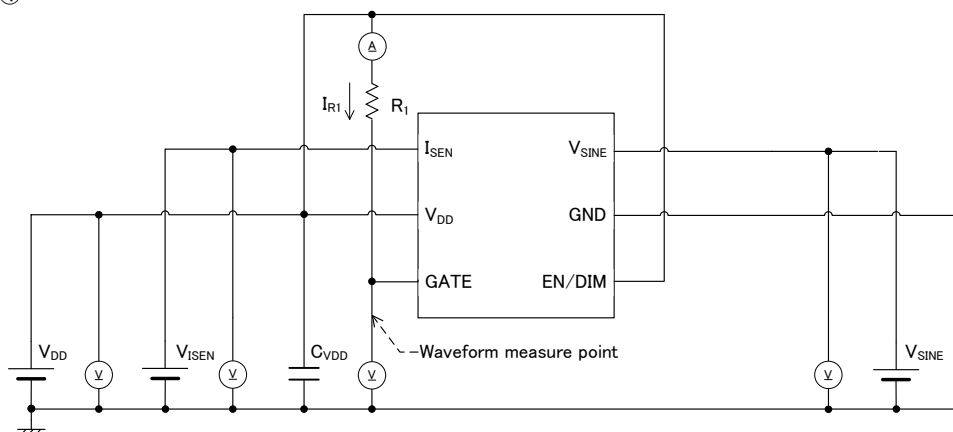
2) CIRCUIT②



3) CIRCUIT③

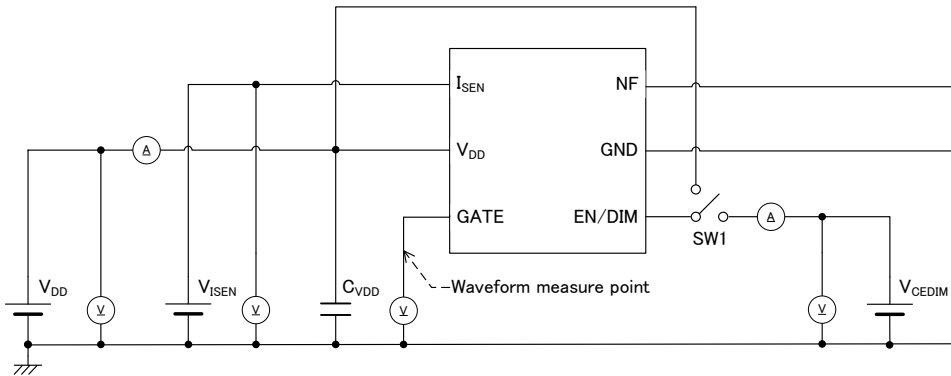


4) CIRCUIT④

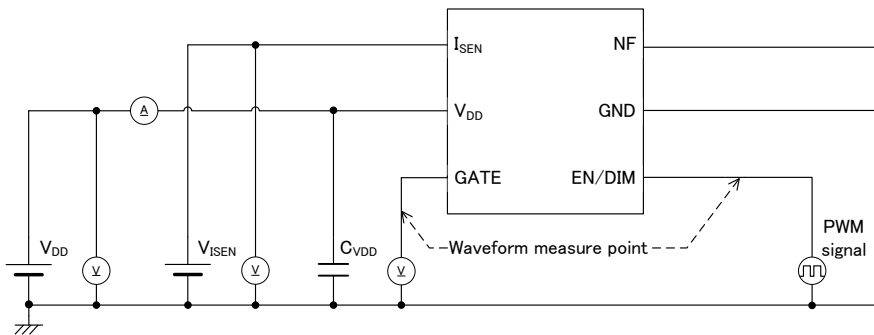


## TYPICAL APPLICATION CIRCUIT (Continued) (Type B)

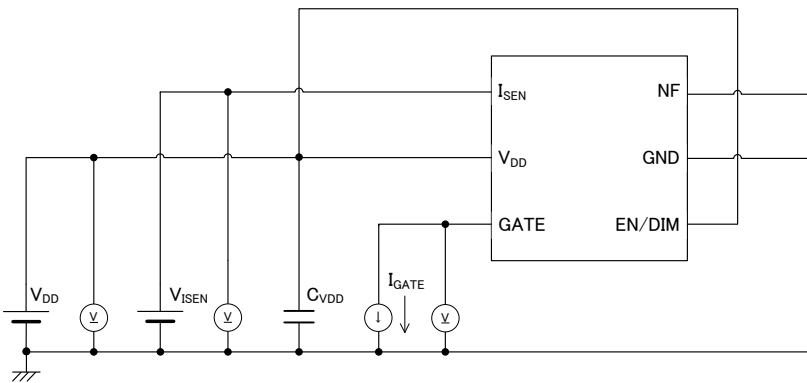
1) CIRCUIT①



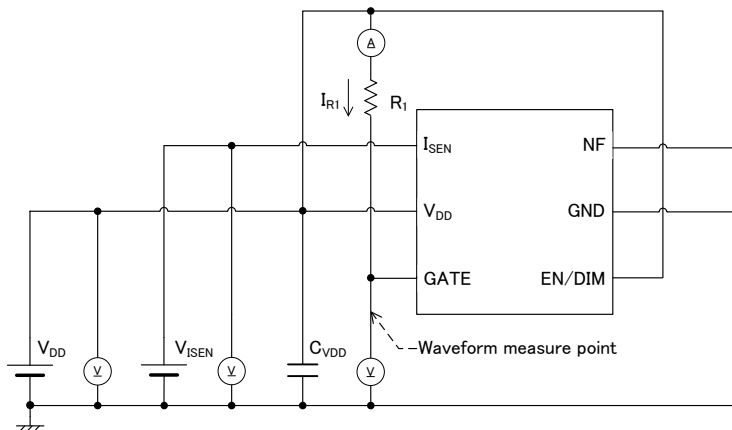
2) CIRCUIT②



3) CIRCUIT③

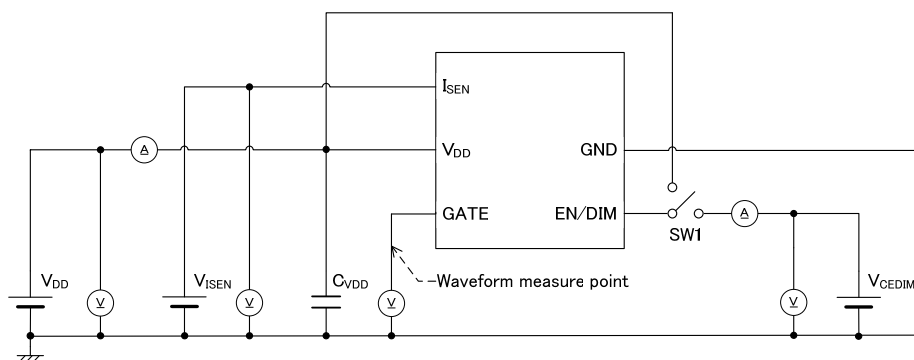


4) CIRCUIT④

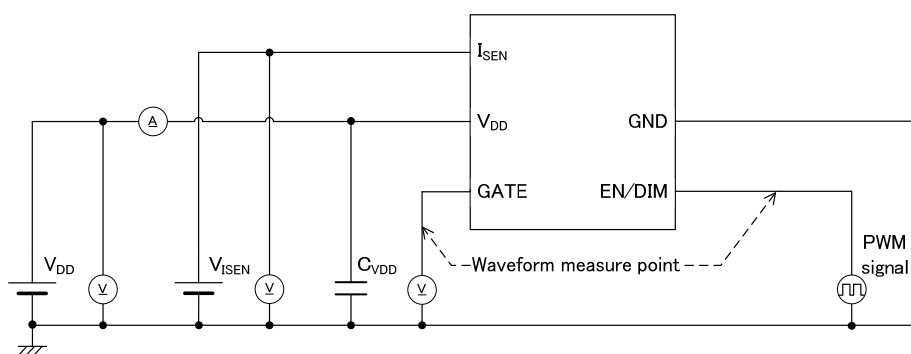


## TYPICAL APPLICATION CIRCUIT (Continued) (Type C)

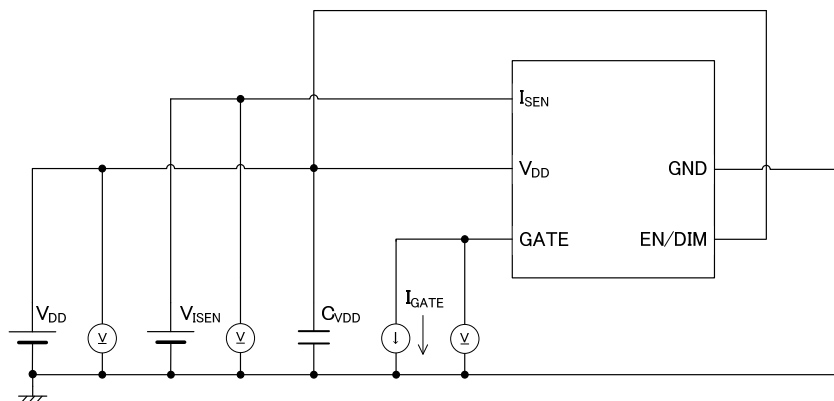
1) CIRCUIT①



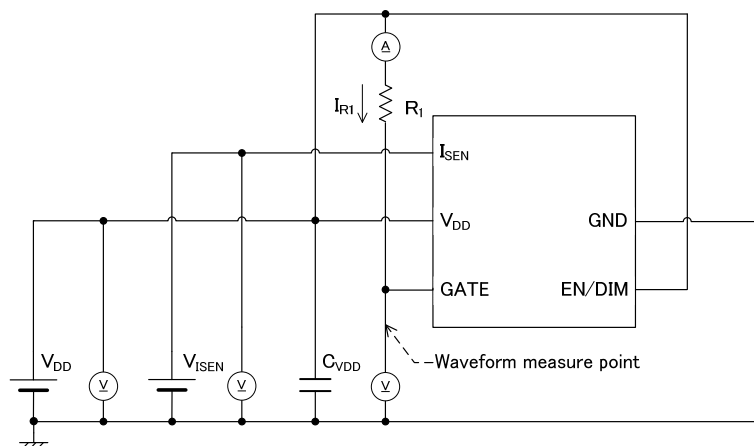
2) CIRCUIT②



3) CIRCUIT③



4) CIRCUIT④



## OPERATIONAL EXPLANATION

The internal circuitry of the XC9401 series consists of a reference voltage source ( $V_{REF}$ ), PWM comparator (PWWCMP), buffer drive circuit (Buffer Drive), over-current protection circuit (Over Current Limit), under-voltage lockout circuit (UVLO),  $V_{DD}$  over voltage protection circuit (OVP), thermal shutdown circuit (Thermal Shutdown), and other circuits. (Refer to the block diagram.) The control method is fixed off-time control.

With type A, the PWM comparator compares the voltage at the  $I_{SEN}$  pin to that at the  $V_{SINE}$  pin  $\times 0.2783$  (TYP.) The output of the PWM comparator is connected to the buffer drive circuit and an external Power MOS FET drive signal is output from the GATE pin. When the  $I_{SEN}$  pin voltage is 0.2783 times (TYP.) higher than the  $V_{SINE}$  pin voltage, the GATE pin switches to low. After a fixed off-time elapses, the GATE pin switches to high. This operation is repeated continuously.

With type B, the PWM comparator compares the 0.3430V (TYP.), which is 0.2783 times (TYP.) the reference voltage, to the  $I_{SEN}$  pin voltage.

With type C compares the 0.3400V to the  $I_{SEN}$  pin voltage.

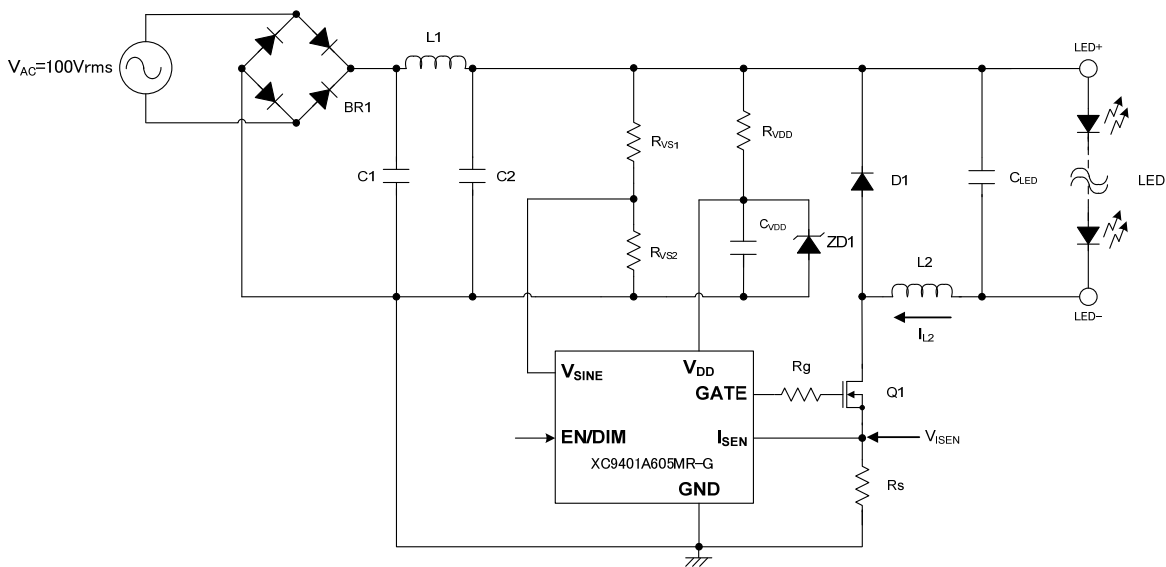


Fig.1. Reference Circuit (XC9401A605MR-G)

## ■ OPERATIONAL EXPLANATION (Continued)

Details of each circuit block are as follows.

### <Reference voltage supply ( $V_{REF}$ )>

Reference voltage that enables stable operation of the internal functions of the IC.

### <PWM comparator (PWMCMP)>

With type A, the PWM comparator compares the voltage at the  $I_{SEN}$  pin to that at the  $V_{SINE}$  pin  $\times 0.2783$  (TYP.) When the  $I_{SEN}$  pin voltage is higher than  $V_{SINE} \times 0.2783$ , the GATE pin switches to low.

With type B, the PWM comparator compares the 0.3430V (TYP.), which is 0.2783 times (TYP.) the reference voltage, to the  $I_{SEN}$  pin voltage.

With type C compares the 0.3400V to the  $I_{SEN}$  pin voltage.

### <Buffer drive circuit (Buffer Drive)>

This outputs an H or L signal from the GATE pin that drives the external Power MOS FET. The H level is the  $V_{DD}$  pin voltage, and the L level is the GND pin voltage. The signal that is output is determined by the PWM comparator, as well as by the below described UVLO circuit,  $V_{DD}$  over-voltage protection circuit, over-current protection circuit, thermal shutdown, and EN/DIM pin voltage.

### <Enable / PWM Dimming (EN/DIM)>

Inputting "L" to the EN/DIM pin voltage forcibly puts the GATE pin voltage in the "L" state. The stopped state when the EN/DIM pin voltage is "L" is not shutdown; rather, it is a Stand-by state wherein the switching pulse output is stopped by logic circuit and the internal circuitry continues to operate.

With type A and type B when the power is turned on, it takes about 200 $\mu$ s (MAX.) for the GATE pin to initially become "H" after the EN/DIM pin voltage is changed from "L" to "H".

The C type can do the dimming control between 1% and 100% more precisely by adjusting the duty width and following at high speed to the PWM signal ( 500Hz to 1kHz) because it is as fast as 10 $\mu$ sec that Gate terminal is tuned into "H" state at the beginning after changing EN/DIM terminal voltage into "H" from "L".

### < Minimum on time controller circuit >

Noise occurs in the XC9401 series due to switching. To prevent malfunction of the internal circuit by such noises, a minimum on time is established. The GATE pin voltage is forcibly kept at "H" until the minimum on time elapses. (Refer to Fig. 2.)

During the minimum on time, if the below described UVLO, OVP or thermal shutdown is detected, or if the Stand-by state is set from the EN/DIM pin, the GATE pin voltage is immediately changed to "L".

### <Off Time Controller>

This circuit controls the fixed off time. The off time is normally fixed at 6 $\mu$ s (TYP.), and the GATE pin voltage is kept at "L" during this time. After the fixed off time, the GATE pin voltage becomes "H". (Refer to Fig. 2.)

If the EN/DIM pin voltage is changed from "L" to "H" during the above PWM dimming, the off time is 140 $\mu$ s (TYP.) during the over-current protection and ULVO release described below.

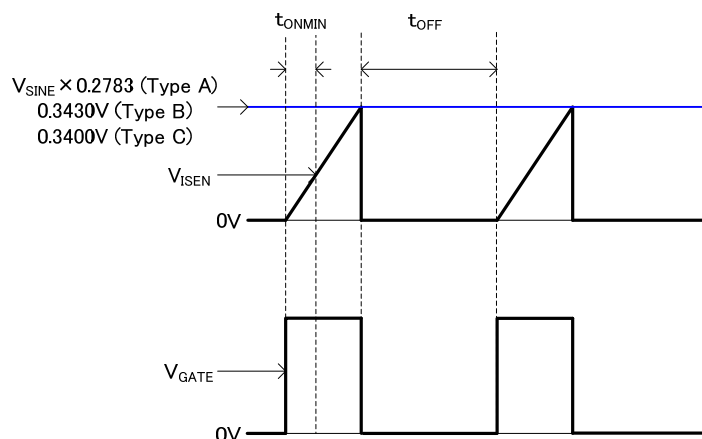


Fig.2. Off Time Controller, Min. On Time Controller

## OPERATIONAL EXPLANATION (Continued)

### <Over Current Limit>

When the switching current of the external power MOSFET is in the over-current state and the I<sub>SEN</sub> pin voltage reaches 0.7V (TYP.), L level voltage is output to the GATE pin and the external power MOSFET is turned off. In addition, the off time is temporarily extended from the normal 6.0μs (TYP.) to 140μs (TYP.). When the I<sub>SEN</sub> pin voltage falls to 0.7V (TYP.) or less after the extended off time, normal operation resumes.

When LED+ and LED- short circuit in the reference circuit shown in Fig.1, the current slope of the coil (L2) becomes smaller during the off time than the slope during normal switching, which prevents sufficient discharge during the 6.0μs (TYP.) off time. During the minimum on time, the external power MOSFET Q1 always turns on, and thus the coil current gradually increases. The I<sub>SEN</sub> pin voltage becomes higher at the same time as the coil current increases, and when the I<sub>SEN</sub> pin voltage reaches 0.7V (TYP.), the off time is extended to 140μs (TYP.). (Refer to Fig.3)

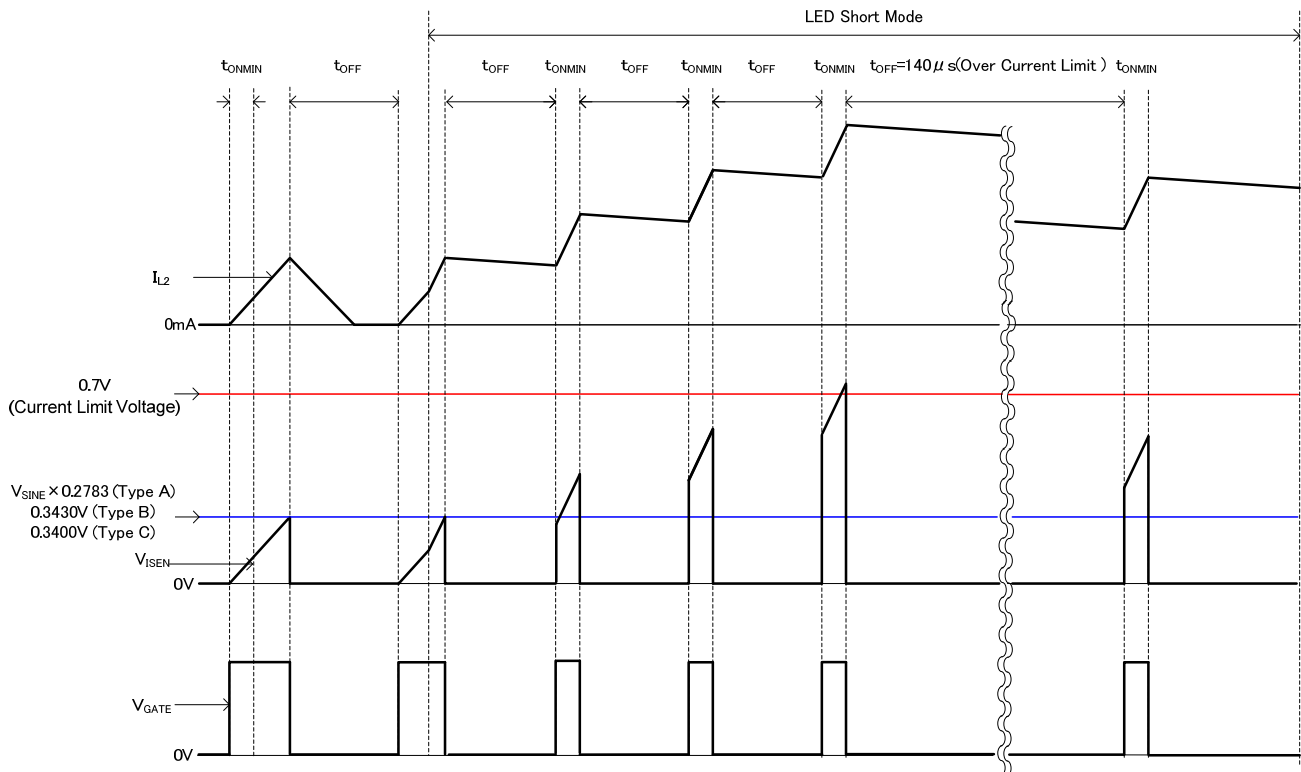


Fig.3. Over Current Limit (Fig.1. Operation when LED+ and LED- short-circuit in the reference circuit)

### <Under-voltage lockout circuit>

When the V<sub>DD</sub> pin voltage falls to the UVLO Detect Voltage (V<sub>UVLO</sub>) or lower, the GATE pin voltage is forcibly set to “L” to prevent incorrect pulse output. When the V<sub>DD</sub> pin voltage rises to the UVLO Release Voltage (V<sub>UVLOR</sub>) or higher, switching resumes.

A UVLO stop simply stops pulse output; it is not a shutdown state and the internal circuitry continues to operate.

### <V<sub>DD</sub> over-voltage protection circuit>

When the V<sub>DD</sub> pin voltage rises to the V<sub>DD</sub> Overvoltage Protection Voltage (V<sub>OVP</sub>) or higher, the charge of the capacitance between the V<sub>DD</sub> pin and GND pin is discharged by the resistance and transistor connected between the V<sub>DD</sub> pin and GND pin in order to prevent withstand voltage destruction in the internal circuitry. The GATE pin voltage at this time is forcibly set to “L”.

When the V<sub>DD</sub> pin voltage falls to the V<sub>DD</sub> Overvoltage Protection Release Voltage (V<sub>OVPR</sub>) or lower, switching is resumed.

### <Thermal shutdown>

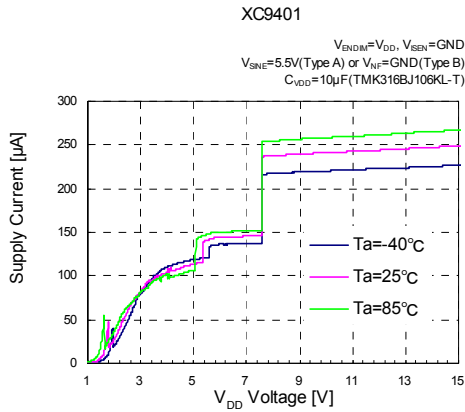
To protect the IC from thermal destruction, thermal shutdown activates when the chip temperature reaches 150 °C (TYP.) and forcibly sets the GATE pin voltage to “L”. When the chip temperature falls to 130 °C (TYP.), switching is resumed.

## ■ NOTE ON USE

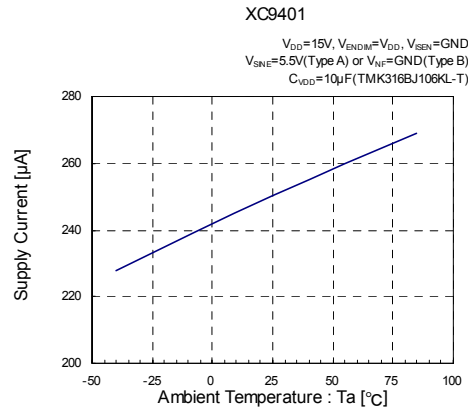
1. For the phenomenon of temporal and transitional voltage decrease or voltage increase, the IC may be damaged or deteriorated if IC is used beyond the absolute MAX. specifications.
2. In a switching controller such as the XC9401 series, and in a peripheral circuit controlled by a switching controller, spike voltage and ripple voltage occur. These are greatly affected by the peripheral components (inductance value of the coil, capacitors, peripheral component board layout).  
Please select the appropriate external components with referring to the XC9401 series application note. During design, test sufficiently using the actual equipment.
3. With type A and type B, a delay time of 200 $\mu$ s (MAX.) after the UVLO release voltage, and after EN/DIM pin voltage "H", has been established in the IC.  
With type C a delay time of 140 $\mu$ s (TYP.) after the UVLO release voltage, and a delay time of 10 $\mu$ s after EN/DIM pin voltage "H", has been established in the IC. Keep these delay times in mind during sequence design.
4. The NF pin of the XC9401B605MR-G is connected to part of the internal circuitry, although not as a circuit function. When using this IC, connect this pin to GND.
5. Make sure to use this IC within specified electric characteristics.
6. Please pay attention not to exceed absolute maximum ratings of this IC and external components.
7. To reduce  $V_{DD}$  fluctuations as much as possible, connect a bypass capacitor ( $C_{VDD}$ ) over the shortest path between  $V_{DD}$  and GND. If there is too much distance between the IC and  $C_{VDD}$ , operation may become unstable.
8. Please mount each external component as close to the IC as possible. Please also wire external components as close to the IC as possible and use thick, short connecting traces to reduce the circuit impedance.
9. Sufficiently reinforce the wiring between  $V_{DD}$  and GND. Noise that enters through  $V_{DD}$  and GND during switching may cause unstable IC operation.
10. Torex places an importance on improving our products and their reliability.  
We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

## TYPICAL PERFORMANCE CHARACTERISTICS

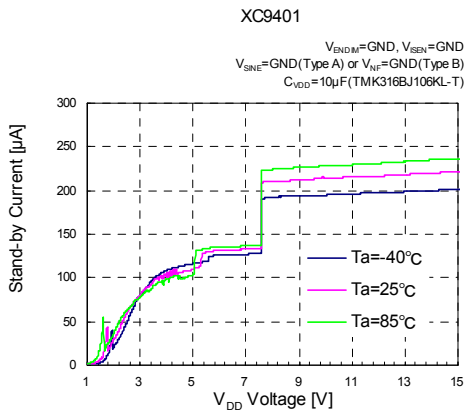
(1) Supply Current vs.  $V_{DD}$  Voltage



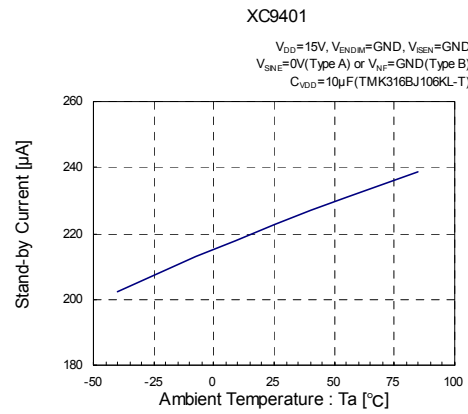
(2) Supply Current vs. Ambient Temperature



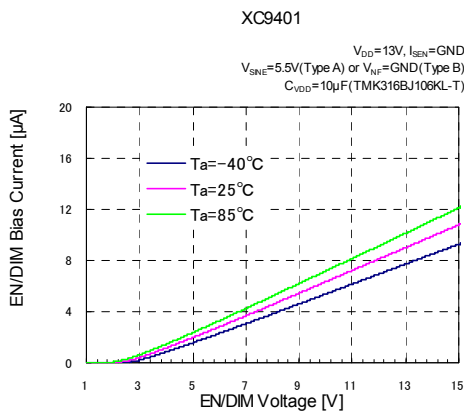
(3) Stand-by Current vs.  $V_{DD}$  Voltage



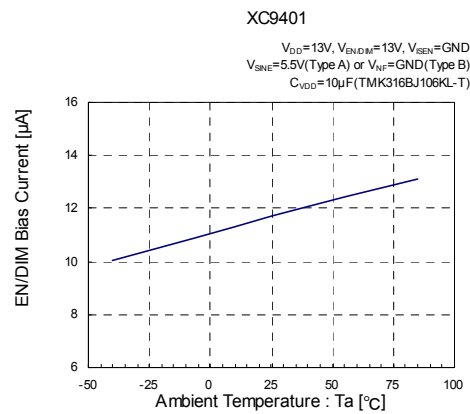
(4) Stand-by Current vs. Ambient Temperature



(5) EN/DIM Bias Current vs. EN/DIM Voltage



(6) EN/DIM Bias Current vs. Ambient Temperature



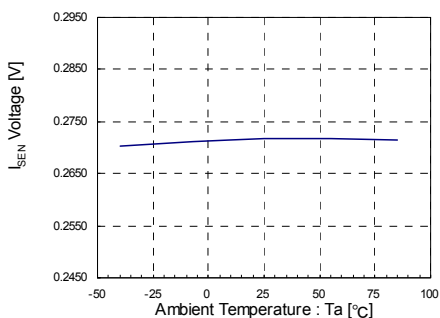


## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(7)  $I_{SEN}$  Voltage vs. Ambient Temperature

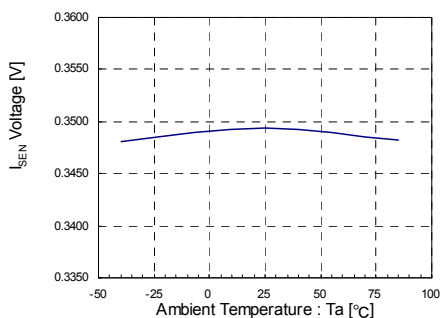
XC9401A605MR

$V_{DD}=13V, V_{ENDM}=V_{DD}$   
 $V_{SNE}=1.0V$  (Type A)  
 $C_{VDD}=10\mu F$  (TMK316BJ106KL-T)



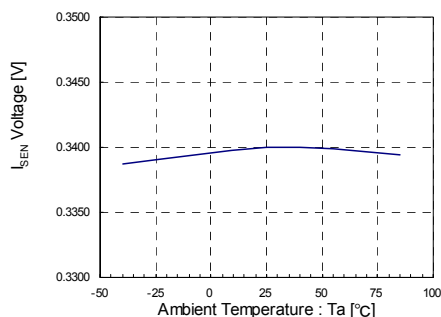
XC9401B605MR

$V_{DD}=13V, V_{ENDM}=V_{DD}$   
 $V_{NF}=GND$  (Type B)  
 $C_{VDD}=10\mu F$  (TMK316BJ106KL-T)



XC9401C60CSR

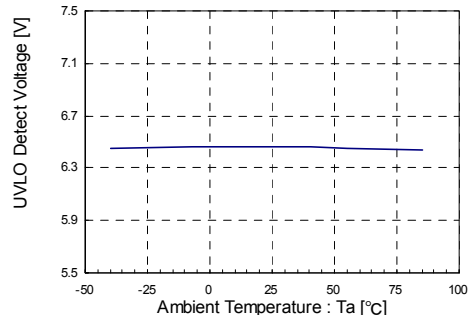
$V_{DD}=13V, V_{ENDM}=V_{DD}$   
 $C_{VDD}=10\mu F$  (TMK316BJ106KL-T)



(8) UVLO Detect Voltage vs. Ambient Temperature

XC9401

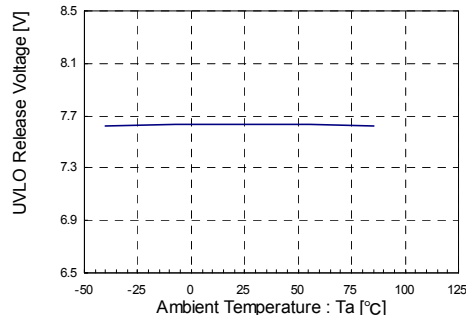
$V_{ENDM}=V_{DD}, V_{SEN}=GND$   
 $V_{SNE}=5.5V$  (Type A) or  $V_{NF}=GND$  (Type B)  
 $C_{VDD}=10\mu F$  (TMK316BJ106KL-T)



(9) UVLO Release Voltage vs. Ambient Temperature

XC9401

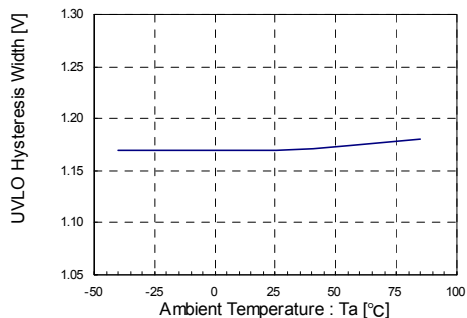
$V_{ENDM}=V_{DD}, V_{SEN}=GND$   
 $V_{SNE}=5.5V$  (Type A) or  $V_{NF}=GND$  (Type B)  
 $C_{VDD}=10\mu F$  (TMK316BJ106KL-T)



(10) UVLO Hysteresis Width vs. Ambient Temperature

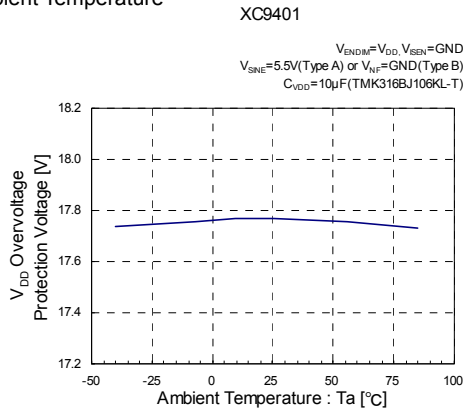
XC9401

$V_{ENDM}=V_{DD}, V_{SEN}=GND$   
 $V_{SNE}=5.5V$  (Type A) or  $V_{NF}=GND$  (Type B)  
 $C_{VDD}=10\mu F$  (TMK316BJ106KL-T)

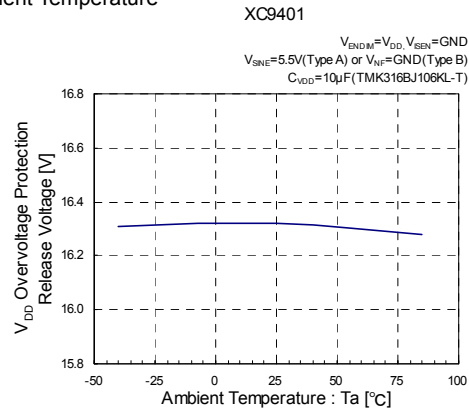


## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

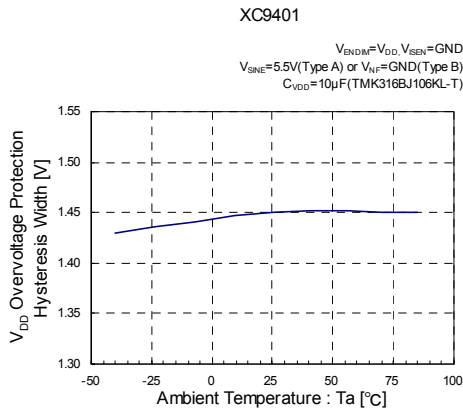
(11)  $V_{DD}$  Overvoltage Protection Voltage vs. Ambient Temperature



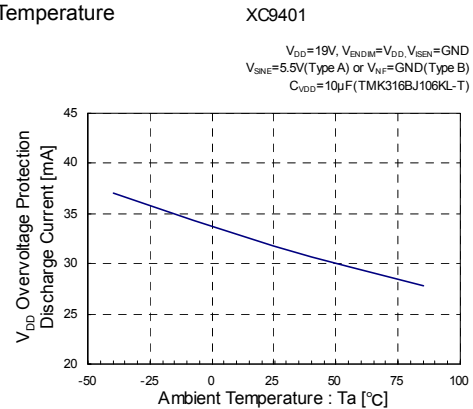
(12)  $V_{DD}$  Overvoltage Protection Release Voltage vs. Ambient Temperature



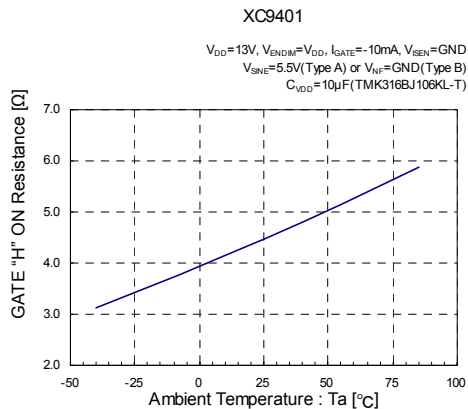
(13)  $V_{DD}$  Overvoltage Protection Hysteresis Width



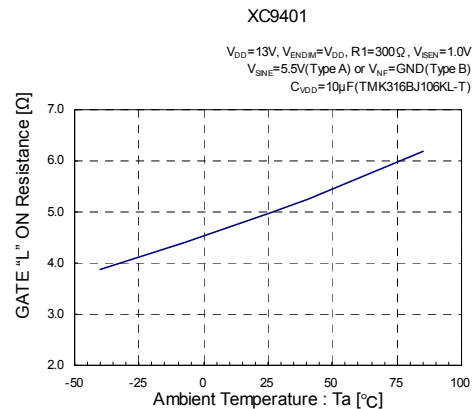
(14)  $V_{DD}$  Overvoltage Protection Discharge Current vs. Ambient Temperature



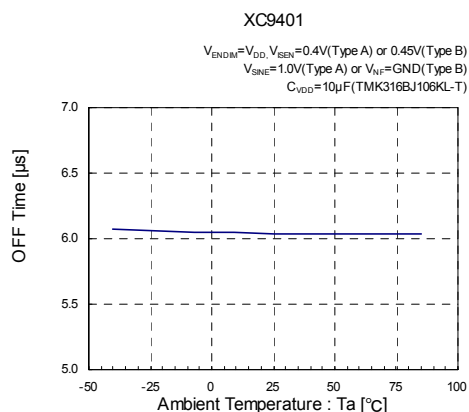
(15) GATE "H" ON Resistance vs. Ambient Temperature



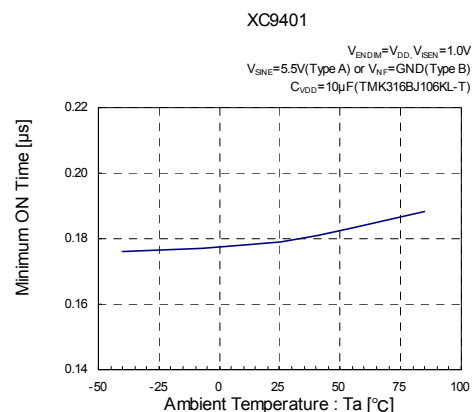
(16) GATE "L" ON Resistance vs. Ambient Temperature



(17) OFF Time vs. Ambient Temperature

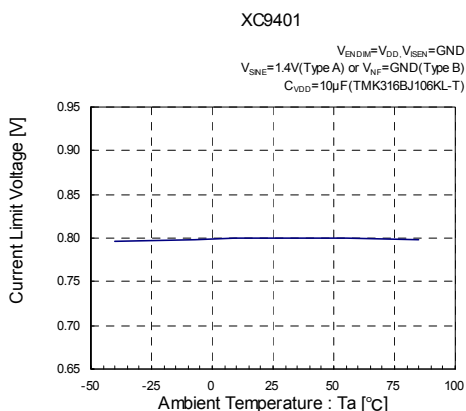


(18) Minimum ON Time vs. Ambient Temperature

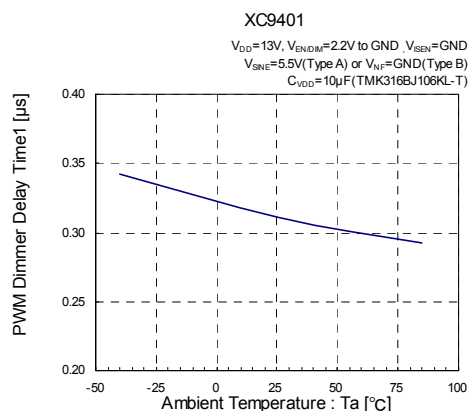


## ■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

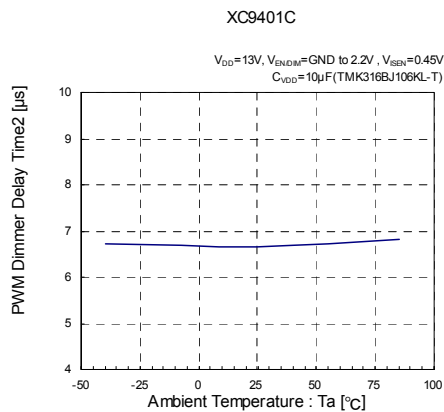
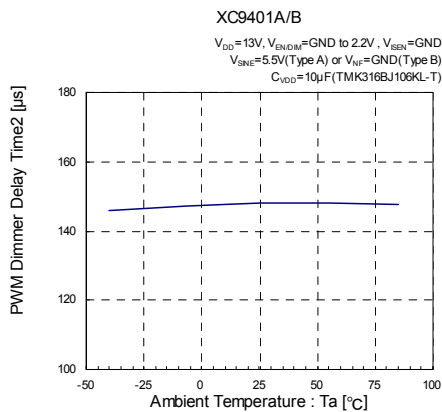
(19) Current Limit Voltage vs. Ambient Temperature



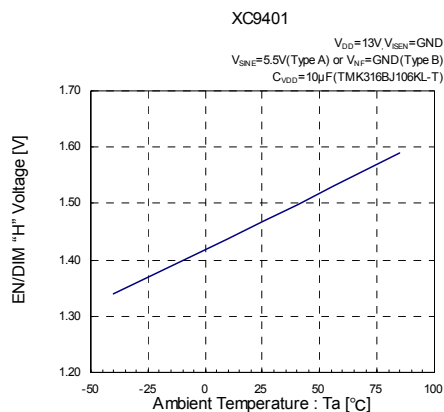
(20) PWM Dimmer Delay Time1 vs. Ambient Temperature



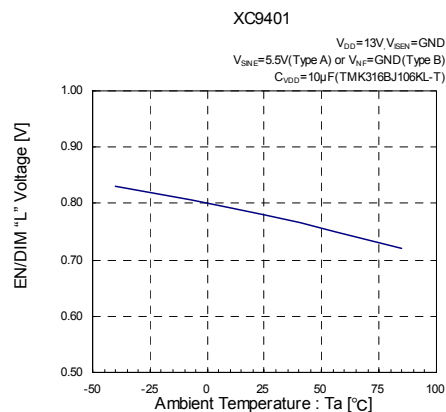
(21) PWM Dimmer Delay Time2 vs. Ambient Temperature



(22) EN/DIM "H" Voltage vs. Ambient Temperature

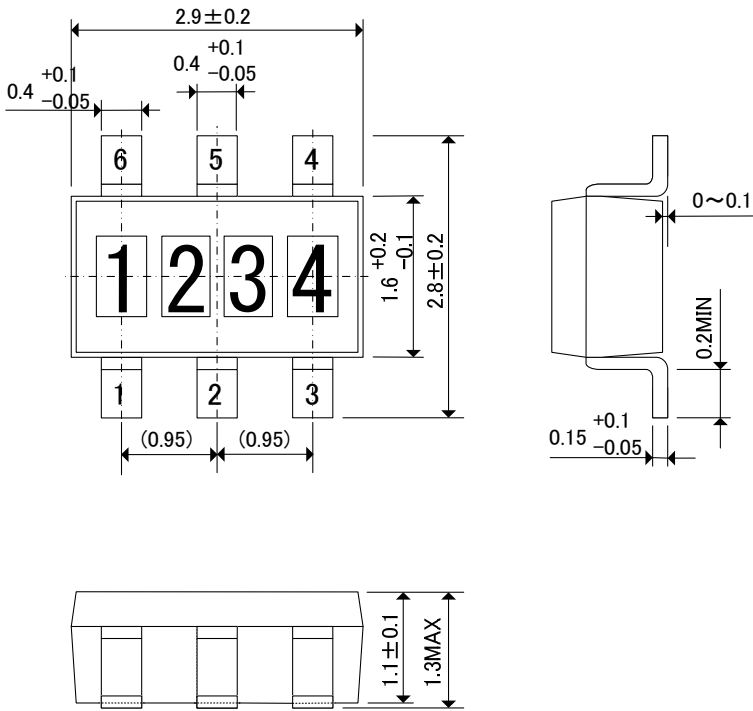


(23) EN/DIM "L" Voltage vs. Ambient Temperature

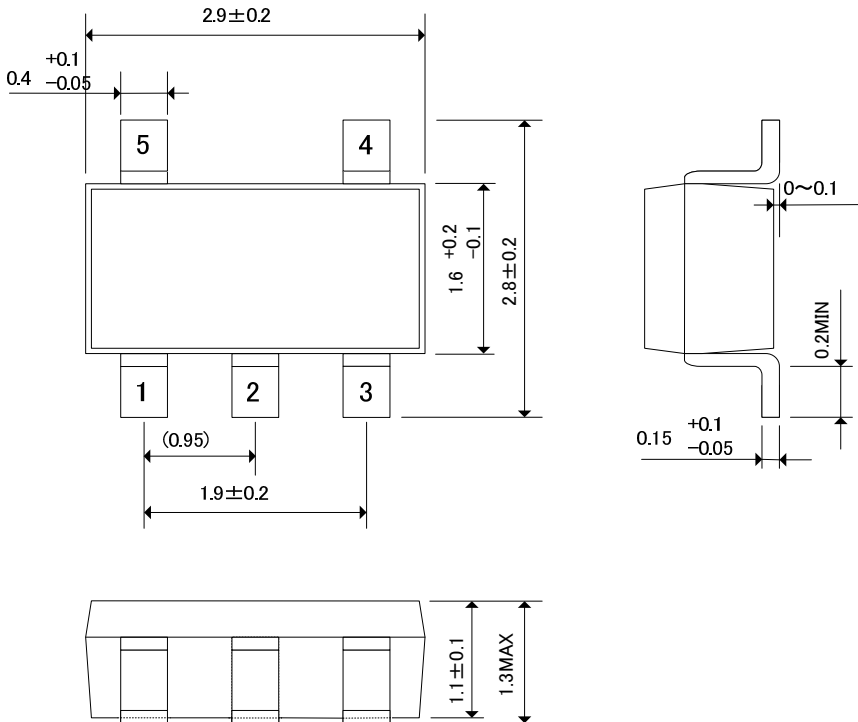


## PACKAGING INFORMATION

### ●SOT-26 (unit: mm)



### ●SOT-25 (unit: mm)



## ■ PACKAGING INFORMATION (Continued)

### ● SOT-25 Power Dissipation

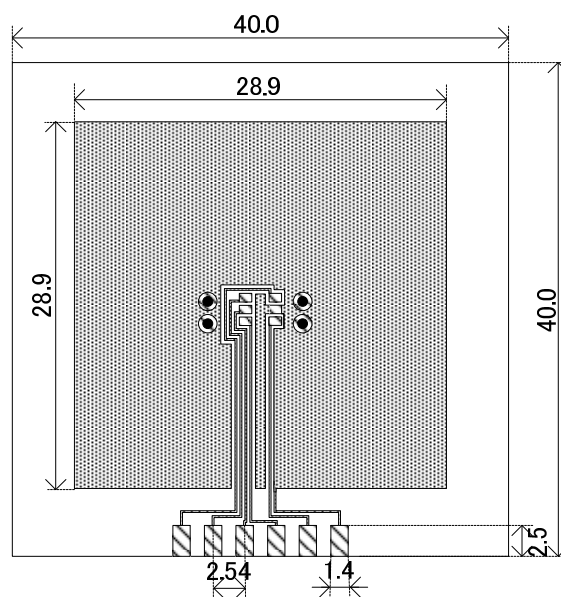
Power dissipation data for the SOT-25 is shown in this page.

The value of power dissipation varies with the mount board conditions.

Please use this data as one of reference data taken in the described condition.

#### 1. Measurement Condition (Reference data)

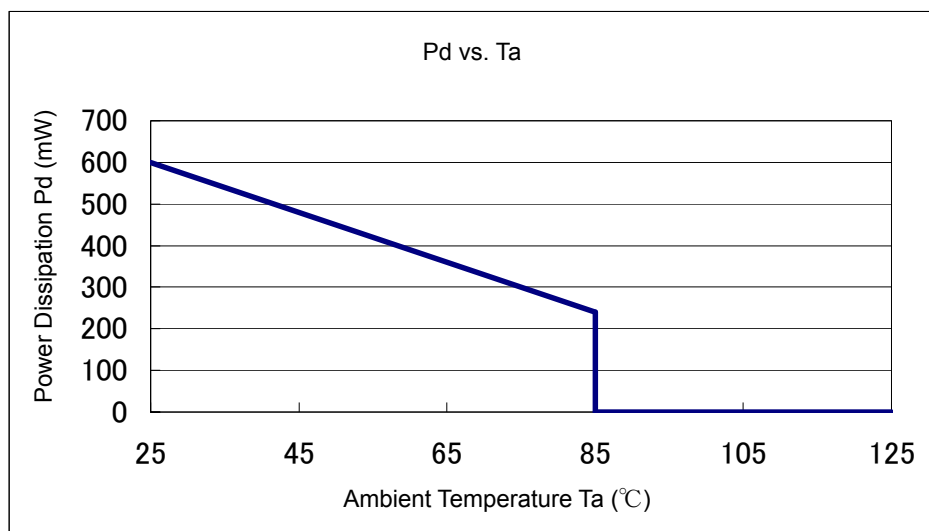
- Condition: Mount on a board
- Ambient: Natural convection
- Soldering: Lead (Pb) free
- Board: Dimensions 40 x 40 mm (1600 mm<sup>2</sup> in one side)  
Copper (Cu) traces occupy 50% of the board area  
In top and back faces  
Package heat-sink is tied to the copper traces  
(Board of SOT-26 is used.)
- Material: Glass Epoxy (FR-4)
- Thickness: 1.6 mm
- Through-hole: 4 x 0.8 Diameter



#### 2. Power Dissipation vs. Ambient temperature

Board Mount ( $T_j \text{ max} = 125^\circ\text{C}$ )

Ambient Temperature ( $^\circ\text{C}$ )	Power Dissipation Pd (mW)	Thermal Resistance ( $^\circ\text{C}/\text{W}$ )
25	600	166.67
85	240	



## PACKAGING INFORMATION (Continued)

### SOT-26 Power Dissipation

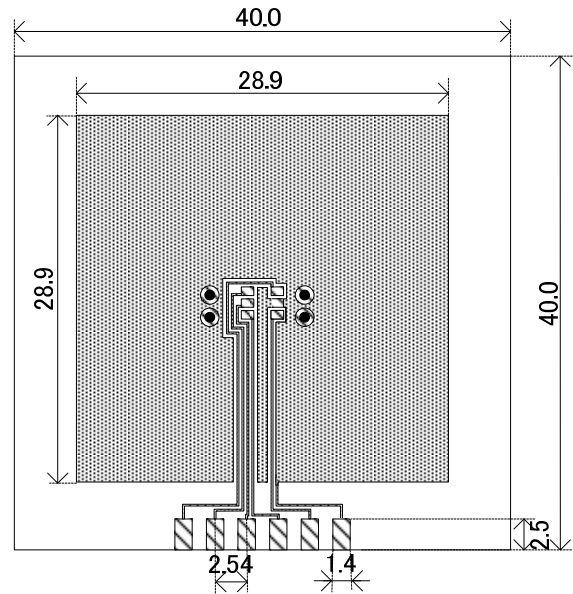
Power dissipation data for the SOT-26 is shown in this page.

The value of power dissipation varies with the mount board conditions.

Please use this data as one of reference data taken in the described condition.

#### 1. Measurement Condition (Reference data)

- Condition: Mount on a board
- Ambient: Natural convection
- Soldering: Lead (Pb) free
- Board: Dimensions 40 x 40 mm (1600 mm<sup>2</sup> in one side)  
Copper (Cu) traces occupy 50% of the board area  
In top and back faces  
Package heat-sink is tied to the copper traces  
(Board of SOT-26 is used.)
- Material: Glass Epoxy (FR-4)
- Thickness: 1.6 mm
- Through-hole: 4 x 0.8 Diameter

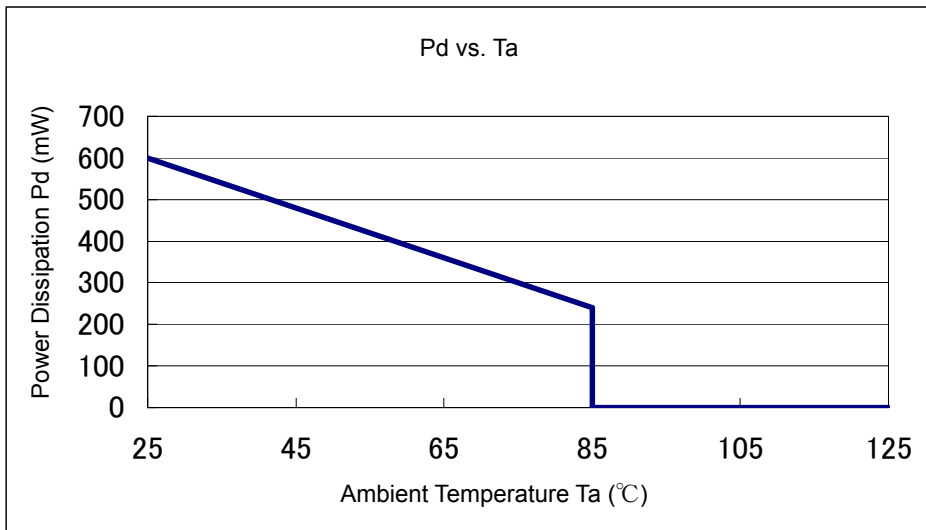


#### 2. Power Dissipation vs. Ambient temperature

Evaluation Board (Unit: mm)

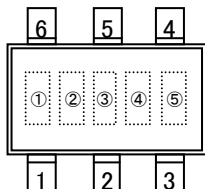
Board Mount ( $T_j \text{ max} = 125^\circ\text{C}$ )

Ambient Temperature ( $^\circ\text{C}$ )	Power Dissipation Pd (mW)	Thermal Resistance ( $^\circ\text{C/W}$ )
25	600	166.67
85	240	



## MARKING RULE

### ●SOT-26



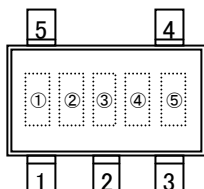
① represents product series

MARK	PRODUCT SERIES
3	XC9401*****-G

②③ represents product type

MARK		PRODUCT SERIES
②	③	
A	A	XC9401A605**-G
B	A	XC9401B605**-G
C	A	XC9401C60C**-G

### ●SOT-25



④⑤ represents production lot number

01~09, 0A~0Z, 11~9Z, A1~A9, AA~AZ, B1~ZZ in order.

(G, I, J, O, Q, W excluded)

\*No character inversion used.

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