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December 2014

FAN4010
High-Side Current Sensor

Features at +5 V

- Low Cost, Accurate, High-Side Current Sensing
- Output Voltage Scaling
- Up to 2.5 V Sense Voltage
- 2 V to 6 V Supply Range
- 2 µA Typical Offset Current
- 3.5 µA Quiescent Current
- -0.2% Accuracy
- 6-Lead MicroPak™ MLP Package

Applications Battery Chargers

- Battery Chargers
- Smart Battery Packs
- DC Motor Control
- Over-Current Monitor
- Power Management
- Programmable Current Source

Description

The FAN4010 is a high-side current sense amplifier designed for battery-powered systems. Using the FAN4010 for high-side power-line monitoring does not interfere with the battery charger's ground path. The FAN4010 is designed for portable PCs, cellular phones, and other portable systems where battery / DC power-line monitoring is critical.

To provide a high level of flexibility, the FAN4010 functions with an external sense resistor to set the range of load current to be monitored. It has a current output that can be converted to a ground-referred voltage with a single resistor, accommodating a wide range of battery voltages and currents. The FAN4010 features allow it to be used for gas gauging as well as uni-directional or bi-directional current monitoring.

Ordering Information

Part Number	Operating Temperature Range	Top Mark	Package	Packing Method
FAN4010IL6X	-40°C to +85°C	PX	6-Lead, Molded Leadless Package (MLP)	Tape & Reel
FAN4010IL6X_F113[1]				

Notes:
1. Legacy product number; please order FAN4010IL6X for new designs.
2. All packages are lead free per JEDEC: J-STD-020B standard.
3. Moisture sensitivity level for all parts is MSL-1.

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Block Diagram and Typical Circuit
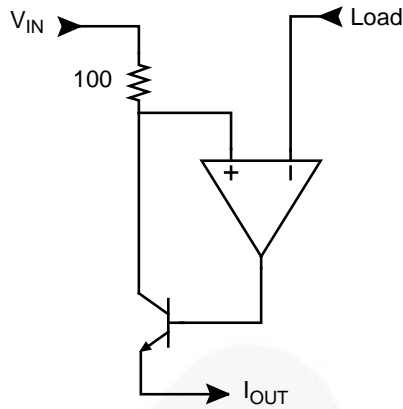
Figure 1.   Functional Block Diagram

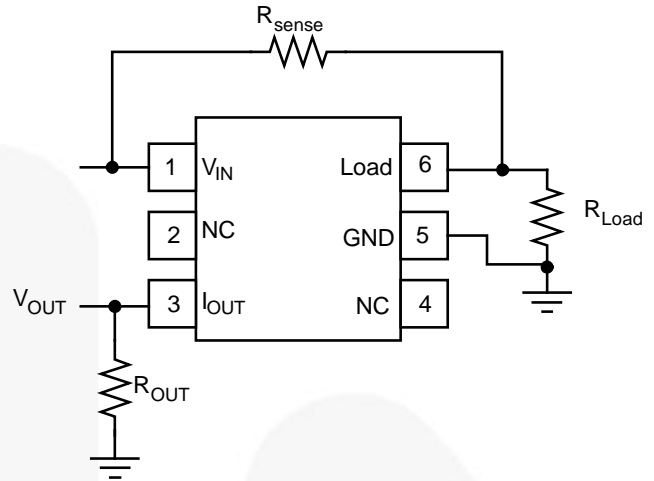**Figure 2.   Typical Circuit**

Pin Configuration
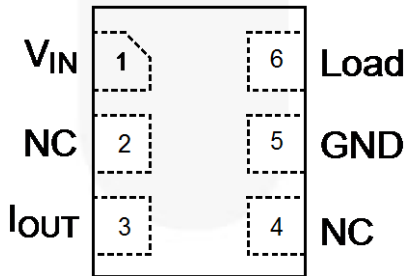
Figure 3.   Pin Assignment (Top Through View)

Pin Descriptions

Name	Type	Description
2, 4	NC	No Connect; leave pin floating
5	GND	Ground
3	I_{OUT}	Output Current, proportional to V_{IN}-V_{LOAD}
1	V_{IN}	Input Voltage, Supply Voltage
6	Load	Connection to load or battery

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Min.	Typ.	Max.	Unit
V_S	Supply Voltage		0		6.3	V
V_{IN}	Input Voltage Range		0		6.3	V
T_J	Junction Temperature				+150	°C
T_{STG}	Storage Temperature Range		-65		+150	°C
T_L	Reflow Temperature, Soldering				+260	°C
Θ_{JA}	Package Thermal Resistance[4]			456		°C/W
ESD	Electrostatic Discharge Protection	Human Body Model, JESD22-A114			5000	V
		Charged Device Model, JESD22-C101			1000	

Note:

4. Package thermal resistance (Θ_{JA}), JEDEC standard, multi-layer test boards, still air.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
T_A	Operating Temperature Range	-40	+85	°C
V_S	Supply Voltage Range	2	6	V
V_{IN}	Input Voltage	2	6	V
V_{SENSE}	Sensor Voltage Range, $V_{SENSE}=V_{IN}-V_{LOAD}$, $R_{OUT}=0\ \Omega$		2.5	V

Electrical Characteristics at +5 V

T_A = 25°C, $V_S = V_{IN}$ = 5 V, R_{OUT} = 100 Ω, R_{SENSE} = 100 Ω, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Frequency Domain Response						
B_{WSS}	Small Signal Bandwidth	P_{IN}=-40 dBm[5], V_{SENSE}=10 mV		600		kHz
B_{WLS}	Large Signal Bandwidth	P_{IN}=-20 dBm[6], V_{SENSE}=100 mV		2		MHz
V_{IN}	Input Voltage Range	$V_{IN}=V_S$	2		6	V
I_{OUT}	Output Current[7,8]	V_{SENSE}=0 mV	0	1	9	µA
		V_{SENSE}=10 mV	90	100	110	
		V_{SENSE}=100 mV	0.975	1.000	1.025	mA
		V_{SENSE}=200 mV	1.95	2.00	2.05	
		V_{SENSE}=1 V	9.7	10.0	10.3	
I_S	Supply Current[7]	V_{SENSE}=0 V, GND Pin Current		3.5	5.0	µA
I_{SENSE}	Load Pin Input Current			2		nA
A_{CY}	Accuracy	R_{SENSE}=100 Ω, R_{SENSE}=200 mV[7]	-2.5	-0.2	2.5	%
G_m	Transconductance	I_{OUT}/V_{SENSE}		10000		µA/V

Notes:
5.   -40 dBm = 6.3 mVpp into 50 Ω.
6.   -20 dBm = 63 mVpp into 50 Ω.
7.   100% tested at 25°C.
8.   Includes input offset voltage contribution.

Typical Performance Characteristics

T_A = 25°C, V_S = V_{IN} = 5 V, R_{OUT} = 100 Ω, R_{SENSE} = 100 Ω, unless otherwise noted.
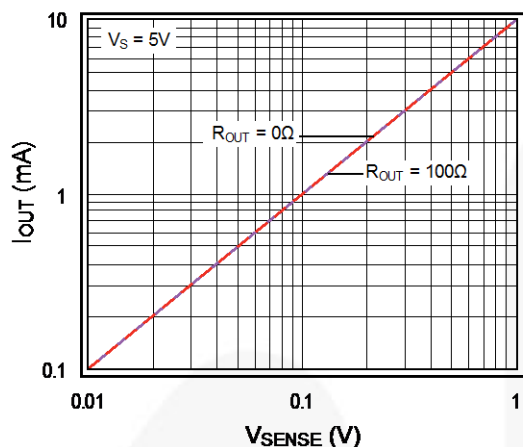
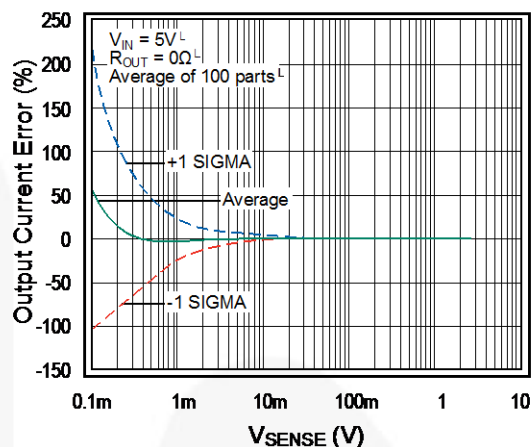**Figure 4.** V_{SENSE} vs. Output Current

Figure 5. Output Current Error vs. V_{SENSE}
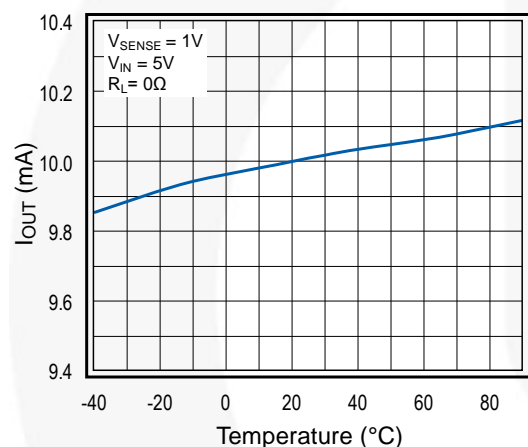
Figure 6. Output Current vs. Temperature

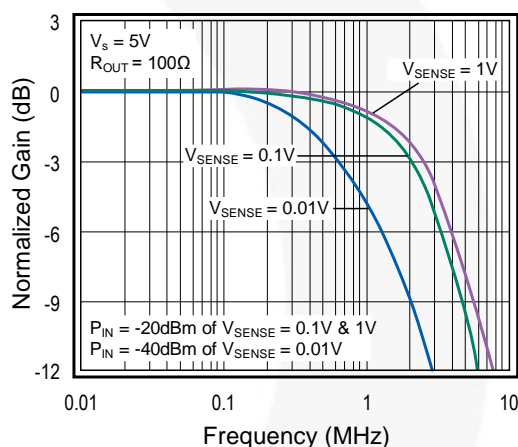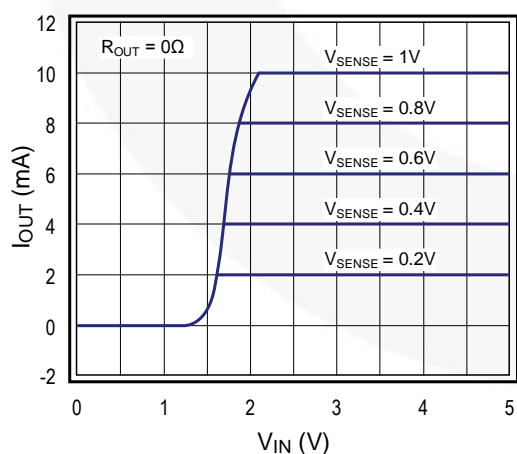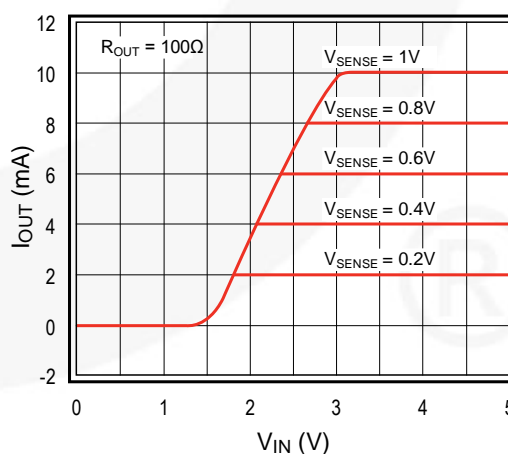**Figure 7.** Frequency Response

Figure 8. Transfer Characteristics

Figure 9. Transfer Characteristics

Typical Performance Characteristics (Continued)

T_A = 25°C, V_S = V_{IN} = 5 V, R_{OUT} = 100 Ω, R_{SENSE} = 100 Ω, unless otherwise noted.
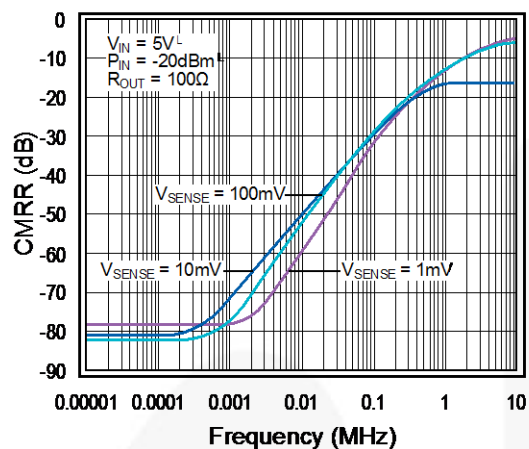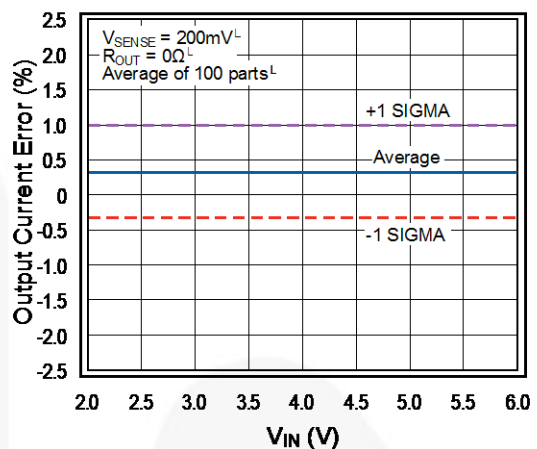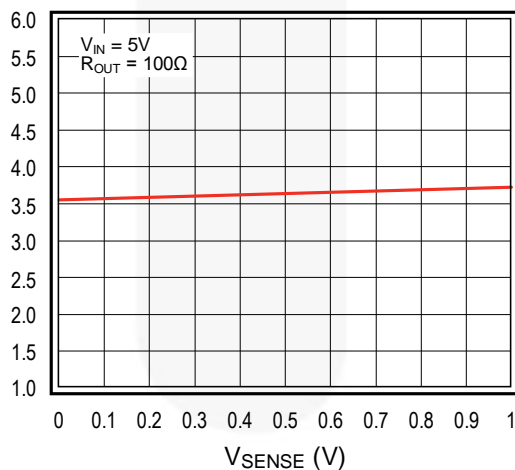
Figure 10.   CMRR vs. Frequency

Figure 11. V_{IN} vs. Output Current Error

Figure 12.   Supply Current vs. V_{SENSE}

Application Information

Detailed Description

The FAN4010 measures the voltage drop (V_{SENSE}) across an external sense resistor in the high-voltage side of the circuit. V_{SENSE} is converted to a linear current via an internal operational amplifier and precision 100 Ω resistor. The value of this current is V_{SENSE}/100 Ω (internal). Output current flows from the I_{OUT} pin to an external resistor R_{OUT} to generate an output voltage proportional to the current flowing to the load.

Use the following equations to scale a load current to an output voltage:

$$V_{SENSE} = I_{LOAD} \bullet R_{SENSE} \tag{1}$$

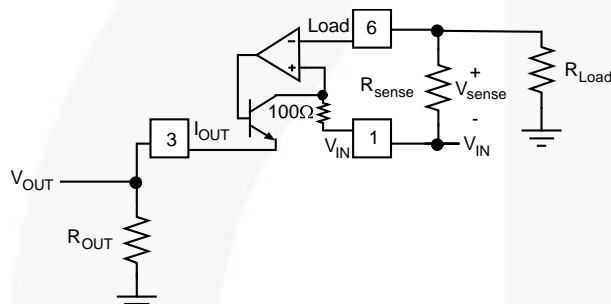$$V_{OUT} = 0.01 \times V_{SENSE} \times R_{OUT} \tag{2}$$

Figure 13.   Functional Circuit

Selecting R$_{SENSE}$

Selection of R_{SENSE} is a balance between desired accuracy and allowable voltage loss. Although the FAN4010 is optimized for high accuracy with low V_{SENSE} values, a larger R_{SENSE} value provides additional accuracy. However, larger values of R_{SENSE} create a larger voltage drop, reducing the effective voltage available to the load. This can be troublesome in low-voltage applications. Because of this, the maximum expected load current and allowable load voltage should be well understood. Although higher values of V_{SENSE} can be used, R_{SENSE} should be chosen to satisfy the following condition:

$$10\text{mV} < V_{SENSE} < 200mV \tag{3}$$

For low-cost applications where accuracy is not as important, a portion of the printed circuit board (PCB) trace can be used as an R_{SENSE} resistor. Figure 14 shows an example of this configuration. The resistivity of a 0.1-inch wide trace of two-ounce copper is about 30 mΩ/ft. Unfortunately, the resistance temperature coefficient is relatively large (approximately 0.4%/°C), so systems with a wide temperature range may need to compensate for this effect. Additionally, self heating due to load currents introduces a nonlinearity error. Care

must be taken not to exceed the maximum power dissipation of the copper trace.
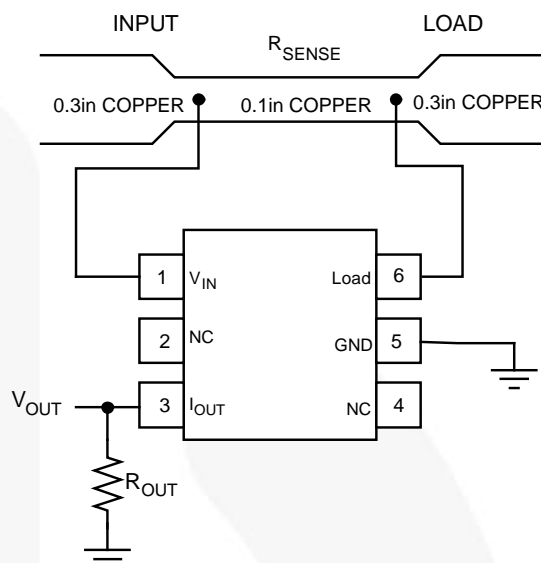

Figure 14.   Using PCB Trace for R$_{SENSE}$

Selecting R$_{OUT}$

R_{OUT} can be chosen to obtain the output voltage range required for the particular downstream application. For example, if the output of the FAN4010 is intended to drive an analog-to-digital convertor (ADC), R_{OUT} should be chosen such that the expected full-scale output current produces an input voltage that matches the input range of the ADC. For instance, if expected loading current ranges from 0 to 1 A, an R_{SENSE} resistor of 1 Ω produces an output current that ranges from 0 to 10 mA. If the input voltage range of the ADC is 0 to 2 V, an R_{OUT} value of 200 Ω should be used. The input voltage and full-scale output current (I_{OUT_FS}) needs to be taken into account when setting up the output range. To ensure sufficient operating headroom, choose:

$$\left(R_{OUT} \cdot I_{OUT_{FS}}\right) \text{ such that}$$
$$V_{IN} - V_{SENSE} - \left(R_{OUT} \cdot I_{OUT_{FS}}\right) > 1.6V \tag{4}$$

Output current accuracy for the recommended V_{SENSE} between 10 mV and 200 mV are typically better than 1%. As a result, the absolute output voltage accuracy is dependent on the precision of the output resistor.

Make sure the input impedance of the circuit connected to V_{OUT} is much higher than R_{OUT} to ensure accurate V_{OUT} values.

Since the FAN4010 provides a trans-impedance function, it is suitable for applications involving current rather than voltage sensing.
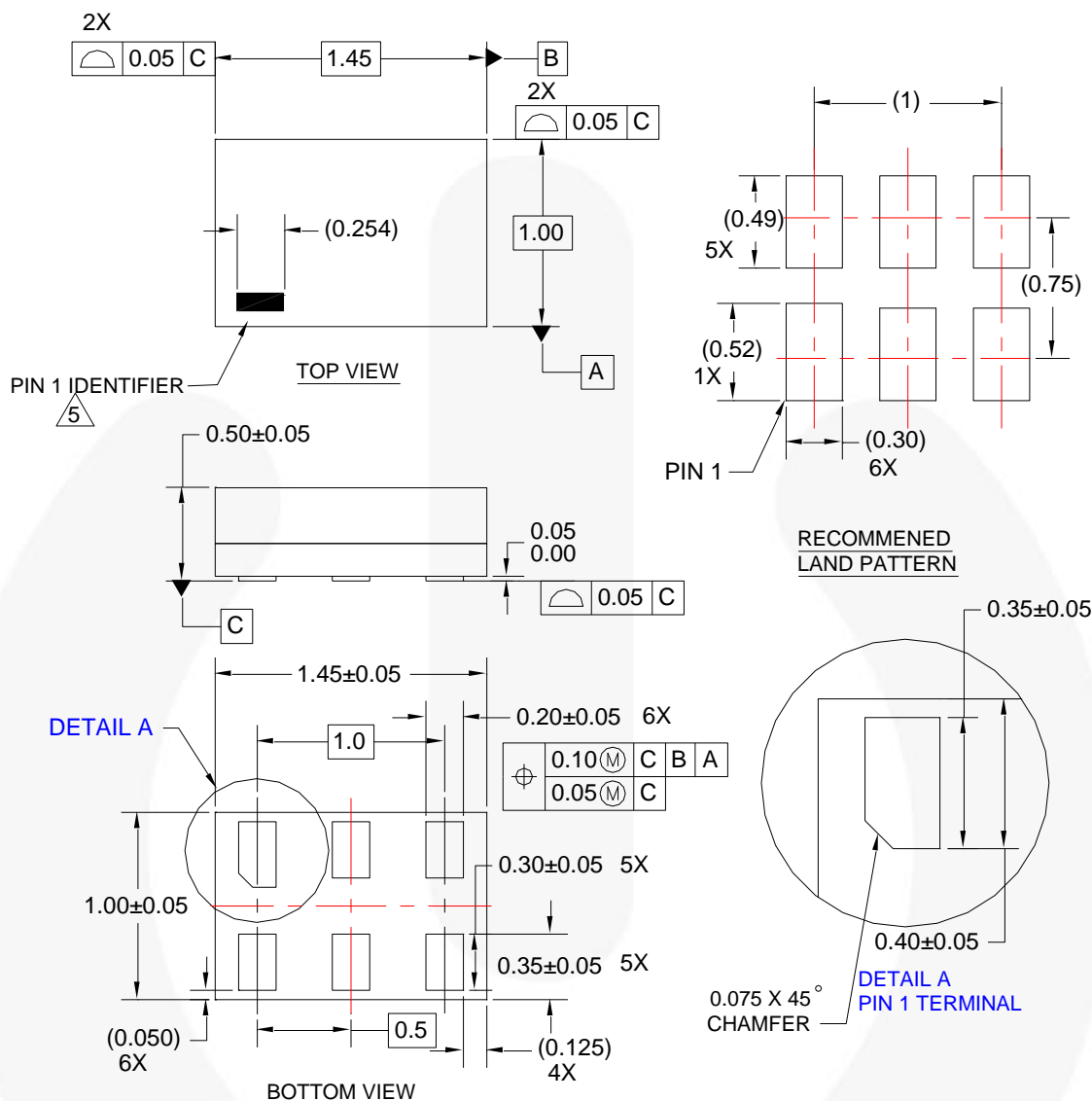
Physical Dimensions

TOP VIEW

PIN 1 IDENTIFIER
⑤

RECOMMENED LAND PATTERN

PIN 1

BOTTOM VIEW

DETAIL A

0.075 X 45° CHAMFER

DETAIL A PIN 1 TERMINAL

NOTES:

1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-2009
4. LANDPATTERN RECOMMENDATION PER FSC
⑤ PIN ONE IDENTIFIER IS 2X LENGTH OF ANY OTHER LINE IN THE MARK CODE LAYOUT.
6. FILENAME AND REVISION: MAC06AREV6

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Figure 15.   6-Lead MicroPak™ Molded Leadless Package (MLP)

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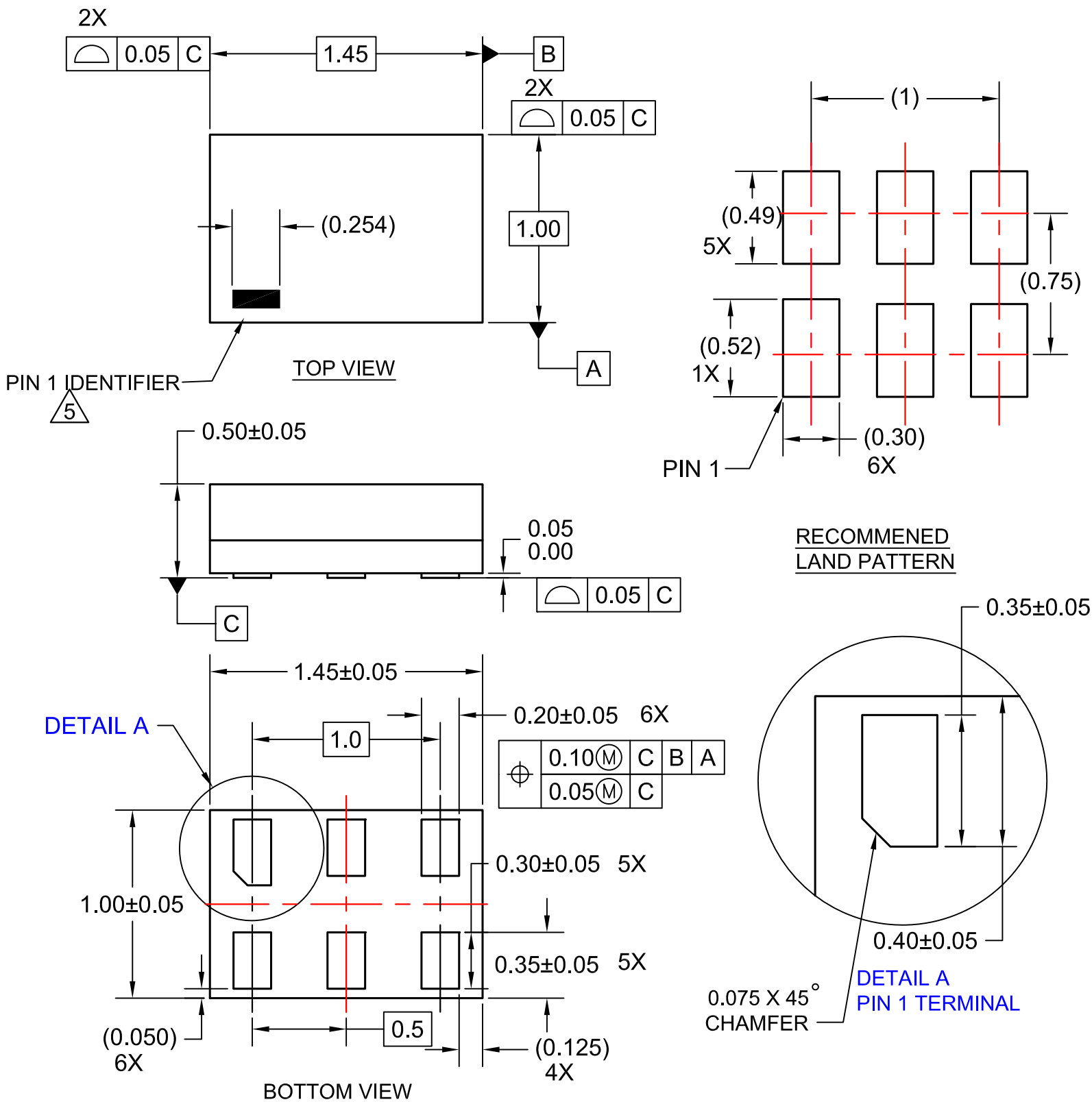
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Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. I72

TOP VIEW

2X

⌒ | 0.05 | C ← 1.45 → ▶ B

2X
⌒ | 0.05 | C

(0.254)

1.00

PIN 1 IDENTIFIER

⑤

A

BOTTOM VIEW

0.50±0.05

⌒ | 0.05 | C

C

DETAIL A

1.45±0.05

1.0

0.20±0.05 6X

⊕ | 0.10 Ⓜ | C B A
     | 0.05 Ⓜ | C

0.30±0.05 5X

1.00±0.05

0.35±0.05 5X

(0.050)
6X

0.5

(0.125)
4X

RECOMMENED LAND PATTERN

(1)

(0.49)
5X

(0.75)

(0.52)
1X

PIN 1

(0.30)
6X

DETAIL A PIN 1 TERMINAL

0.35±0.05

0.40±0.05

0.075 X 45°
CHAMFER

NOTES:

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