

## Power saving Push Button Load switch

## ■ GENERAL DESCRIPTION

The XC6192 series are the Push Button load switch with functions best suitable for battery operated devices.

The built-in high side switch is turned on by the Push Button (Turning off is also possible on the XC6192A type.) and turned off by the "L" level signal into the SHDN pin from the MCU or the like.

In addition to these functions, this IC is equipped with output capacitor inrush current limiting function and short-circuit protection function, realizing an intelligent load switch.

The high side switch is turned on and latched by inputting "L" signal from the Push Button of the device to the SW pin.

It is possible to shut down (OFF) by inputting a 1-pulse signal of "H" level from the MCU or the like to the SHDN pin. This realizes a main switch of battery operated devices easily. <sup>(\*1)</sup>

The leak current at shutdown is so small, which is 10nA(Typ.), that this IC will contribute to reducing the discharge of the battery and making shelf life longer of the devices after shipping as well as functioning as a main switch.

For the A type, it is possible to turn off forcibly in case of emergency by Push Button signal. This enables a freezed device to be turned off. For the B type, turn-off is available only with SHDN pin.

The output capacitor inrush current limiting function suppresses excessive current that occurs when the switch is turned on, preventing it from going into a brownout state.

The output short-circuit protection function detects the voltage drop due to the short circuit and turns off the power supply line by force. The "L" signal on the SW pin by Push button makes the device recover.

The power good function is used to properly adjust the timing of turning on the DC-DC regulator or other system on the rear stage.

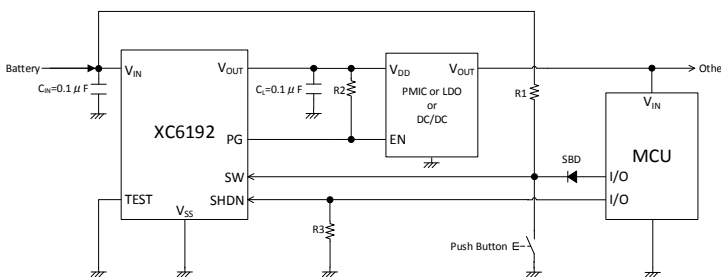
(\*1) Please be sure to complete the preparation for shutting down safely before inputting the signal to SHDN pin from the MCU or the likes.

(\*2)  $V_{OUT}$  may not start up completely if the load current is 350 $\mu$ A (DC) or more at starting up due to the output capacitor inrush current limiting function. Please design so that the started up load current is less than 350 $\mu$ A (DC) or the output on the rear stage is enabled by using the PG pin.

## ■ APPLICATIONS

- Wearable devices
- Wireless headphones / Earphones
- Mobile devices
- Various applications equipped with buttons
- Various applications equipped with mechanical switches
- The devices with waterproof function.

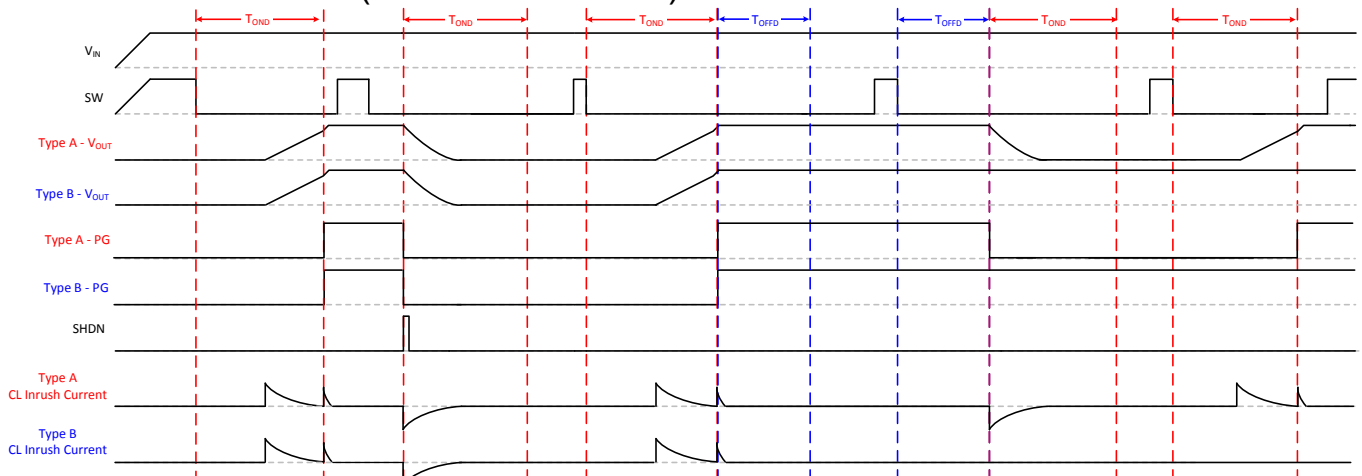
## ■ TYPICAL APPLICATION CIRCUIT



## ■ FEATURES

Input Voltage Range	:	2.5V~6.0V
Stand-by Current	:	0.01 $\mu$ A (Typ.) / Turn-Off state
Quiescent Current	:	0.45 $\mu$ A (Typ.) / Turn-On state
Output Current	:	400mA ( $V_{IN}=2.5V$ , $T_a=25^{\circ}C$ )
Turn-On Delay Time	:	0.5s, 1.0s, 3.0s or 5s
( $T_{OND}$ )		
Turn-Off Method	:	Type A •By inputting "H" voltage to the SHDN pin. •By inputting "L" voltage during the $T_{OFFD}$ to the SW pin.
		Type B •By inputting "H" voltage to the SHDN pin.
Turn-Off Delay Time	:	3s, 5s, 10s or 15s
( $T_{OFFD}$ )		
Added function	:	Power Good function (the PG pin) Forced shutdown function (the SHDN pin)
Protective function	:	Output capacitor inrush current limit function Output circuit short protection Output capacitor discharge function
Operating Ambient Temperature	:	-40 $^{\circ}C$ ~85 $^{\circ}C$
Package	:	USP-8B06

## ■ TIMING CHART (XC6192xxxxxR-G)



## ■ BLOCK DIAGRAM

XC6192A



XC6192B



\*Diodes inside the circuit are an ESD protection diodes and a parasitic diodes.

## ■ PRODUCT CLASSIFICATION

### ● Ordering Information

XC6192①②③④⑤⑥-⑦<sup>(\*)</sup>

DESIGNATOR	DESCRIPTION	SYMBOL	DESCRIPTION
①	TYPE	A	Turn-on: By the SW pin Shut-down: By the SW pin or the SHDN pin
		B	Turn-on: By the SW pin Shut-down: By the SHDN pin
②	Turn-On delay time	A	0.5s.
		1	1s. option <sup>(*)2</sup>
		3	3s. option <sup>(*)2</sup>
		5	5s. option <sup>(*)2</sup>
③④	Turn-Off delay time	NN	No "Turn-Off delay time" function. Applies to XC6192B only
		03	3s. option <sup>(*)2</sup>
		05	5s.
		10	10s.
		15	15s. option <sup>(*)2</sup>
⑤⑥-⑦	Package and Taping Type	ER-G	USP-8B06 (5,000pcs/Reel)

<sup>(\*)1</sup> The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

<sup>(\*)2</sup> For option products, please contact your local Torex sales office or representative.

### ● Selection Guide

Parts No.	Turn-On Delay Time (s)	Turn-Off Delay Time (s)	Package
XC6192AA05ER-G	0.5s	5s	USP-8B06
XC6192AA10ER-G		10s	
XC6192BANNER-G		Not applicable	

## PIN CONFIGURATION



\*The dissipation pad for the USP-8B06 package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release.

When taking out a potential of the heat-sink, connect with  $V_{SS}$  pin (#5 pin).

## PIN ASSIGNMENT

PIN NUMBER	PIN NAME	FUNCTION
USP-8B06		
1	$V_{IN}$	Power Supply Input
2	N.C	No connection pin (The N.C pin should be connected the $V_{SS}$ pin.)
3	SW	Push Button Signal input pin
4	PG	Power Good Status Output
5	$V_{SS}$	Ground Pin
6	TEST	The TEST pin must be connected the $V_{SS}$ pin.
7	SHDN	Forced Shutdown pin
8	$V_{OUT}$	Output pin

## PIN FUNCTIOS ASSIGNMENT

PIN NAME	SIGNAL	STATUS
SW <sup>(*)</sup>	L	Active
	H	Keep the current state
	OPEN	Undefined State <sup>(*)</sup>
SHDN <sup>(*)</sup>	L	Keep the current state
	H	Shut down
	OPEN	Undefined State <sup>(*)</sup>
PG	Low impedance	$V_{OUT} < V_{RUSH}^{(*)}$
	High impedance	$V_{OUT} \geq V_{RUSH}^{(*)}$

<sup>(\*)</sup> Please do not leave the SW pin and the SHDN pin open. Each should have a certain voltage

<sup>(\*)</sup> Please refer to electrical characteristics.

<sup>(\*)</sup> Please refer to notes on use.

## ■ ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMETER		SYMBOL	RATINGS	UNITS
V <sub>IN</sub> Pin Voltage		V <sub>IN</sub>	-0.3 ~ +6.5	V
V <sub>OUT</sub> Pin Voltage		V <sub>OUT</sub>	-0.3 ~ V <sub>IN</sub> +0.3 or +6.5 <sup>(*)</sup>	V
V <sub>OUT</sub> Pin Output Current		I <sub>OUT</sub>	470	mA
SW Pin Voltage		V <sub>SW</sub>	-0.3 ~ +6.5	V
SHDN Pin Voltage		V <sub>SHDN</sub>	-0.3 ~ +6.5	V
PG Pin Voltage		V <sub>PG</sub>	-0.3 ~ +6.5	V
TEST Pin Voltage		V <sub>TEST</sub>	-0.3 ~ V <sub>IN</sub> +0.3 or +6.5 <sup>(*)</sup>	V
Power Dissipation	USP-8B06	P <sub>d</sub>	1800 <sup>(**)</sup>	mW
Operating Ambient Temperature		T <sub>opr</sub>	-40 ~ +85	°C
Storage Temperature		T <sub>stg</sub>	-55 ~ +125	°C

Each rating voltage is based on the V<sub>SS</sub>

(\*) Either of lower one, V<sub>IN</sub>+0.3 or +6.5, is applicable.

(\*\*) The power dissipation figure shown is PCB mounted. Please see the power dissipation page for the mounting condition.

## ELECTRICAL CHARACTERISTICS

XC6192 Series

Ta=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT	CIRCUIT
Input Voltage Range	V <sub>IN</sub>		2.5	-	6	V	(1)
Stand-by Current <sup>(*)1)</sup>	I <sub>STB</sub>	V <sub>IN</sub> =6.0V, V <sub>SW</sub> =V <sub>IN</sub>	-0.10	0.01	0.10	μA	(1)
Operation Current <sup>(*)2)</sup>	I <sub>OP</sub>	Turn-Off states, V <sub>IN</sub> =6.0V, V <sub>SW</sub> =0V,	-	48	100	μA	(1)
Quiescent Current <sup>(*)3)</sup>	I <sub>Q</sub>	Turn-On keeps, V <sub>IN</sub> =6.0V, V <sub>SW</sub> =V <sub>IN</sub>	-	0.45	1.30	μA	(1)
Turn-On Delay Time <sup>(*)4)</sup> (*)9)	T <sub>OND</sub>	XC6192xAxx V <sub>IN</sub> =3.7V, Change the V <sub>SW</sub> from V <sub>IN</sub> to 0V.	0.35	0.50	0.65	s	(1)
		XC6192x1xx V <sub>IN</sub> =3.7V, Change the V <sub>SW</sub> from V <sub>IN</sub> to 0V.	0.70	1.00	1.30		
		XC6192x3xx V <sub>IN</sub> =3.7V, Change the V <sub>SW</sub> from V <sub>IN</sub> to 0V.	2.10	3.00	3.90		
		XC6192x5xx V <sub>IN</sub> =3.7V, Change the V <sub>SW</sub> from V <sub>IN</sub> to 0V.	3.50	5.00	6.50		
Turn-Off Delay Time <sup>(*)5)</sup> (*)6)(*)9)	T <sub>OFFD</sub>	XC6192Ax03 V <sub>IN</sub> =3.7V, Change the V <sub>SW</sub> from V <sub>IN</sub> to 0V.	2.1	3.0	3.9	s	(1)
		XC6192Ax05 V <sub>IN</sub> =3.7V, Change the V <sub>SW</sub> from V <sub>IN</sub> to 0V.	3.5	5.0	6.5		
		XC6192Ax10 V <sub>IN</sub> =3.7V, Change the V <sub>SW</sub> from V <sub>IN</sub> to 0V.	7.0	10.0	13.0		
		XC6192Ax15 V <sub>IN</sub> =3.7V, Change the V <sub>SW</sub> from V <sub>IN</sub> to 0V.	10.5	15.0	19.5		
Inrush Current Limit Voltage	V <sub>RUSH</sub>	V <sub>OUT</sub> condition	V <sub>IN</sub> - 0.23	V <sub>IN</sub> - 0.13	V <sub>IN</sub> - 0.05	V	(1)
Short Circuit Detect Voltage	V <sub>SHORT</sub>	V <sub>OUT</sub> condition	V <sub>IN</sub> - 0.98	V <sub>IN</sub> - 0.66	V <sub>IN</sub> - 0.32	V	(1)
Pre-driver On Resistor	R <sub>ONP</sub>	Turn-On states, V <sub>IN</sub> =2.5V	63	105	139	Ω	(1)
		Turn-On states, V <sub>IN</sub> =3.7V					(1)
		Turn-On states, V <sub>IN</sub> =6.0V					(1)
Main Driver On Resistor	R <sub>ON</sub>	Turn-On states, V <sub>IN</sub> =2.5V	0.40	0.59	0.78	Ω	(1)
		Turn-On states, V <sub>IN</sub> =3.7V	0.28	0.45	0.59	Ω	(1)
		Turn-On states, V <sub>IN</sub> =6.0V	0.20	0.34	0.49	Ω	(1)

Unless otherwise stated, V<sub>SS</sub> common, V<sub>IN</sub>=6.0V, V<sub>SW</sub>=6.0V, V<sub>SHDN</sub>=0V, V<sub>PG</sub>=Open, I<sub>OUT</sub>=0mA, C<sub>IN</sub>=0.1μF, C<sub>L</sub>=0.1μF

(\*)1) Self-supply current when V<sub>OUT</sub> is in the Turn-off state and V<sub>IN</sub> level is input into the V<sub>SW</sub> pin.

(\*)2) Self-supply current when V<sub>OUT</sub> is in the Turn-off state and "L" level is input into the V<sub>SW</sub> pin.

(\*)3) Self-supply current when V<sub>OUT</sub> is in the Turn-on state and V<sub>IN</sub> level is input into the V<sub>SW</sub> pin.

(\*)4) Time from SW pin "L" level until V<sub>OUT</sub> pin "H" level(\*)7) pin in the Turn-off state.

(\*)5) Time from SW pin "L" level until V<sub>OUT</sub> pin "L" level(\*)8) pin in the Turn-on state.

(\*)6) Type B does not have this function.

(\*)9) Electrical characteristics are characteristics before mounting on PCB. Refer to the Usage Cautions.

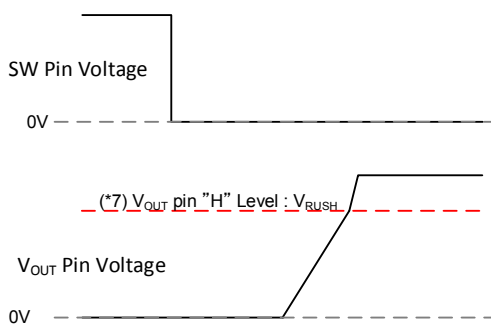


Fig1. V<sub>OUT</sub> pin "H" Voltage

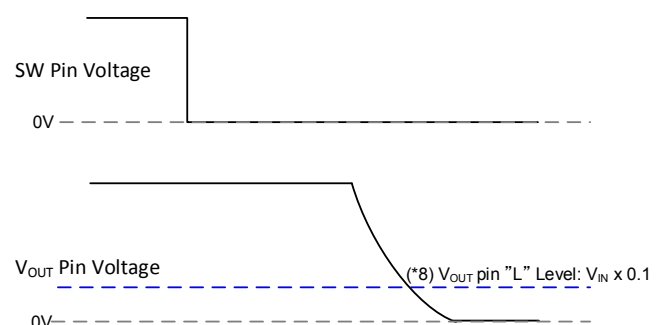


Fig2. V<sub>OUT</sub> pin "L" Voltage

## ■ ELECTRICAL CHARACTERISTICS

XC6192 Series

Ta=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT	CIRCUIT
SW pin "H" Voltage	$V_{SWH}$		1.1	-	6.0	V	(1)
SW pin "L" Voltage	$V_{SWL}$		0	-	0.4	V	(1)
SW pin "H" Current	$I_{SWH}$	$V_{SW}=6.0V$	-0.10	0.01	0.10	$\mu A$	(1)
SW pin "L" Current	$I_{SWL}$	$V_{SW}=0V$	-0.10	0.01	0.10	$\mu A$	(1)
SHDN pin "H" Voltage	$V_{SHDNH}$		1.1	-	6.0	V	(1)
SHDN pin "L" Voltage	$V_{SHDNL}$		0	-	0.4	V	(1)
SHDN pin "H" Current	$I_{SHDNH}$	$V_{SHDN}=6.0V$	-0.10	0.01	0.10	$\mu A$	(1)
SHDN pin "L" Current	$I_{SHDNL}$	$V_{SHDN}=0V$	-0.10	0.01	0.10	$\mu A$	(1)
PG pin Output Current	$I_{PG}$	Turn-Off States, $V_{IN}=2.5V$ , $V_{PG}=0.3V$	1	-	-	mA	(1)
PG pin Leak Current	$I_{PGL}$	Turn-On states, $V_{IN}=6.0V$ , $V_{PG}=6.0V$	-0.10	0.01	0.10	$\mu A$	(1)
CL Discharge Current	$I_{DCG}$	Turn-Off States, $V_{IN}=2.5V$ , $V_{OUT}=0.3V$	1	-	-	mA	(1)

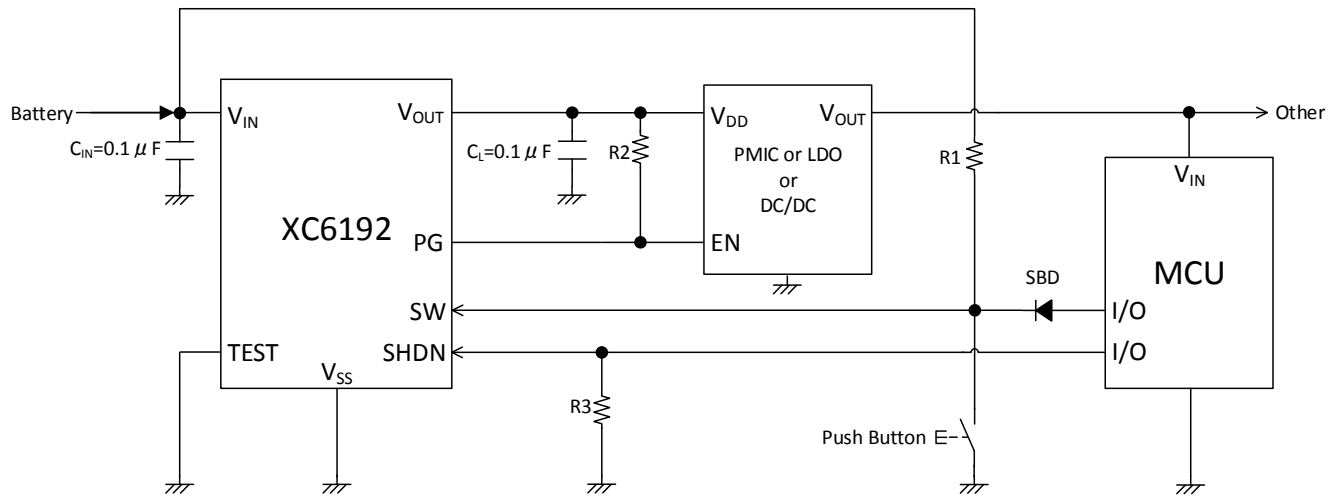
Unless otherwise stated,  $V_{SS}$  common,  $V_{IN}=6.0V$ ,  $V_{SW}=6.0V$ ,  $V_{SHDN}=0V$ ,  $V_{PG}=\text{Open}$ ,  $I_{OUT}=0mA$ ,  $C_{IN}=0.1\mu F$ ,  $C_L=0.1\mu F$

## ■ TEST CIRCUITS

Test Circuit (1)



## TYPICAL APPLICATION CIRCUIT



### 【Typical Examples】

	VALUE	MANUFACTURER	PRODUCT NUMBER
SBD <sup>(*)</sup>	$V_F = 0.37V$	TOREX	XBS013V1DR-G
$C_{IN}$	0.1 $\mu$ F/10V (Ceramic)		
$C_L$	0.1 $\mu$ F/10V (Ceramic)		
R1	200k $\Omega$		
R2	200k $\Omega$		
R3	20k $\Omega$		

<sup>(\*)</sup> Please select freely according to the threshold of the MCU's I/O.



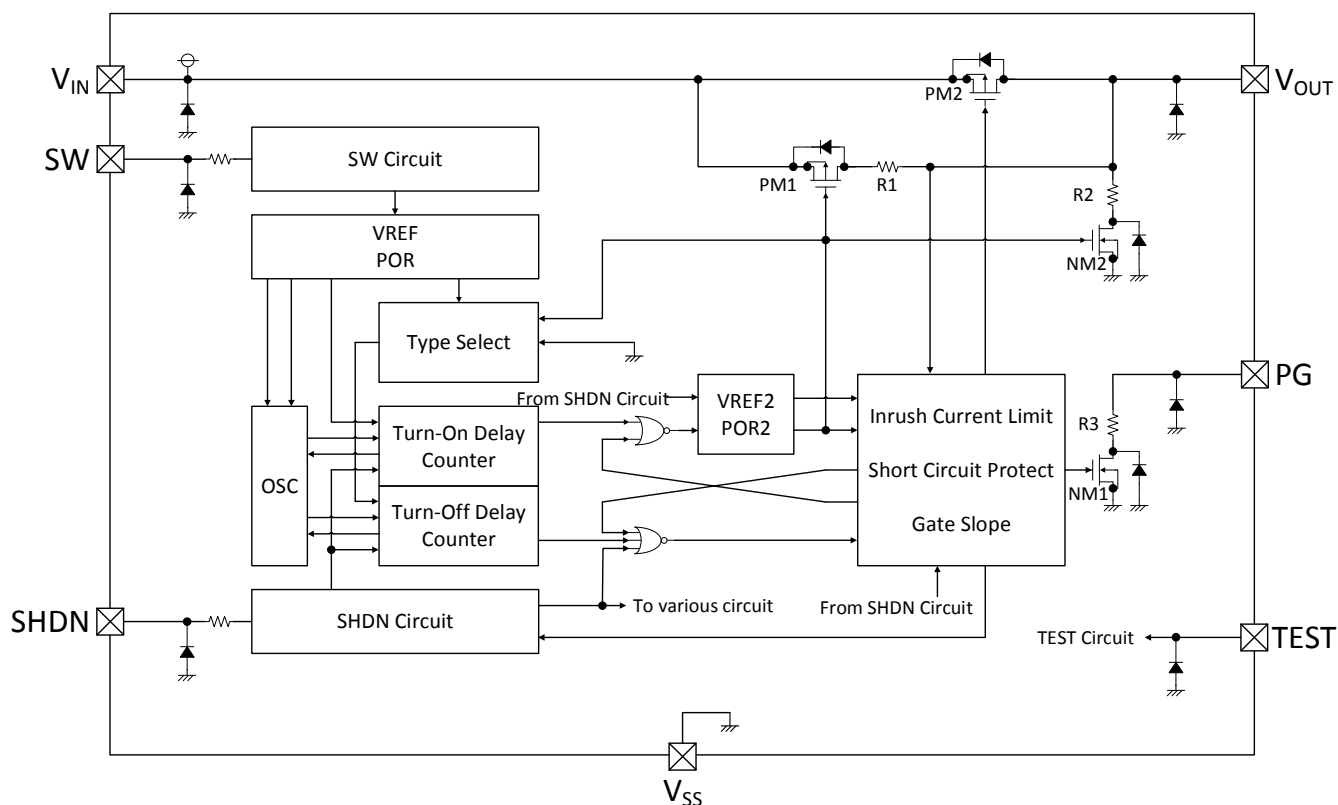
## OPERATIONAL EXPLANATION

The XC6192 series A type fixes  $V_{OUT}$  at "H" voltage (same level as the  $V_{IN}$  pin voltage) when "L" voltage is input the required time into the SW pin, and shuts down  $V_{OUT}$  (same level as GND) when "L" voltage is subsequently input the required time into the SW pin, thereby realizing alternating ON/OFF control<sup>(\*)</sup>. Two shutdown methods are available: inputting "H" voltage into the SHDN pin, and inputting "L" voltage the required time into the SW pin.

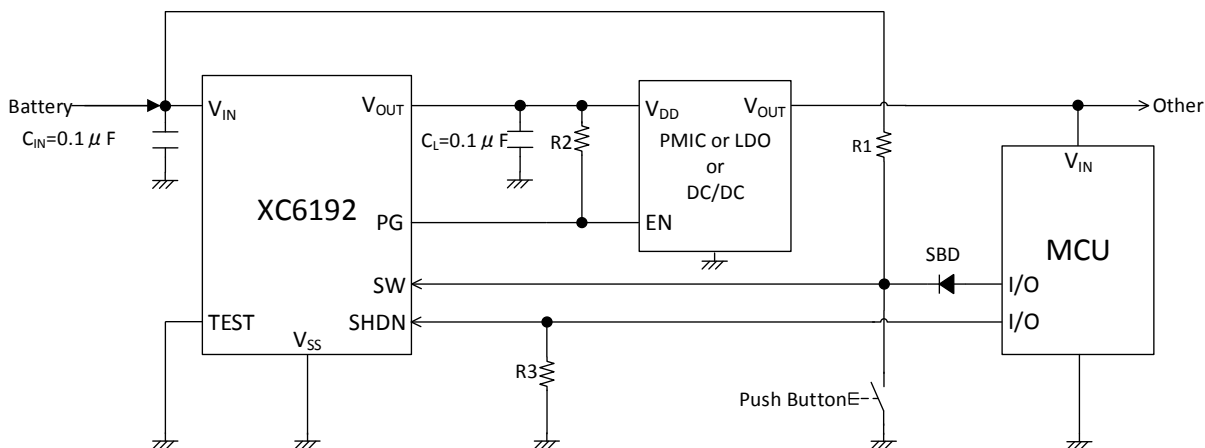
<sup>(\*)</sup>Alternating ON/OFF control on this IC is a system that alternates between  $V_{OUT}$  "H" voltage and shutdown each time "L" voltage is input the required time into the SW pin.

The B type fixes  $V_{OUT}$  at "H" voltage when "L" voltage is input the required time into the SW pin. After  $V_{OUT}$  is fixed at "H" voltage, the signal is not accepted if "L" voltage is subsequently input into the SW pin. The shutdown method is inputting "H" voltage into the SHDN pin.

As protective functions, the XC6192 series is equipped with a rush current prevention circuit and a short-circuit protection circuit.



**Fig.1 XC6192A Block diagram**



**Fig.2 XC6192 Series Typical application circuit**

## OPERATIONAL EXPLANATION (Continued)

### [Turn-On sequence: Fig.3]

On the XC6192 series, when “L” voltage is input for the duration  $T_{OND}$  into the SW pin with  $V_{OUT}$  fixed at “L” voltage, the Pch pre-driver transistor PM1 (Fig. 1) turns ON and  $V_{OUT}$  starts to rise. If the SW pin is switched from “L” voltage to “H” voltage while  $V_{OUT}$  is rising by PM1,  $V_{OUT}$  will change to the shutdown state.

When the  $V_{OUT}$  pin voltage reaches Inrush Current Limit Voltage ( $V_{RUSH}$ : refer to Electrical Characteristics), the main driver transistor PM2 (Fig. 1) turns ON, and  $V_{OUT}$  is fixed at “H” voltage even if the SW pin voltage changes to “H” voltage.

When the  $V_{OUT}$  pin voltage reaches  $V_{RUSH}$ , the Nch transistor NM1 that is connected to the PG pin (Fig. 1) turns OFF and the PG pin changes to high impedance.

These operations suppress rush current to the capacitor that is connected to the  $V_{OUT}$  pin and allow the input power supply to maintain stability. In addition, by connecting the PG pin to the Enable pin of the device that follows the XC6192 series, malfunctioning of that device is prevented. (Fig. 2)

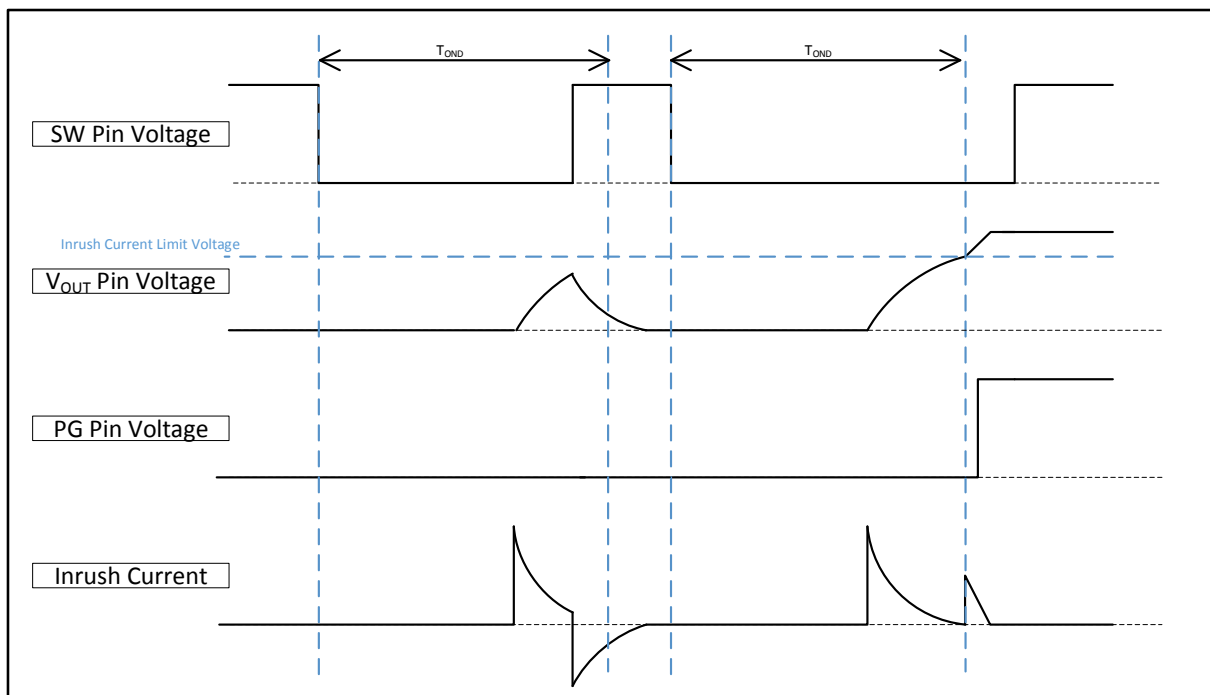


Fig.3 Turn-On sequence

## ■ OPERATIONAL EXPLANATION (Continued)

### [Shutdown sequence: Fig.4]

On the XC6192 series, when one pulse of “H” voltage (at least 1ms as a guideline) is input into the SDHN pin with  $V_{OUT}$  fixed at “H” voltage,  $V_{OUT}$  changes to the shutdown state and the IC enters the standby state.

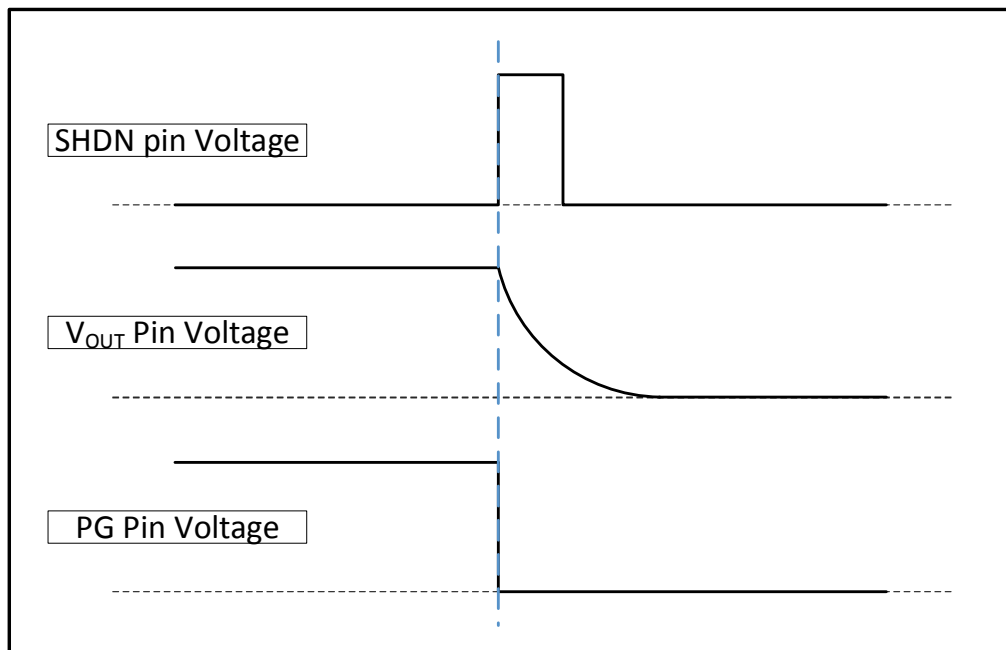


Fig.4 shutdown sequence

### [Turn-Off sequence: Fig.5]

On the A type, when “L” voltage is input into the SW pin for the duration  $T_{OFFD}$  with  $V_{OUT}$  fixed at “H” voltage,  $V_{OUT}$  shuts down. After shutdown, returning the SW pin to “H” voltage reduces the supply current to the standby current while holding the IC’s logic.

The B type does not have this function.



Fig.5 Turn-Off sequence

## OPERATIONAL EXPLANATION (Continued)

### [Operation after Turn-On: Fig. 6]

On the A type, when “L” voltage is input into the SW pin for the duration  $T_{OND}$  and  $V_{OUT}$  is fixed at “H” voltage, and then “L” voltage is input into the SW pin for the duration  $T_{OFFD}$ , the Turn-Off sequence cannot be initiated. To initiate the Turn-Off sequence, “H” voltage must be input into the SW pin, and then “L” voltage must again be input for the duration  $T_{OFFD}$ . The B type does not have a Turn-Off sequence by SW pin.

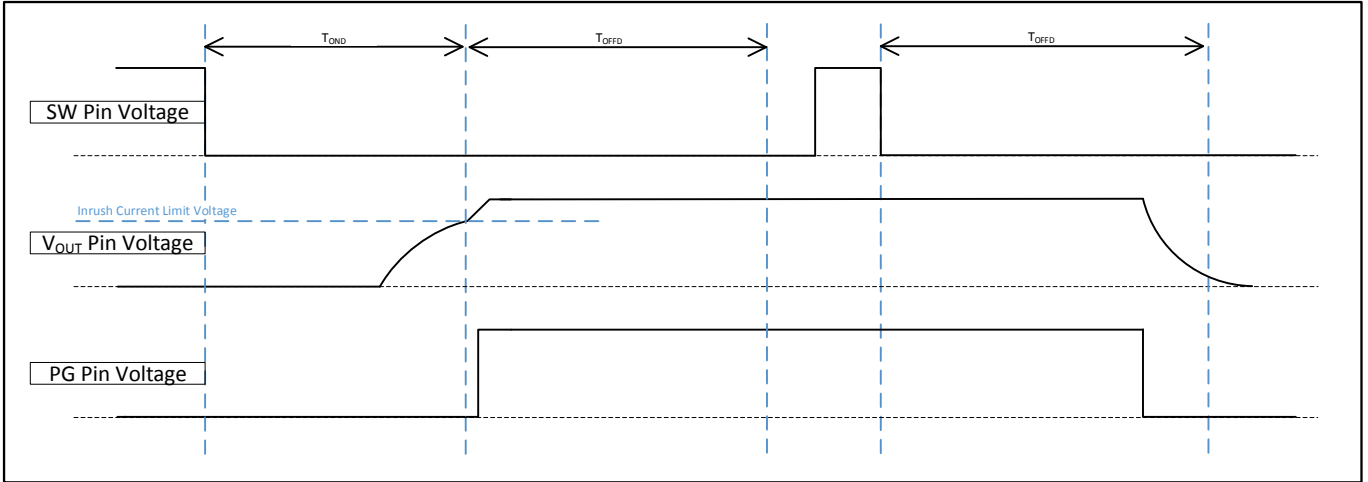


Fig.6 Operation after Turn-On.

### [Operation after Turn-Off: Fig.7]

On the A type, when “L” voltage is input into the SW pin for the duration  $T_{OFFD}$  and  $V_{OUT}$  is shut down, and then “L” voltage is input into the SW pin for the duration  $T_{OND}$ , the Turn-On sequence cannot be initiated. To initiate the Turn-On sequence, “H” voltage must be input into the SW pin, and then “L” voltage must again be input for the duration  $T_{OND}$ .

The B type does not have a Turn-Off sequence by SW pin.

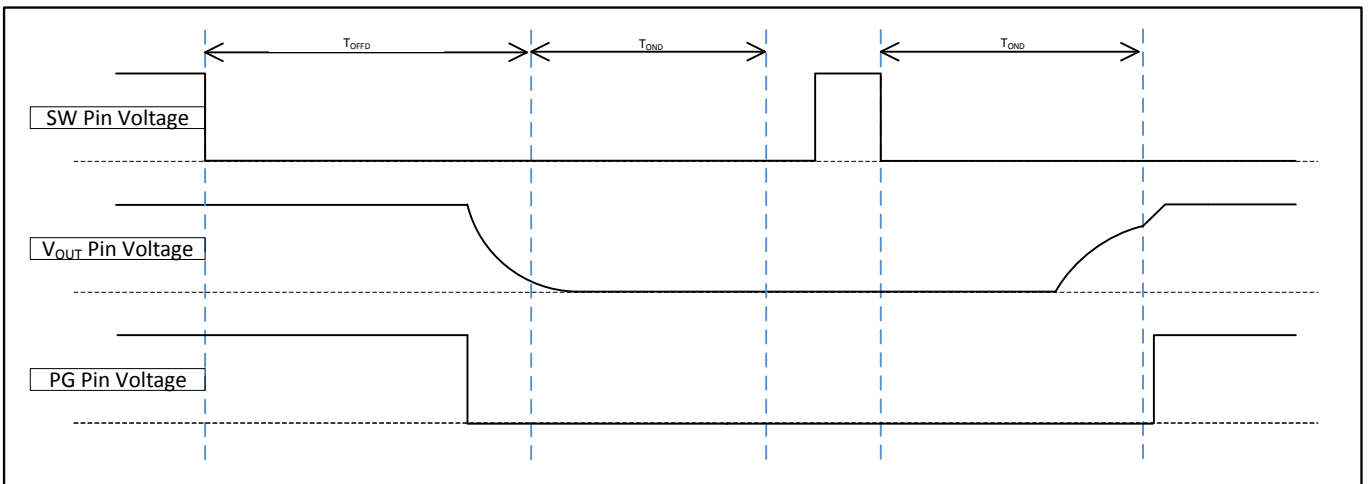


Fig.7 Operation after Turn-Off.

## OPERATIONAL EXPLANATION (Continued)

### [Operation after shutdown: Fig. 8]

On the XC6192 series, in order to initiate the Turn-On sequence after “H” voltage is input into the SHDN pin and  $V_{OUT}$  is shut down, “H” voltage must be input into the SW pin and then “L” voltage must again be input for the duration  $T_{OND}$ .

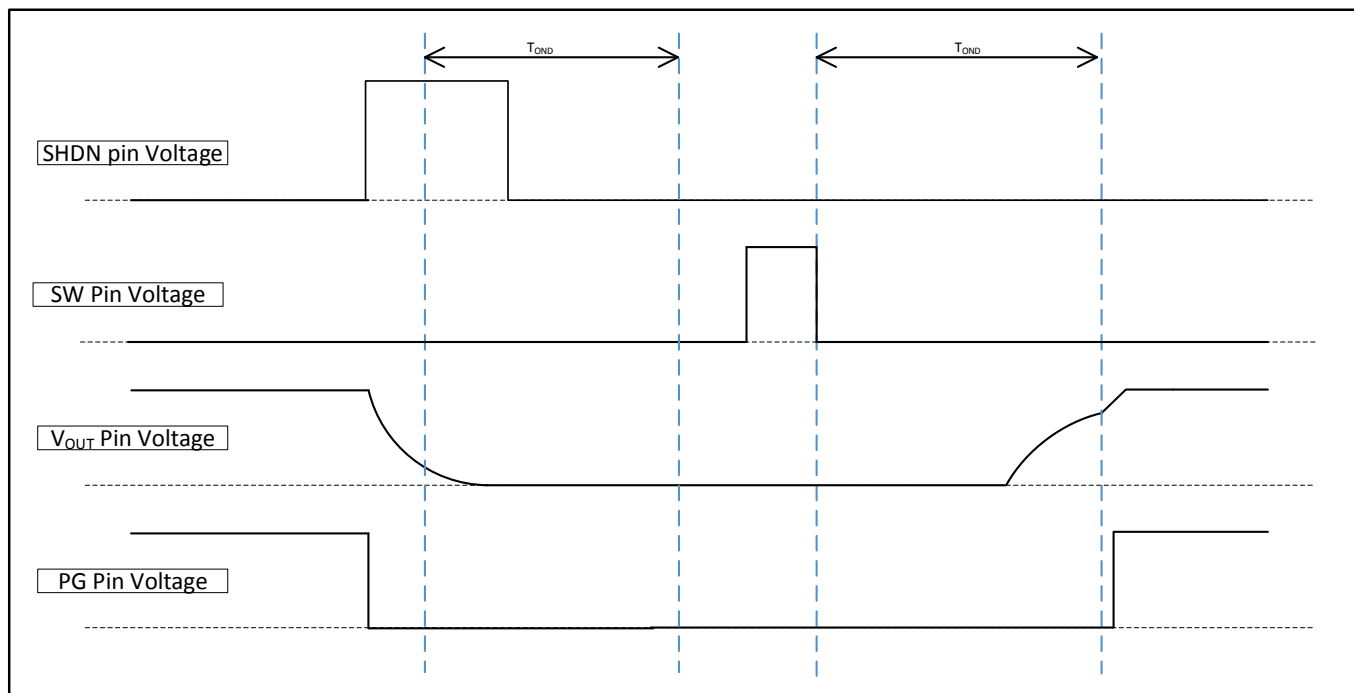


Fig.8 Operation after shutdown.

## OPERATIONAL EXPLANATION (Continued)

### [Inrush current limit and output short circuit protection: Fig.9]

The XC6192 series has a function that limits rush current and a circuit that shuts down the output when an output short-circuit is detected.

During the time until the  $V_{OUT}$  pin voltage reaches  $V_{RUSH}$  during the Turn-On sequence, only PM1 turns ON, and therefore the voltage rises through the Pre-driver On Resistor ( $R_{ONP}$ : refer to the Electrical Characteristics).

When the  $V_{OUT}$  pin voltage reaches  $V_{RUSH}$  or more, PM2 turns ON and rush current limiting is released.

The rush current at the instant when PM1 turns ON is calculated using the following equation.

$$I_{rush} = V_{IN} / R_{ONP} \text{ (A)}$$

In addition, during the time until the  $V_{OUT}$  pin voltage reaches  $V_{RUSH}$  by means of the rush current limiting function, the load current while  $V_{OUT}$  rises must be set to less than the value given by the equation below.

$$I_{start-up} < (V_{IN} - V_{RUSH (Max.)}) / R_{ONP} \text{ (A)}$$

Example:  $4.2V - 4.15V / 135 \text{ Ohms} = 0.359mA$ ;

Note this very low 0.359mA is used to charge the output capacitors. If startup load current exceeds this, the output current will pass thru PM1 only, and have a 135 ohm resistor creating a voltage drop. If it's noticed that  $V_{OUT}$  is significantly below  $V_{IN}$ , please ensure start-up load current is NOT exceeded.

If the  $V_{OUT}$  pin voltage drops below the Short Circuit Detect Voltage ( $V_{SHORT}$ : refer to the Electrical Characteristics) after the Turn-On sequence, a short-circuit is detected and PM1 and PM2 are latched in the OFF state, causing  $V_{OUT}$  to shut down. The relation between the output current ( $I_{SHORT}$ ) and  $V_{SHORT}$  when a short circuit occurs is given by the equation below.

$$I_{SHORT} = (V_{IN} - V_{SHORT}) / R_{ON} \text{ (A)}$$

The equation for the maximum output current is as follows.

$$I_{OUT (Min.)} = (V_{IN} - V_{SHORT (Max.)}) / R_{ON (Max.)} \text{ (A)}$$



**Fig.9 Output capacitor inrush current limit and output short circuit protection**

## ■ OPERATIONAL EXPLANATION (Continued)

### [SW pin]

When “L” voltage is input into the SW pin for the duration  $T_{\text{OND}}$  with the IC in the standby state, the pin fixes  $V_{\text{OUT}}$  at “H” voltage.

On the A type, when  $V_{\text{OUT}}$  is fixed at “H” voltage, “L” voltage can be input for the duration  $T_{\text{OFFD}}$  to shut down  $V_{\text{OUT}}$ .

### [SHDN pin]

This pin shuts down  $V_{\text{OUT}}$  when one pulse of “H” voltage (1ms or more as a guideline) is input into the SHDN pin with  $V_{\text{OUT}}$  fixed at “H” voltage.

### [PG pin]

NM1 and R2 (Fig. 1) are connected to the PG pin.

NM1 is an Nch MOSFET and is synchronized with the GATE signal of PM2, so NM1 turns OFF when PM2 turns ON.

### [VOUT pin]

PM1, PM2, and R1 (Fig. 1), and R2 (Fig. 1) and the CL discharge transistor NM2 (Fig. 1) are connected to the output pin.

PM1 is a Pch MOSFET, and is effective in preventing rush current.

PM2 is a Pch MOSFET and functions as the main driver.

NM2 is an Nch MOSFET, and is synchronized with the GATE signal of PM1. Therefore, when PM1 turns ON, NM2 turns OFF.

### [SW Circuit]

This circuit transmits the signal input into the SW pin to the internal circuitry.

### [SHDN Circuit]

This circuit transmits the signal input into the SHDN pin to the internal circuitry.

### [VREF & POR]

This circuit supplies the internal circuit reference voltage and is a reset circuit that is input into the logic circuitry.

### [Type Select]

This circuit selects the product type.

### [Turn-On Delay Counter]

This circuit count-controls the time until  $V_{\text{OUT}}$  is fixed at “H” voltage after “L” voltage is input into the SW pin.

During the time until the count is completed, the counter circuit can be returned to the initial state by inputting “H” voltage into the SW pin.

### [Turn-Off Delay Counter]

This circuit only operates on the A type.

When  $V_{\text{OUT}}$  is fixed at “H” voltage, this circuit count-controls the time from input of “L” voltage into the SW pin until  $V_{\text{OUT}}$  shuts down

During the time until the count is completed, the counter circuit can be returned to the initial state by inputting “H” voltage into the SW pin.

### [VREF2 & POR2]

This circuit sets the initial state of the  $V_{\text{OUT}}$  pin.

The circuit is set so that  $V_{\text{OUT}}$  is in the shutdown state when the power is turned on, so there is no need to input “H” voltage into the SHDN pin after power is supply to shut down the output.

### [Inrush Current Limit]

This circuit limits rush current until the  $V_{\text{OUT}}$  pin voltage reaches  $V_{\text{RUSH}}$ .

### [Short Circuit Protect]

This circuit outputs a shutdown signal if the  $V_{\text{OUT}}$  pin voltage reaches  $V_{\text{SHORT}}$ .

### [Gate Slope]

After the  $V_{\text{OUT}}$  pin voltage reaches  $V_{\text{RUSH}}$ , this circuit gradually changes the GATE voltage of PM2 from “H” voltage to “L” voltage.

## ■ NOTES ON USE

- 1) For temporary, transitional voltage drop or voltage rising phenomenon. The IC is liable to malfunction should the ratings be exceeded.
- 2) Please use this IC within the specified operating ranges.
- 3) In some cases, power supply noise may cause malfunctioning of the internal counter circuit. Sufficiently reinforce the  $V_{IN}$ ,  $V_{OUT}$ , and GND lines, and connect 0.1  $\mu$ F or higher capacitors near the IC between  $V_{IN}$  and GND ( $V_{SS}$ ), and between  $V_{OUT}$  and GND ( $V_{SS}$ ).
- 4) Turn-On Delay Time characteristics and Turn-Off Delay Time characteristics are increased when using a capacitor larger than 0.1  $\mu$ F for CL connected to  $V_{OUT}$  - GND ( $V_{SS}$ ). Sufficiently test actual operation before use.
- 5) When "L" level is input into the SW pin, Operation Current flows. Sufficiently test actual operation before designing peripheral circuits.
- 6) The SW pin and SHDN pin are connected to the gate of a CMOS inverter. If a voltage lower than the  $V_{IN}$  pin voltage or a voltage higher than the  $V_{SS}$  pin voltage is input into each pin, the flow-through current of the CMOS inverter may appear as supply current.
- 7) If an intermediate voltage between "L" voltage and "H" voltage is input into the SW pin or SHDN pin, starting and stopping of the IC may become unstable. Sufficiently test peripheral components and other parts to ensure that an intermediate voltage between "L" voltage and "H" voltage is not continuously input for an excessive time into the SW pin and SHDN pin.
- 8) The TEST pin must be connected to GND ( $V_{SS}$ ).
- 9) When using for an application other than a push-button application, please design the timing to include deviations and test sufficiently with the actual device before use.
- 10) The USP-8B06 is a thin surface-mount package. Therefore, distortion of the board during PCB mounting may be transmitted to the IC chip, which may affect Turn-On Delay Time characteristics and Turn-Off Delay Time characteristics. Sufficiently test actual operation before use.
- 11) Torex places an importance on improving our products and their reliability.  
We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.



## TYPICAL PERFORMANCE CHARACTERISTICS

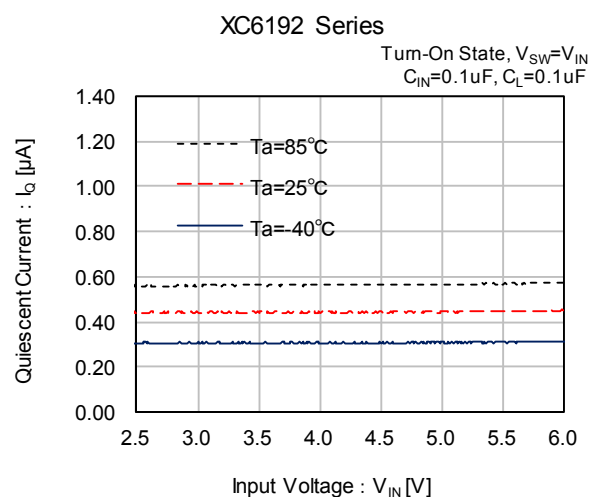
(1) Standby Current vs. Input Voltage



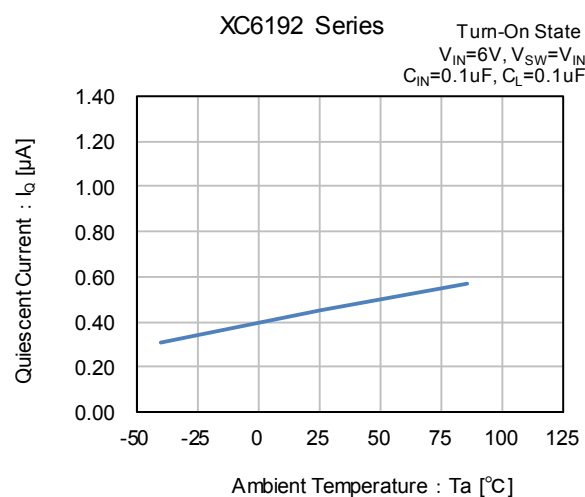
(2) Standby Current vs. Ambient Temperature



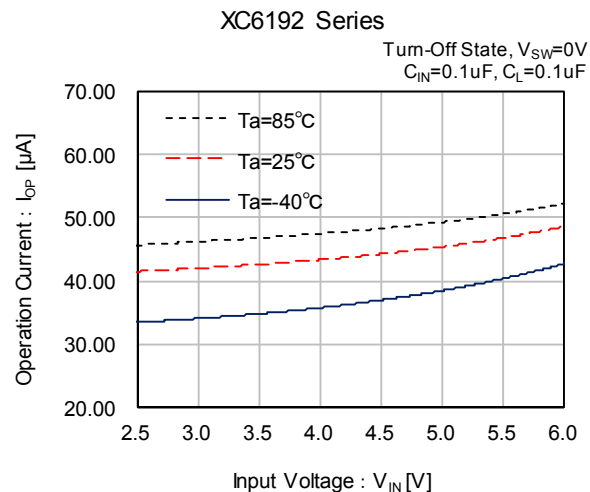
(3) Quiescent Current vs. Input Voltage



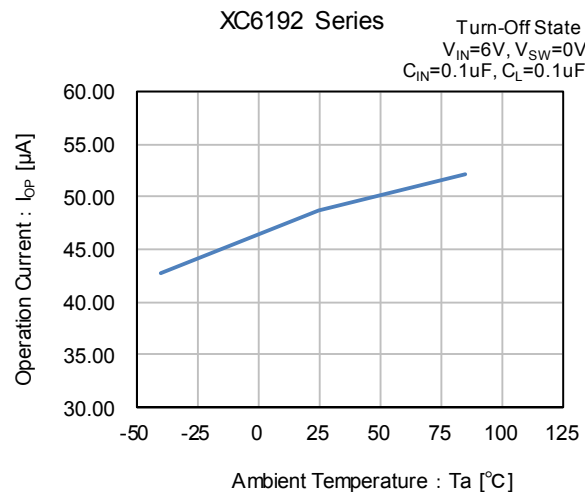
(4) Quiescent Current vs. Ambient Temperature



(5) Operation Current vs. Input Voltage

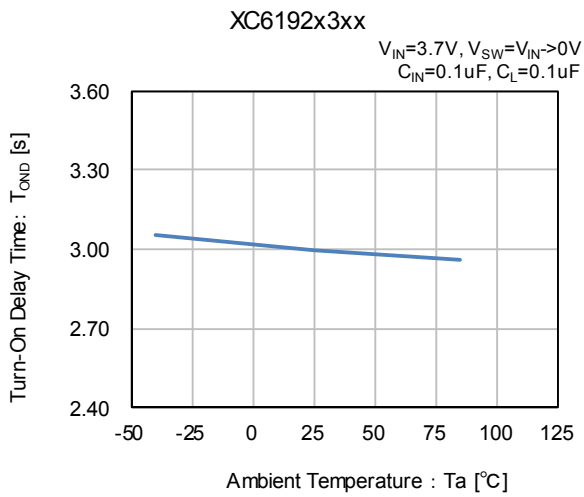
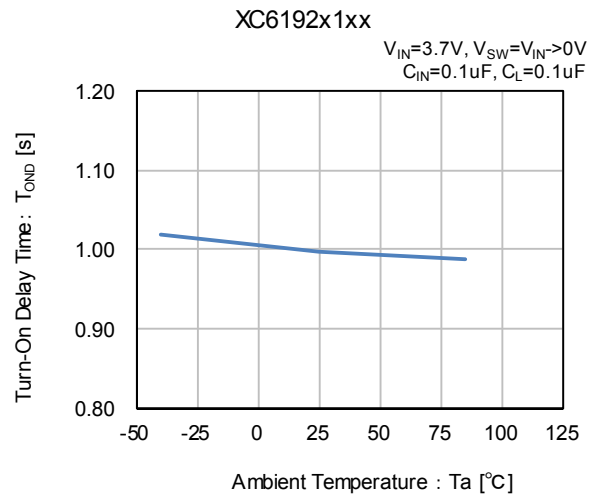
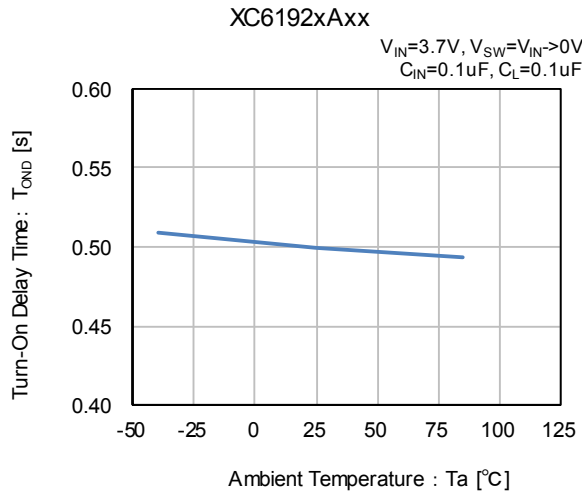


(6) Operation Current vs. Ambient Temperature



## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

### (7) Turn-On Delay Time vs. Ambient Temperature



## ■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

### (8) Turn-Off Delay Time vs. Ambient Temperature

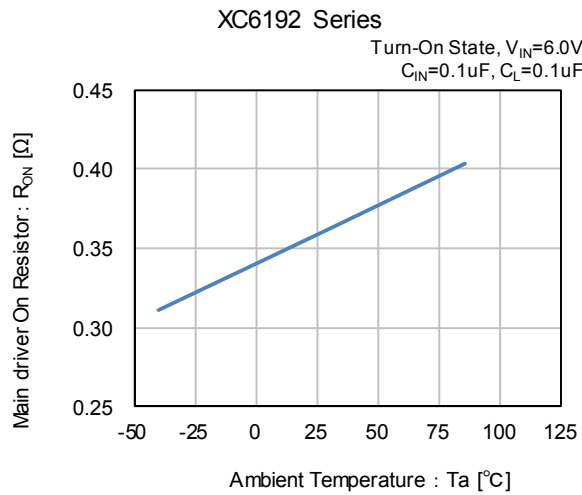
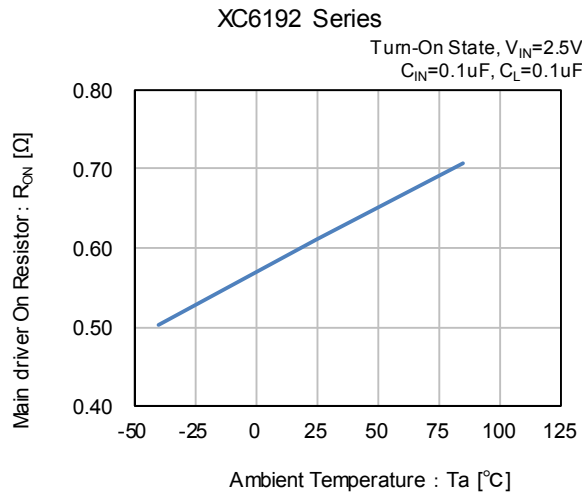


### (9) Pre-driver On Resistor vs. Ambient Temperature



## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(10) Main driver On Resistor vs. Ambient Temperature



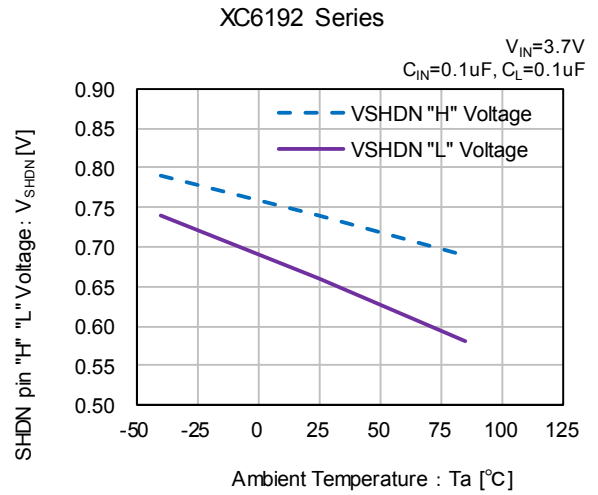
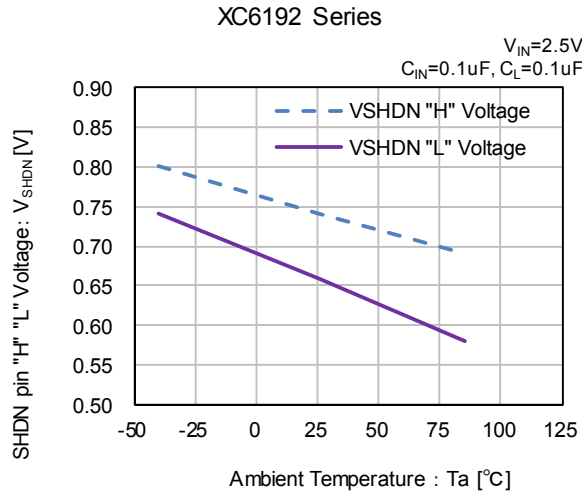
## ■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(11) SW pin "H" "L" Voltage vs. Ambient Temperature

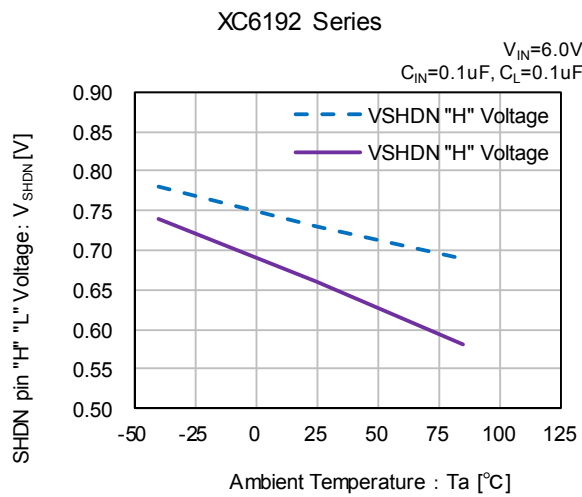


## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

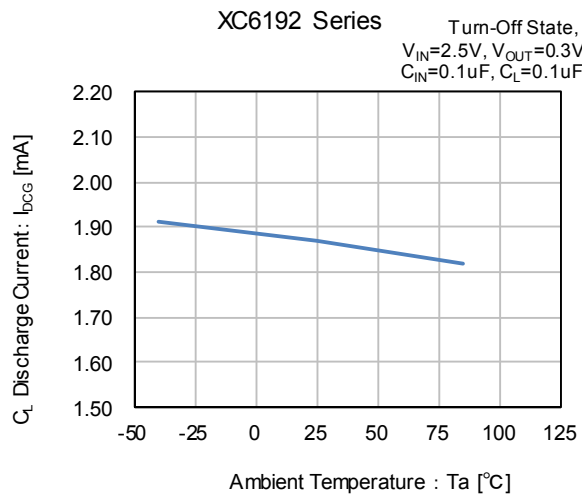
(12) SHDN pin "H" "L" Voltage vs. Ambient Temperature



(13) PG pin Output Current vs. Ambient Temperature

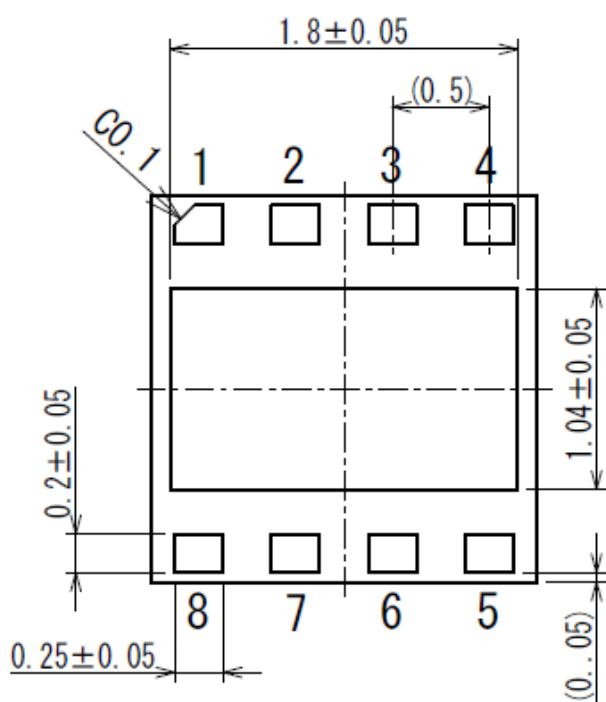


(14) C<sub>L</sub> Discharge Current vs. Ambient Temperature



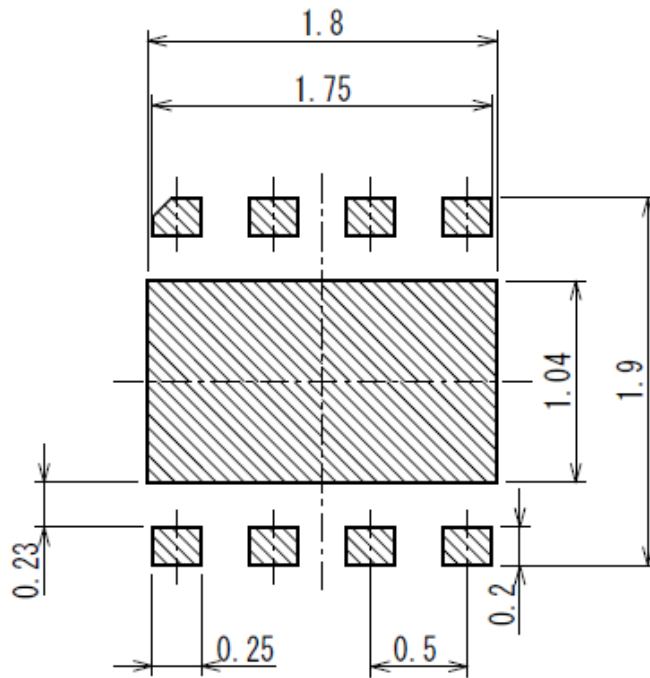
■ PACKAGING INFORMATION

● USP-8B06 (unit: mm)

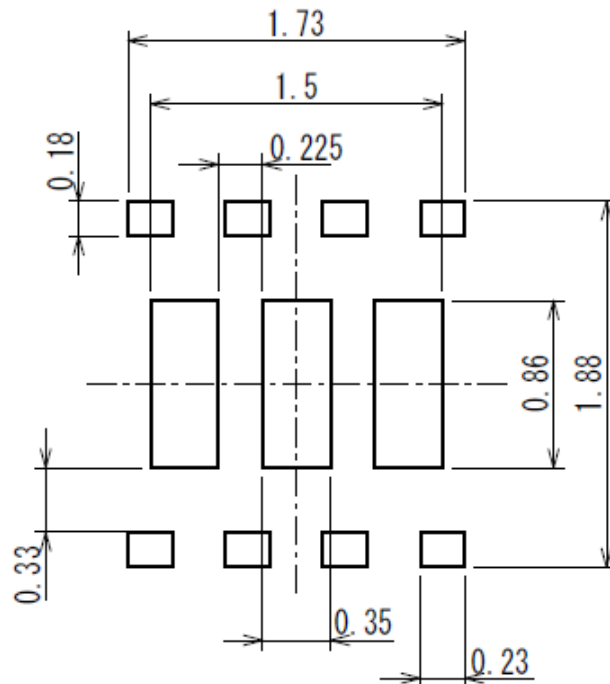


## PACKAGING INFORMATION (Continued)

- USP-8B06 Reference pattern layout (unit: mm)



- USP-8B06 Reference Metal Mask Design (unit: mm)





## ■ PACKAGING INFORMATION (Continued)

### ● USP-8B06(DAF) Power Dissipation

Power dissipation data for the USP-8B06(DAF) is shown in this page.  
The value of power dissipation varies with the mount board conditions.  
Please use this data as the reference data taken in the following condition.

#### 1. Measurement Condition

Condition: Mount on a board

Ambient: Natural convection

Soldering: Lead (Pb) free

Board: The board using 4 copper layer.

(76.2mm×114.3mm · · · Area: about 8700mm<sup>2</sup>)

Each copper layer are as follows.

1st layer: 50mm×50mm\_Connected to heat-sink.

2nd layer: 70mm×70mm\_Connected to heat-sink.

3rd layer: 70mm×70mm\_Connected to heat-sink.

4th layer: 50mm×50mm\_Connected to heat-sink.

Material: Glass Epoxy (FR-4)

Thickness: 1.6mm

Through-hole: φ0.2mm: 60 pcs

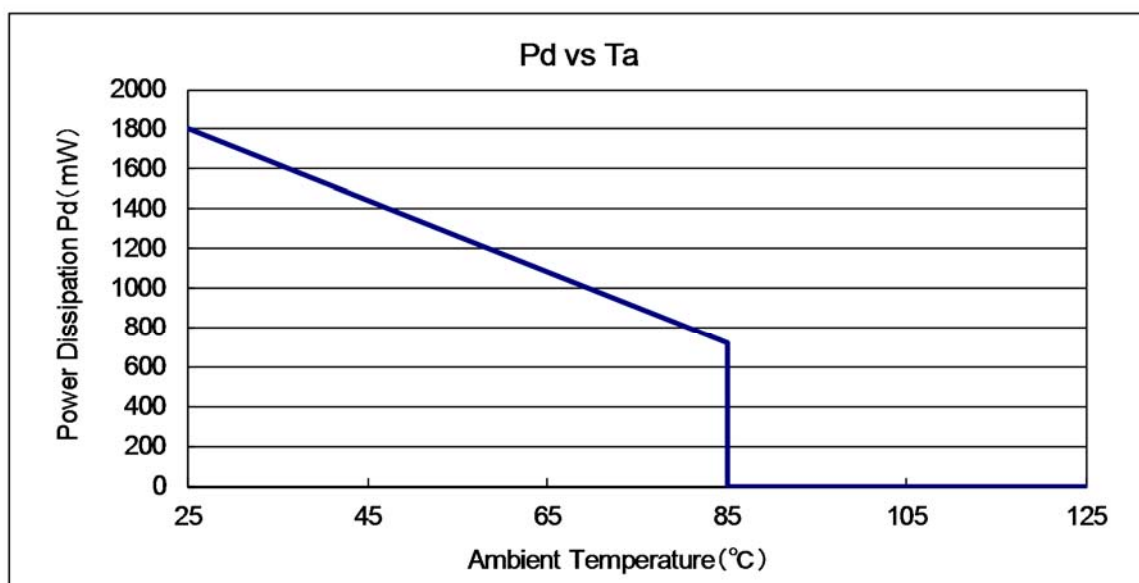


Evaluation Board (Unit: mm)

#### 2. Power Dissipation vs. Ambient Temperature

Board Mount (T<sub>j</sub> max = 125°C)

Ambient Temperature (°C)	Power Dissipation Pd (mW)	Thermal Resistance (°C/W)
25	1800	55.56
85	720	



## MARKING RULE

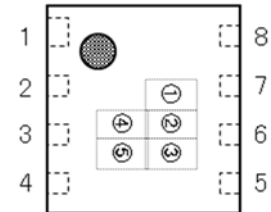
① represents product series.

MARK	PRODUCT SERIE
2	XC6192*****-G

② represents Type and Turn-On delay time.

MARK	Type	Turn-On delay time	PRODUCT SERIE
0	A	0.5s	XC6192AA****-G
1		1.0s	XC6192A1****-G
2		3.0s	XC6192A3****-G
3		5.0s	XC6192A5****-G
4	B	0.5s	XC6192BA****-G
5		1.0s	XC6192B1****-G
6		3.0s	XC6192B3****-G
7		5.0s	XC6192B5****-G

USP-8B06



③ represents Turn-Off delay time.

MARK	Turn-Off delay time	PRODUCT SERIE
A	3s	XC6192**03**-G
B	5s	XC6192**05**-G
C	10s	XC6192**10**-G
D	15s	XC6192**15**-G
0	-	XC6192B*NN**-G

④,⑤ represents production lot number

01~09, 0A~0Z, 11~9Z, A1~A9, AA~AZ, B1~ZZ in order.

(G, I, J, O, Q, W excluded)

\* No character inversion used.

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