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FAIRCHILD SEMICONDUCTOR TM				August 1998		
FDC	6325L					
	grated Load	d Switch				
Genera	l Description			Features		
General Description This device is particularly suited for compact power management in portable electronic equipment where 2.5V to 8V input and 1.8A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOT [™] -6 package.			 V_{DROP}=0.2V @ V_{IN}=5V, I_L=1.5A. R_(ON) = 0.13Ω V_{DROP}=0.2V @ V_{IN}=3.3V, I_L=1.2A. R_(ON) = 0.16Ω V_{DROP}=0.2V @ V_{IN}=2.5V, I_L=1A. R_(ON) = 0.18Ω. SuperSOTTM-6 package design using copper lead frame for superior thermal and electrical capabilities 			
	,",	190				
so	г-23 s	uperSOT [™] -6	SuperSOT [™] -8	SO-8	SOT-223	SOIC-16
	37	5	Vin,R1 4		Vout,C1	
-	_{pin 1} rSOT [™] -6		ON/OFF 5	a1 a2 a1 a1 a1 a1 a1 a1 a1 a1 a1 a1	Vout,C1 IN O Vout,C1 ON/OFF C	• • • • • • • • • • • • • • • • • • •
bsolu	_{pin 1} rSOT [™] -6		0N/0FF 5	a1 a2 a1 a1 a1 a1 a1 a1 a1 a1 a1 a1	Vout,C1 IN O Vout,C1 ON/OFF C	
Absolu ymbol	rSOT [™] -6	Ratings T	ON/OFF 5	a1 a2 a1 a1 a1 a1 a1 a1 a1 a1 a1 a1	Vout,C1 IN O ON/OFF C	
-	rSOT [™] -6	n Ratings T	ON/OFF 5	a1 a2 a1 a1 a1 a1 a1 a1 a1 a1 a1 a1	Vout,C1 IN O ON/OFF C R2 FDC6325L	vo out
Absolı ymbol	rSOT [™] -6 ute Maximum Parameter Input Voltage Rar	nge ange	ON/OFF 5	a1 a2 a1 a1 a1 a1 a1 a1 a1 a1 a1 a1	Vout,C1 IN ON/OFF C R2 FDC6325L 2.5 - 8	vvocepo ou1
	pin 1 rSOT ™-6 ute Maximum Parameter Input Voltage Rar On/Off Voltage R	nge ange - Con	ON/OFF 5 R1,C1 6 See Ap A = 25°C unless otherwise	a1 a2 a1 a1 a1 a1 a1 a1 a1 a1 a1 a1	Vout,C1 Vout,C1 IN O ON/OFF C R2 FDC6325L 2.5 - 8 1.5 - 8	Vortex-
	pin 1 rSOT ™-6 te Maximum Parameter Input Voltage Rar On/Off Voltage R Load Current Maximum Power	n Ratings T nge ange - Con - Pe Dissipation	$O N/OFF 5$ $R1,C1 6$ See Ap $A = 25^{\circ}C \text{ unless otherwise}$ tinuous (Note 1) ulsed (Note 1 & 3) (Note 2)	a1 a2 a1 a1 a1 a1 a1 a1 a1 a1 a1 a1	Vout,C1 Vout,C1 IN O ON/OFF o R2 FDC6325L 2.5 - 8 1.5 - 8 1.8 5 0.7	Vortex-
	pin 1 rSOT ™-6 te Maximum Parameter Input Voltage Rar On/Off Voltage R Load Current	n Ratings T nge ange - Con - Pe Dissipation	$O N/OFF 5$ $R1,C1 6$ See Ap $A = 25^{\circ}C \text{ unless otherwise}$ tinuous (Note 1) ulsed (Note 1 & 3) (Note 2)	a1 a2 a1 a1 a1 a1 a1 a1 a1 a1 a1 a1	Vout,C1 IN O N/OFF C R2 FDC6325L 2.5 - 8 1.5 - 8 1.8 5	Units V V V V A
	pin 1 rSOT [™] -6 te Maximum Parameter Input Voltage Rai On/Off Voltage R Load Current Maximum Power Operating and St	Ratings T nge ange - Con - Pu Dissipation orage Temperatur harge Rating MIL-	$O N/OFF 5$ $R1,C1 6$ See Ap $A = 25^{\circ}C \text{ unless otherwise}$ tinuous (Note 1) ulsed (Note 1 & 3) (Note 2)	noted	Vout,C1 Vout,C1 IN O ON/OFF o R2 FDC6325L 2.5 - 8 1.5 - 8 1.8 5 0.7	Units
	pin 1 rSOT [™] -6 te Maximum Parameter Input Voltage Rai On/Off Voltage R Load Current Maximum Power Operating and St Electrostatic Disc	A Ratings T nge ange - Con - Pu Dissipation orage Temperatur harge Rating MIL- 00Ohm)	o N/O FF 5 R1, C1 6 See Ap tinuous (Note 1) ulsed (Note 1 & 3) (Note 2) re Range	noted	Vout,C1 Vout,C1 IN O ON/OFF C R2 FDC6325L 2.5 - 8 1.5 - 8 1.5 - 8 1.8 5 0.7 -55 to 150	✓ ✓ ○
	pin 1 rSOT ™-6 te Maximum Parameter Input Voltage Rat On/Off Voltage Rat On/Off Voltage R Load Current Maximum Power Operating and St Electrostatic Disc Model (100pf/150	A Ratings T nge ange - Con - Pe Dissipation orage Temperatur harge Rating MIL- 00hm) ICS	ON/OFF 5 R1,C1 6 See Ap See Ap tinuous (Note 1) ulsed (Note 1 & 3) (Note 2) re Range -STD-883D Human Body	noted	Vout,C1 Vout,C1 IN O ON/OFF C R2 FDC6325L 2.5 - 8 1.5 - 8 1.5 - 8 1.8 5 0.7 -55 to 150	✓ ✓ ○

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FDC6325L Rev.D1

Electrical Characteristics (T _A = 25°C unless otherwise noted)							
Symbol	Parameter	Conditions	Min	Тур	Max	Units	
OFF CHA	RACTERISTICS						
I _{FL}	Forward Leakage Current	$V_{IN} = 8 V, V_{ONOFF} = 0 V$			1	μA	
ON CHAR	ACTERISTICS (Note 3)						
V _{DROP}	Conduction Voltage Drop	$V_{IN} = 5 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}, I_{L} = 1.5 \text{ A}$		0.15	0.2	V	
		$V_{IN} = 3.3 \text{ V}, V_{ONOFF} = 3.3 \text{ V}, I_L = 1.2 \text{ A}$		0.145	0.2		
		$V_{IN} = 2.5 \text{ V}, V_{ONOFF} = 3.3 \text{ V}, I_{L} = 1 \text{ A}$		0.13	0.2]	
R _(ON)	Q ₂ - Static On-Resistance	$V_{GS} = -5 \text{ V}, \ \text{I}_{\text{D}} = -1.8 \text{ A}$		0.115	0.13	Ω	
		$V_{GS} = -3.3 \text{ V}, I_{D} = -1.6 \text{ A}$		0.13	0.16		
		$V_{GS} = -2.5 \text{ V}, I_{D} = -1.5 \text{ A}$		0.155	0.18		
IL.	Load Current	$V_{\text{DROP}} = 0.13 \text{ V}, \text{ V}_{\text{IN}} = 5 \text{ V}, \text{ V}_{\text{ONOFF}} = 3.3 \text{ V}$	1			А	
		$V_{\text{DROP}} = 0.16 \text{ V}, \text{ V}_{\text{IN}} = 3.3 \text{ V}, \text{ V}_{\text{ONOFF}} = 3.3 \text{ V}$	1]	
1		$V_{\text{DROP}} = 0.2 \text{ V}, V_{\text{IN}} = 2.5 \text{V}, V_{\text{ONOFF}} = 3.3 \text{ V}$	1]	

Notes:

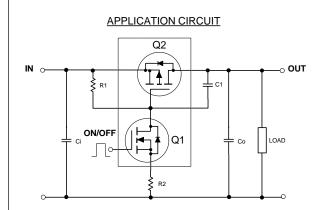
1. V_{IN} =8V, $V_{ON/OFF}$ =8V, T_A =25°C

2. R_{eJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface

of the drain pins. $R_{_{\theta JC}}$ is guaranteed by design while $R_{_{\theta CA}}$ is determined by the user's board design.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2.0%.

FDC6325L Load Switch Application

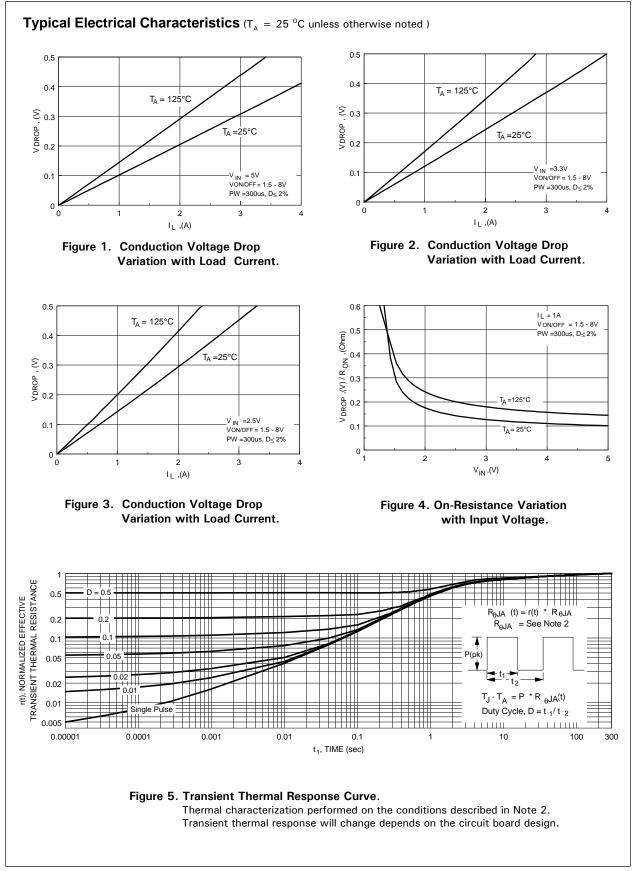


External Component Recommendation

For Co £ 1uF applications:

First select R2, 100 - 1kW, for Slew Rate control. C1 \pm 1000pF can be added in addition to R2 for further In-rush current control.

Then select R1 such that R1/R2 ratio maintains between 10 - 100. R1 is required to turn Q2 off. For SPICE simulation, users can download a "FDC6325L.MOD" Spice model from Fairchild Web Site at www.fairchildsemi.com



FDC6325L Rev.D1

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Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж: moschip.ru moschip.ru_4

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