Lattice Semiconductor Corporation

IP Suites for LatticeECP3™

Express Yourself with Bundles of Logic

Lattice IP Suites are economical packages of IP cores that allow designers to conveniently obtain critical functions for their applications. The suites offer ready-made building-blocks for solving a variety of problems, such as high-speed data transfer, Ethernet networking, high-speed memory interfaces, signal processing, video pixel processing, etc. These comprehensive IP Suites empower design engineers to quickly build leading-edge wireline, wireless, embedded, industrial, compute, video and display, and consumer systems.

IP Suite	Description	Included LatticeCORE™ IP	Part #
Value	DDR3 memory interfaces, Tri-speed Gigabit Ethernet MAC and common DSP Functions.	• DDR3/DDR2/DDR Memory Controller • FFT Compiler • FIR Filter Generator • Triple Speed 10/100/1G Ethernet MAC	DS-VAL-ST-U1
PCI Express	High-speed data transfer in industrial and computing systems using PCI Express.	• PCI Express Endpoint x1 / x4 • PCI Express Root Complex Lite x1 / x4 • PCI Master/Target 32-bit / 64-bit • PCI Target 32-bit / 64-bit • Scatter Gather DMA	DS-PCIE-ST-U1
Ethernet	Comprehensive Ethernet networking solutions.	• 10Gb+ Ethernet MAC • Scatter Gather DMA • SGMII & Gigabit Ethernet MAC • Triple Speed 10/100/1G Ethernet MAC • XAUI	DS-ETH-ST-U1
Digital Signal Processing	Wide range of filtering, error-correction, and domain conversion functions.	• Advanced FIR Filter • Block Convolutional Encoder • Block Viterbi Decoder • Cascaded Integrated Comb Filter - CIC • CORDIC • Correlator • Distributed Arithmetic FIR (DA-FIR) Filter • Dynamic Block Reed Solomon Decoder / Encoder • FFT Compiler • FIR Filter Generator • Interleaver / De-interleaver • Numerically Controlled Oscillator • Turbo Decoder / Encoder	DS-DSP-ST-U1
Video & Display	Popular solutions for video and display applications.	• 2D FIR Filter • Color Space Converter • Edge Detector • Gamma Corrector • Median Filter • Tri-rate SDI PHY • Scaler	DS-VDS-ST-U1

* Limited quantity and limited time offer. Offer valid until availability of limited promotional licenses are depleted or July 31, 2011.

LatticeCORE IP

LatticeCORE IP cores are configurable design blocks that implement popular industry-standard functions. The IP blocks can be easily combined with your proprietary logic to build differentiated products. Many of the Lattice IP Cores use an industry standard WISHBONE bus for plug-and-play interoperability in designs. These proven IP Cores are optimized for Lattice device architectures, resulting in fast and logic efficient solutions.

LatticeCORE IP Key Features	Benefits
Logic Efficient	Optimal use of device resources.
High Performance	Designed to extract the highest performance from Lattice architectures.
Proven Quality	Pre-tested IP solutions for challenging design problems.
Plug and Play	LatticeCORE IP cores use industry-standard WISHBONE bus for plug and play interoperability.
Click-thru-License	One convenient click-through license for all IP in an application domain.
Economical Packages	IP requirements are satisfied with one convenient, economical package.

Try Before You Buy

Design engineers can evaluate all cores within an IP Suite using the IPexpress™ tool within Lattice Diamond™ design software. IPexpress allows you to seamlessly access the latest IP cores from the Lattice website, and customize, integrate, and test the IP in your design before purchasing. You can test your design for a limited time on hardware (usually 30 minutes) after which you have to re-program the silicon device. For permanent configuration of hardware, purchase an annual node-locked IP Suite license as described in the next section. The node-locked license can be used on multiple designs or projects over a one year period.

Pricing and Availability

Lattice is offering a limited quantity of specially priced IP Suites to encourage new, qualified design engineers to build their next generation systems with LatticeECP3 FPGAs. The LatticeECP3 FPGA is the best-in-class FPGA in the industry, with high-caliber SERDES, full-featured DSP blocks, and state-of-the-art DDR3 memory support. The limited number of IP Suite licenses are available for a promotional first year subscription of only $99 each. Lattice IP Suites are available for immediate ordering through select authorized Lattice distributors listed at www.latticesemi.com/sales.

Learn More

For more information about how Lattice IP Cores can bring value to your design projects, please visit www.latticesemi.com/IPSuites or contact your local Lattice sales representative.

Quick Links

LATTICECORE IP
www.latticesemi.com/IPSuites

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www.latticesemi.com/ecp3

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Данный компонент на территории Российской Федерации

Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

http://moschip.ru/get-element

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное   производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

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