

# 74LV4060-Q100

14-stage binary ripple counter with oscillator

Rev. 1 — 25 July 2014

Product data sheet

## 1. General description

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The 74LV4060-Q100 is a low-voltage Si-gate CMOS device and is pin and function compatible with the 74HC4060-Q100; 74HCT4060-Q100.

The 74LV4060-Q100 is a 14-stage ripple-carry counter/divider and oscillator with three oscillator terminals (RS, RTC and CTC). It has ten buffered outputs (Q3 to Q9 and Q11 to Q13) and an overriding asynchronous master reset (MR). The oscillator configuration allows design of either RC or crystal oscillator circuits. The oscillator can be replaced by an external clock signal at input RS. In this case, keep the oscillator pins (RTC and CTC) floating.

The counter advances on the negative-going transition of RS. A HIGH-level on MR resets the counter (Q3 to Q9 and Q11 to Q13 = LOW), independent of the other input conditions.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

## 2. Features and benefits

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- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
  - ◆ Specified from  $-40\text{ }^{\circ}\text{C}$  to  $+85\text{ }^{\circ}\text{C}$  and from  $-40\text{ }^{\circ}\text{C}$  to  $+125\text{ }^{\circ}\text{C}$
- Wide operating voltage range from 1.0 V to 5.5 V
- Optimized for low voltage applications from 1.0 V to 3.6 V
- Accepts TTL input levels between  $V_{CC} = 2.7\text{ V}$  and  $V_{CC} = 3.6\text{ V}$
- Typical  $V_{OLP}$  (output ground bounce)  $< 0.8\text{ V}$  at  $V_{CC} = 3.3\text{ V}$ ;  $T_{amb} = 25\text{ }^{\circ}\text{C}$
- Typical  $V_{OHV}$  (output  $V_{OH}$  undershoot)  $> 2\text{ V}$  at  $V_{CC} = 3.3\text{ V}$ ;  $T_{amb} = 25\text{ }^{\circ}\text{C}$
- All active components on chip
- RC or crystal oscillator configuration
- Complies with JEDEC standard no. 7A
- ESD protection:
  - ◆ MIL-STD-883, method 3015 exceeds 2000 V
  - ◆ HBM JESD22-A114F exceeds 2000 V
  - ◆ MM JESD22-A115-A exceeds 200 V ( $C = 200\text{ pF}$ ,  $R = 0\text{ }\Omega$ )

## 3. Applications

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- Control counters
- Timers
- Frequency dividers
- Time-delay circuits



### 4. Ordering information

Table 1. Ordering information

Type number	Package			Version
	Temperature range	Name	Description	
74LV4060D-Q100	-40 °C to +125 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1
74LV4060PW-Q100	-40 °C to +125 °C	TSSOP16	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	SOT403-1

### 5. Functional diagram

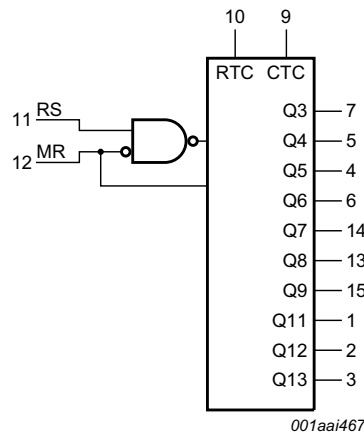


Fig 1. Logic symbol

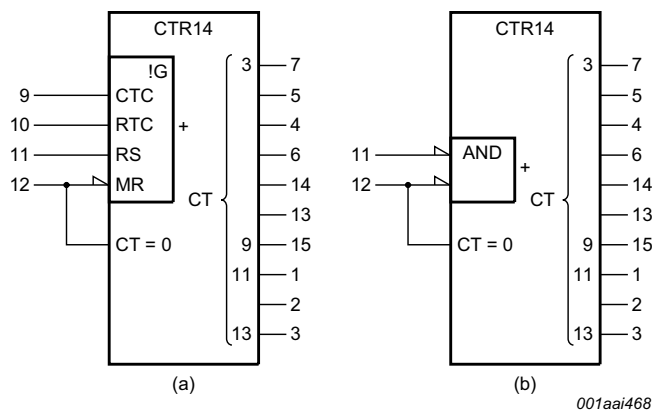


Fig 2. IEC logic symbol

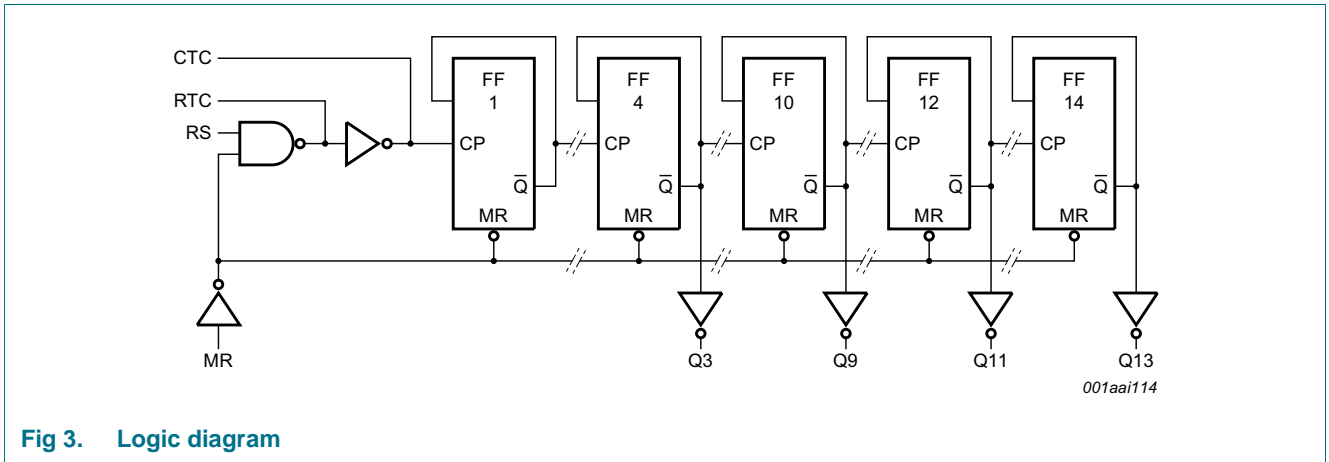


Fig 3. Logic diagram

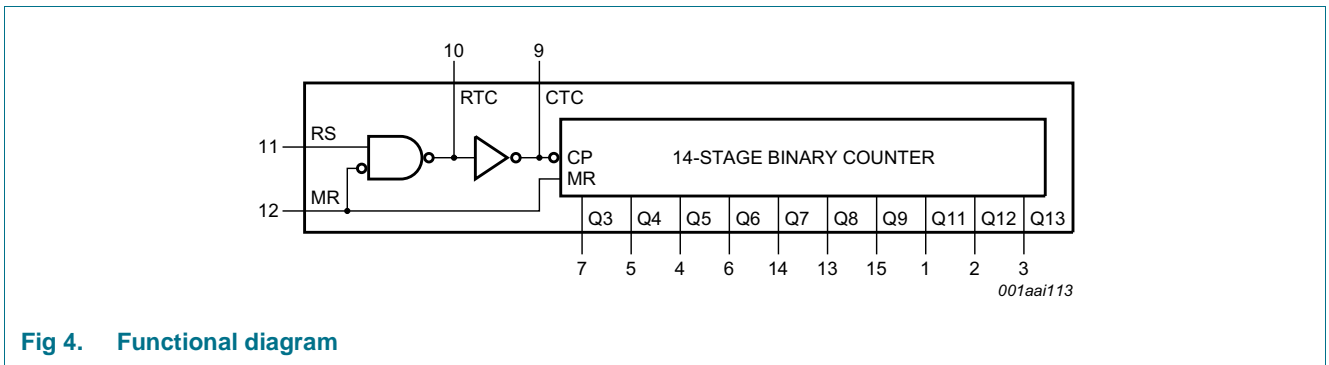


Fig 4. Functional diagram

## 6. Pinning information

### 6.1 Pinning

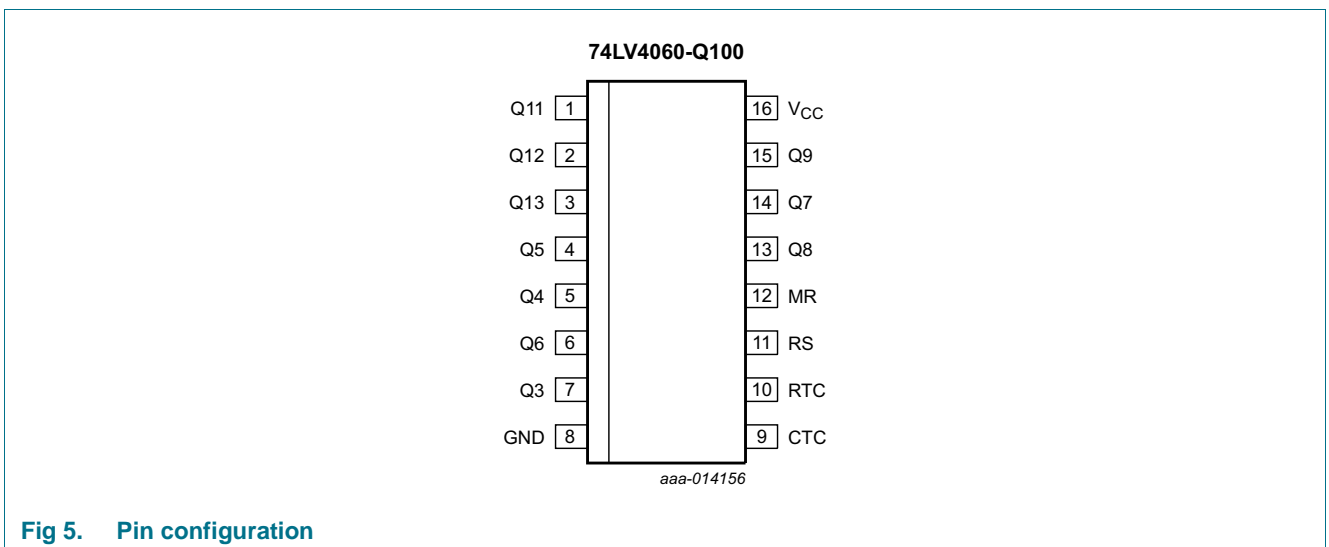


Fig 5. Pin configuration

### 6.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
Q11 to Q13	1, 2, 3	counter output
Q3 to Q9	7, 5, 4, 6, 14, 13, 15	counter output
GND	8	ground (0 V)
CTC	9	external capacitor connection
RTC	10	external resistor connection
RS	11	clock input/oscillator pin
MR	12	master reset
V <sub>CC</sub>	16	supply voltage

### 7. Functional description

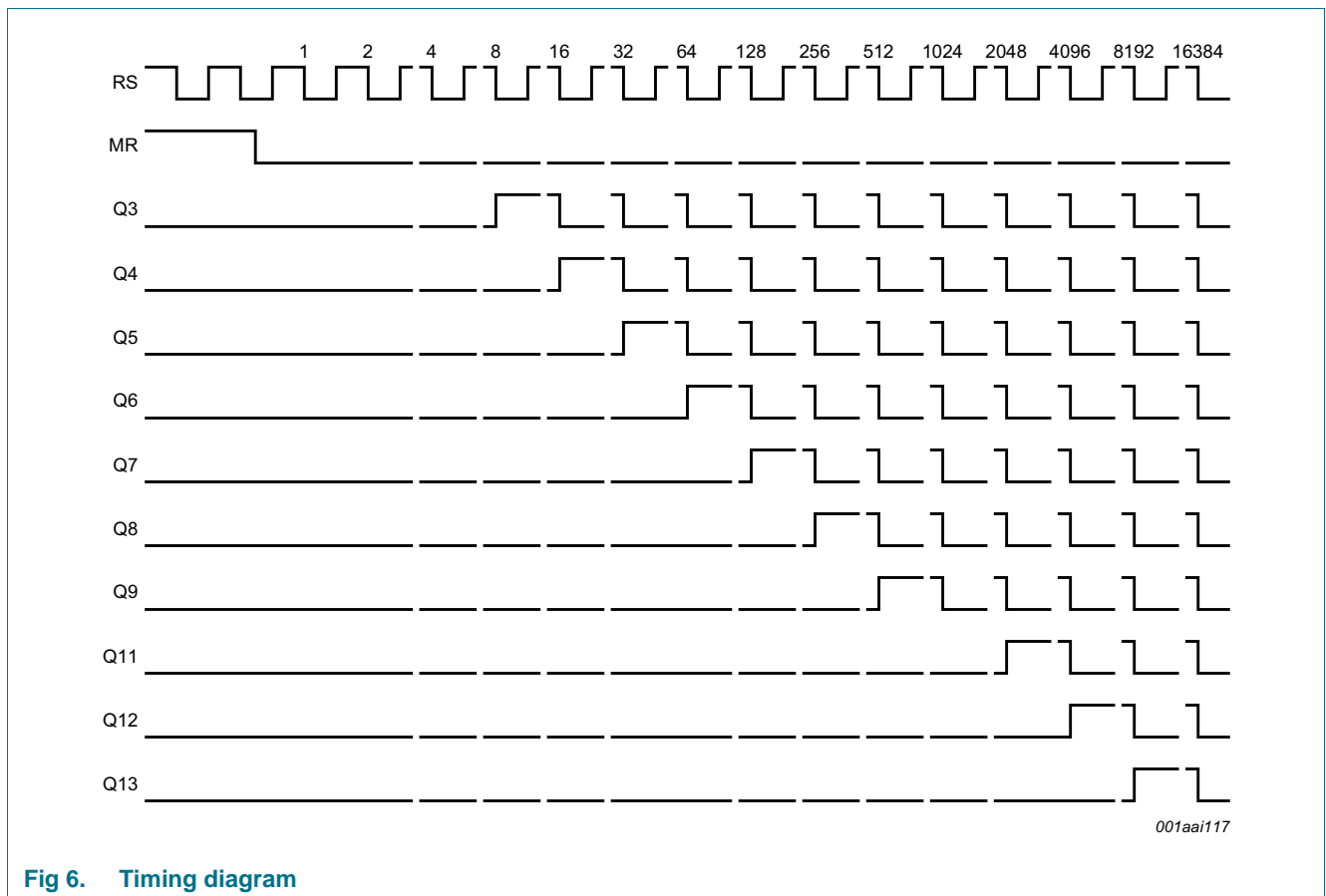


Fig 6. Timing diagram

## 8. Limiting values

**Table 3. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		-0.5	+7.0	V
$I_{IK}$	input clamping current	$V_I < -0.5\text{ V}$ or $V_I > V_{CC} + 0.5\text{ V}$ [1]	-	±20	mA
$I_{OK}$	output clamping current	$V_O < -0.5\text{ V}$ or $V_O > V_{CC} + 0.5\text{ V}$ [1]	-	±50	mA
$I_O$	output current	$-0.5\text{ V} < V_O < V_{CC} + 0.5\text{ V}$	-	±25	mA
$I_{CC}$	supply current		-	50	mA
$I_{GND}$	ground current		-50	-	mA
$T_{stg}$	storage temperature		-65	+150	°C
$P_{tot}$	total power dissipation	$T_{amb} = -40\text{ °C}$ to $+125\text{ °C}$			
		SO16 package [2]	-	500	mW
		TSSOP16 package [3]	-	400	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2]  $P_{tot}$  derates linearly with 8 mW/K above 70 °C.

[3]  $P_{tot}$  derates linearly with 5.5 mW/K above 60 °C.

## 9. Recommended operating conditions

**Table 4. Recommended operating conditions**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{CC}$	supply voltage	[1]	1.0	3.3	5.5	V
$V_I$	input voltage		0	-	$V_{CC}$	V
$V_O$	output voltage		0	-	$V_{CC}$	V
$T_{amb}$	ambient temperature	in free air	-40	-	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 1.0\text{ V}$ to $2.0\text{ V}$	-	-	500	ns/V
		$V_{CC} = 2.0\text{ V}$ to $2.7\text{ V}$	-	-	200	ns/V
		$V_{CC} = 2.7\text{ V}$ to $3.6\text{ V}$	-	-	100	ns/V
		$V_{CC} = 3.6\text{ V}$ to $5.5\text{ V}$	-	-	50	ns/V

[1] The 74LV4060-Q100 is guaranteed to function down to  $V_{CC} = 1.0\text{ V}$  (input levels GND or  $V_{CC}$ ); DC characteristics are guaranteed from  $V_{CC} = 1.2\text{ V}$  to  $V_{CC} = 5.5\text{ V}$ .

### 10. Static characteristics

**Table 5. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ <sup>[1]</sup>	Max	Min	Max	
V <sub>IH</sub>	HIGH-level input voltage	MR input						
		V <sub>CC</sub> = 1.2 V	0.9	-	-	0.9	-	V
		V <sub>CC</sub> = 2.0 V	1.4	-	-	1.4	-	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	2.0	-	-	2.0	-	V
		V <sub>CC</sub> = 4.5 V to 5.5 V	0.7V <sub>CC</sub>	-	-	0.7V <sub>CC</sub>	-	V
		RS input						
		V <sub>CC</sub> = 1.2 V	1.0	-	-	1.0	-	V
		V <sub>CC</sub> = 2.0 V	1.6	-	-	1.6	-	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	2.4	-	-	2.4	-	V
V <sub>CC</sub> = 4.5 V to 5.5 V	0.8V <sub>CC</sub>	-	-	0.8V <sub>CC</sub>	-	V		
V <sub>IL</sub>	LOW-level input voltage	MR input						
		V <sub>CC</sub> = 1.2 V	-	-	0.3	-	0.3	V
		V <sub>CC</sub> = 2.0 V	-	-	0.6	-	0.6	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	0.8	-	0.8	V
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	-	0.3V <sub>CC</sub>	-	0.3V <sub>CC</sub>	V
		RS input						
		V <sub>CC</sub> = 1.2 V	-	-	0.2	-	0.2	V
		V <sub>CC</sub> = 2.0 V	-	-	0.4	-	0.4	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	0.5	-	0.5	V
V <sub>CC</sub> = 4.5 V to 5.5 V	-	-	0.2V <sub>CC</sub>	-	0.2V <sub>CC</sub>	V		
V <sub>OH</sub>	HIGH-level output voltage	RTC output; RS = MR = GND						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -3.4 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -3.4 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -3.4 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -3.4 mA	2.40	2.82	-	2.20	-	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -3.4 mA	-	-	-	-	-	V
		RTC output; RS = MR = V <sub>CC</sub>						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -0.8 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -0.8 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -0.8 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -0.8 mA	2.40	2.82	-	2.20	-	V
V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -0.8 mA	-	-	-	-	-	V		

**Table 5. Static characteristics ...continued**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ <sup>[1]</sup>	Max	Min	Max	
V <sub>OH</sub>	HIGH-level output voltage	RTC output; RS = MR = GND						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -100 μA	1.0	1.2	-	1.0	-	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -100 μA	1.8	2.0	-	1.8	-	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -100 μA	2.8	3.0	-	2.8	-	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		RTC output; RS = MR = V <sub>CC</sub>						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -100 μA	1.0	1.2	-	1.0	-	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -100 μA	1.8	2.0	-	1.8	-	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -100 μA	2.8	3.0	-	2.8	-	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		CTC output; RS = V <sub>IH</sub> and MR = V <sub>IL</sub>						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -3.8 mA	-	1.2	-	-	-	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -3.8 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -3.8 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -3.8 mA	2.40	2.82	-	2.20	-	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -3.8 mA	-	-	-	-	-	V
		except RTC output; V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -100 μA	1.0	1.2	-	1.0	-	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -100 μA	1.8	2.0	-	1.8	-	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -100 μA	2.8	3.0	-	2.8	-	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
except RTC and CTC outputs; V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>								
V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -6 mA	-	-	-	-	-	V		
V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -6 mA	-	-	-	-	-	V		
V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -6 mA	-	-	-	-	-	V		
V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -6 mA	2.40	2.82	-	2.20	-	V		
V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -6 mA	-	-	-	-	-	V		
V <sub>OL</sub>	LOW-level output voltage	RTC output; RS = V <sub>CC</sub> and MR = GND						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -3.4 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -3.4 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -3.4 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -3.4 mA	-	0.25	0.40	-	0.50	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -3.4 mA	-	-	-	-	-	V

**Table 5. Static characteristics ...continued**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ <sup>[1]</sup>	Max	Min	Max	
V <sub>OL</sub>	LOW-level output voltage	RTC output; RS = V <sub>CC</sub> and MR = GND						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -100 μA	-	0	0.2	-	0.2	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -100 μA	-	0	0.2	-	0.2	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -100 μA	-	0	0.2	-	0.2	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		CTC output; RS = V <sub>IH</sub> and MR = V <sub>IL</sub>						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -3.8 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -3.8 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -3.8 mA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -3.8 mA	-	0.25	-	0.40	0.50	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -3.8 mA	-	-	-	-	-	V
		except RTC output; V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -100 μA	-	0	0.2	-	0.2	V
		V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -100 μA	-	0	0.2	-	0.2	V
		V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -100 μA	-	0	0.2	-	0.2	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -100 μA	-	-	-	-	-	V
		except RTC and CTC output; V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>						
		V <sub>CC</sub> = 1.2 V; I <sub>O</sub> = -6 mA	-	-	-	-	-	V
V <sub>CC</sub> = 2.0 V; I <sub>O</sub> = -6 mA	-	-	-	-	-	V		
V <sub>CC</sub> = 2.7 V; I <sub>O</sub> = -6 mA	-	0.25	0.40	-	0.50	V		
V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -6 mA	-	-	-	-	-	V		
V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -6 mA	-	-	-	-	-	V		
I <sub>I</sub>	input leakage current	V <sub>CC</sub> = 5.5 V; V <sub>I</sub> = V <sub>CC</sub> or GND	-	-	1.0	-	1.0	μA
I <sub>CC</sub>	supply current	V <sub>CC</sub> = 3.6 V; V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A	-	-	20	-	160	μA
		V <sub>CC</sub> = 5.5 V; V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A	-	-	-	-	80	μA
ΔI <sub>CC</sub>	additional supply current	V <sub>CC</sub> = 2.7 V to 3.6 V; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A	-	-	500	-	850	μA
C <sub>I</sub>	input capacitance		-	3.5	-	-	-	pF

[1] All typical values are measured at T<sub>amb</sub> = 25 °C.



## 11. Dynamic characteristics

**Table 6. Dynamic characteristics**

GND = 0 V; for test circuit, see [Figure 10](#).

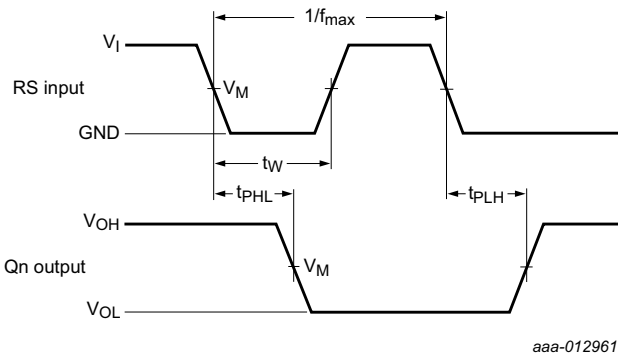
Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ <sup>[1]</sup>	Max	Min	Max	
t <sub>pd</sub>	propagation delay	RS to Q3; see <a href="#">Figure 7</a> and <a href="#">Figure 9</a> <sup>[2]</sup>						
		V <sub>CC</sub> = 1.2 V	-	180	-	-	-	ns
		V <sub>CC</sub> = 2.0 V	-	52	84	-	105	ns
		V <sub>CC</sub> = 2.7 V	-	42	66	-	83	ns
		V <sub>CC</sub> = 3.3 V; C <sub>L</sub> = 15 pF	-	29	-	-	-	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V <sup>[3]</sup>	-	33	53	-	66	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V <sup>[4]</sup>	-	24	39	-	49	ns
		Qn to Qn+1; see <a href="#">Figure 8</a> and <a href="#">Figure 9</a>						
		V <sub>CC</sub> = 1.2 V	-	40	-	-	-	ns
		V <sub>CC</sub> = 2.0 V	-	14	23	-	29	ns
		V <sub>CC</sub> = 2.7 V	-	10	16	-	20	ns
		V <sub>CC</sub> = 3.3 V; C <sub>L</sub> = 15 pF	-	6	-	-	-	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V <sup>[3]</sup>	-	8	13	-	16	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V <sup>[4]</sup>	-	6	9	-	11	ns
t <sub>PHL</sub>	HIGH to LOW propagation delay	MR to Qn; see <a href="#">Figure 8</a> and <a href="#">Figure 9</a>						
		V <sub>CC</sub> = 1.2 V	-	100	-	-	-	ns
		V <sub>CC</sub> = 2.0 V	-	29	46	-	58	ns
		V <sub>CC</sub> = 2.7 V	-	24	39	-	49	ns
		V <sub>CC</sub> = 3.3 V; C <sub>L</sub> = 15 pF	-	16	-	-	-	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V <sup>[3]</sup>	-	19	31	-	39	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V <sup>[4]</sup>	-	14	23	-	29	ns
t <sub>w</sub>	pulse width	RS HIGH or LOW; see <a href="#">Figure 7</a>						
		V <sub>CC</sub> = 2.0 V	34	9	-	38	-	ns
		V <sub>CC</sub> = 2.7 V	25	6	-	30	-	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V <sup>[3]</sup>	20	5	-	24	-	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V <sup>[4]</sup>	16	4	-	20	-	ns
		MR HIGH; see <a href="#">Figure 9</a>						
		V <sub>CC</sub> = 2.0 V	34	10	-	38	-	ns
		V <sub>CC</sub> = 2.7 V	25	8	-	30	-	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V <sup>[3]</sup>	20	6	-	24	-	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V <sup>[4]</sup>	16	4	-	20	-	ns

**Table 6. Dynamic characteristics**  
*GND = 0 V; for test circuit, see [Figure 10](#).*

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ <sup>[1]</sup>	Max	Min	Max	
t <sub>rec</sub>	recovery time	MR to RS; see <a href="#">Figure 9</a>						
		V <sub>CC</sub> = 2.0 V	29	18	-	37	-	ns
		V <sub>CC</sub> = 2.7 V	26	16	-	32	-	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V <a href="#">[3]</a>	18	11	-	23	-	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V <a href="#">[4]</a>	12	7	-	15	-	ns
f <sub>max</sub>	maximum frequency	RS; see <a href="#">Figure 7</a>						
		V <sub>CC</sub> = 2.0 V	14	40	-	9	-	MHz
		V <sub>CC</sub> = 2.7 V	19	70	-	12	-	MHz
		V <sub>CC</sub> = 3.3 V; C <sub>L</sub> = 15 pF	-	99	-	-	-	MHz
		V <sub>CC</sub> = 3.0 V to 3.6 V <a href="#">[3]</a>	24	90	-	15	-	MHz
		V <sub>CC</sub> = 4.5 V to 5.5 V <a href="#">[4]</a>	30	100	-	19	-	MHz
C <sub>PD</sub>	power dissipation capacitance	V <sub>I</sub> = GND to V <sub>CC</sub> <a href="#">[5]</a>	-	40	-	-	-	pF

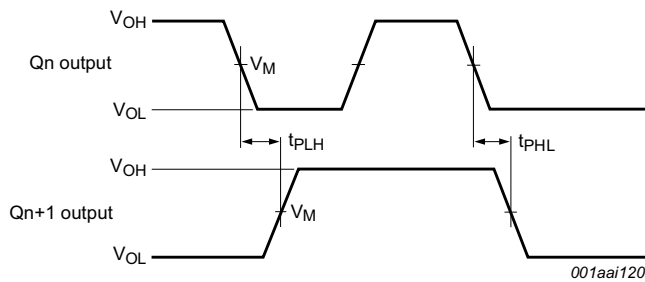
- [1] All typical values are measured at T<sub>amb</sub> = 25 °C.  
 [2] t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.  
 [3] Typical value measured at V<sub>CC</sub> = 3.3 V.  
 [4] Typical value measured at V<sub>CC</sub> = 5.0 V.  
 [5] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).  
 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$  where:  
 f<sub>i</sub> = input frequency in MHz;  
 f<sub>o</sub> = output frequency in MHz;  
 C<sub>L</sub> = output load capacitance in pF;  
 V<sub>CC</sub> = supply voltage in V;  
 N = number of inputs switching;  
 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of outputs.

12. Waveforms



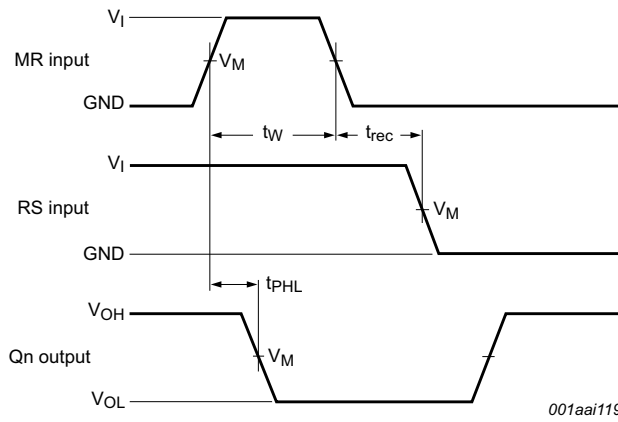
Measurement points are given in [Table 7](#).  
 $V_{OL}$  and  $V_{OH}$  are typical voltage output levels that occur with the output load.

**Fig 7. Waveforms showing the clock (RS) to output (Qn) propagation delays, the clock pulse width, the output transition times and the maximum frequency**



Measurement points are given in [Table 7](#).  
 $V_{OL}$  and  $V_{OH}$  are typical voltage output levels that occur with the output load.

**Fig 8. Waveforms showing the output Qn to output Qn+1 propagation delays**

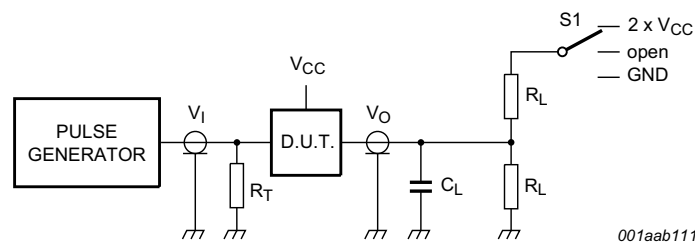


Measurement points are given in [Table 7](#).  
 $V_{OL}$  and  $V_{OH}$  are typical voltage output levels that occur with the output load.

**Fig 9. Waveforms showing the master reset (MR) pulse width, the master reset to output (Qn) propagation delays and the master reset to clock (RS) recovery time**

**Table 7. Measurement points**

Supply voltage	Input	Output
$V_{CC}$	$V_M$	$V_M$
< 2.7 V	$0.5V_{CC}$	$0.5V_{CC}$
2.7 V to 3.6 V	1.5 V	1.5 V
$\geq 4.5$ V	$0.5V_{CC}$	$0.5V_{CC}$



Test data is given in [Table 8](#).  
 Definitions test circuit:  
 $R_T$  = Termination resistance should be equal to output impedance  $Z_o$  of the pulse generator.  
 $C_L$  = Load capacitance including jig and probe capacitance.  
 $R_L$  = Load resistance.

**Fig 10. Test circuit for measuring switching times**

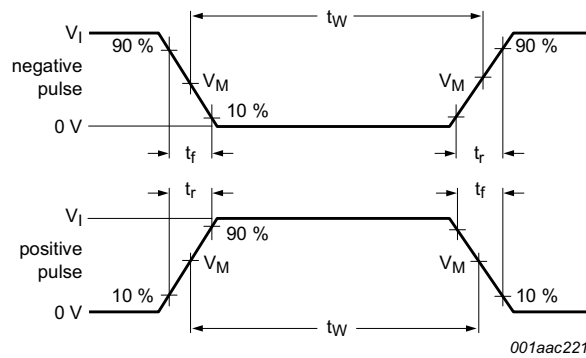
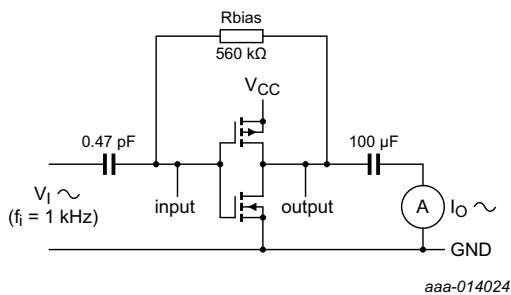


Fig 11. Input pulse definition

Table 8. Test data

Supply voltage	Input		Load		S1
$V_{CC}$	$V_I$	$t_r, t_f$	$C_L$	$R_L$	$t_{PLH}, t_{PHL}$
< 2.7 V	$V_{CC}$	2.5 ns	50 pF	1 k $\Omega$	open
2.7 V to 3.6 V	2.7 V	2.5 ns	15 pF, 50 pF	1 k $\Omega$	open
$\geq 4.5$ V	$V_{CC}$	2.5 ns	50 pF	1 k $\Omega$	open

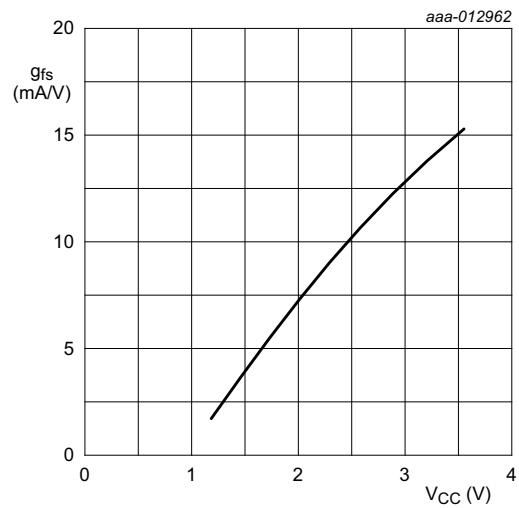
### 13. Typical forward transconductance



$g_{fs} = \Delta I_O / \Delta V_I$  at  $V_O$  is constant; MR = LOW.

See [Figure 13](#).

Fig 12. Test setup for measuring forward transconductance



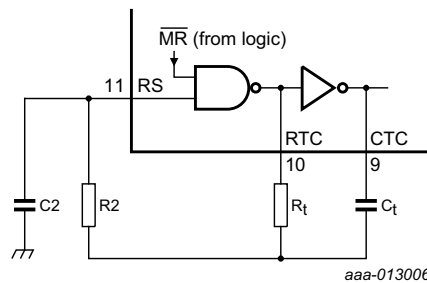
$T_{amb} = 25$  °C

Fig 13. Typical forward transconductance as function of the supply voltage

## 14. RC oscillator

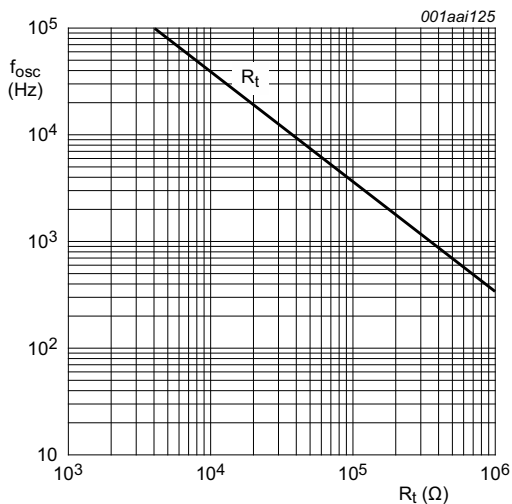
### 14.1 Timing component limitations

The oscillator frequency is mainly determined by  $R_t \times C_t$ , provided  $R_2 \approx 2R_t$  and  $R_2 \times C_2$  is much less than  $R_t \times C_t$ . The function of  $R_2$  is to minimize the influence of the forward voltage across the input protection diodes on the frequency. The stray capacitance  $C_2$  should be kept as small as possible. In consideration of accuracy,  $C_t$  must be larger than the inherent stray capacitance.  $R_t$  must be larger than the 'ON' resistance in series with it, which typically is 280  $\Omega$  at  $V_{CC} = 1.2$  V, 130  $\Omega$  at  $V_{CC} = 2.0$  V and 100  $\Omega$  at  $V_{CC} = 3.0$  V. The recommended values for these components to maintain agreement with the typical oscillation formula are:  $C_t > 50$  pF, up to any practical value,  $10$  k $\Omega < R_t < 1$  M $\Omega$ . In order to avoid start-up problems,  $R_t \geq 1$  k $\Omega$ .



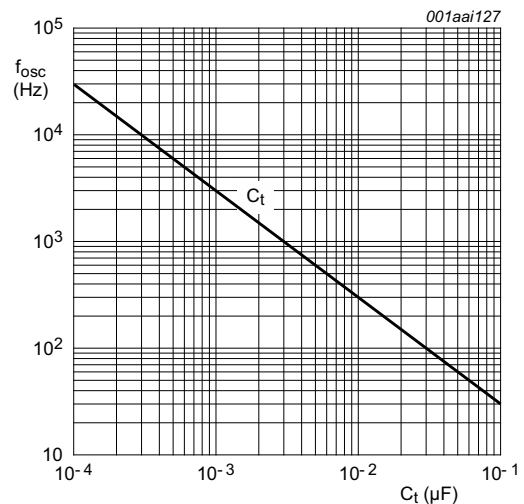
Typical formula for oscillator frequency: 
$$f_{osc} = \frac{1}{2.5 \times R_t \times C_t}$$

Fig 14. Example of an RC oscillator



$V_{CC} = 1.2$  V to 3.6 V;  $T_{amb} = 25$  °C  
 $R_t$  curve:  $C_t = 1$  nF;  $R_2 = 2 \times R_t$

Fig 15. RC oscillator frequency as a function of  $R_t$



$V_{CC} = 1.2$  V to 3.6 V;  $T_{amb} = 25$  °C  
 $C_t$  curve:  $R_t = 100$  k $\Omega$ ;  $R_2 = 200$  k $\Omega$

Fig 16. RC oscillator frequency as a function of  $C_t$

14.2 Typical crystal oscillator circuit

In [Figure 17](#), R2 is the power limiting resistor. For starting and maintaining oscillation, a minimum transconductance is necessary, so R2 must not be too large. A practical value for R2 is 2.2 kΩ.

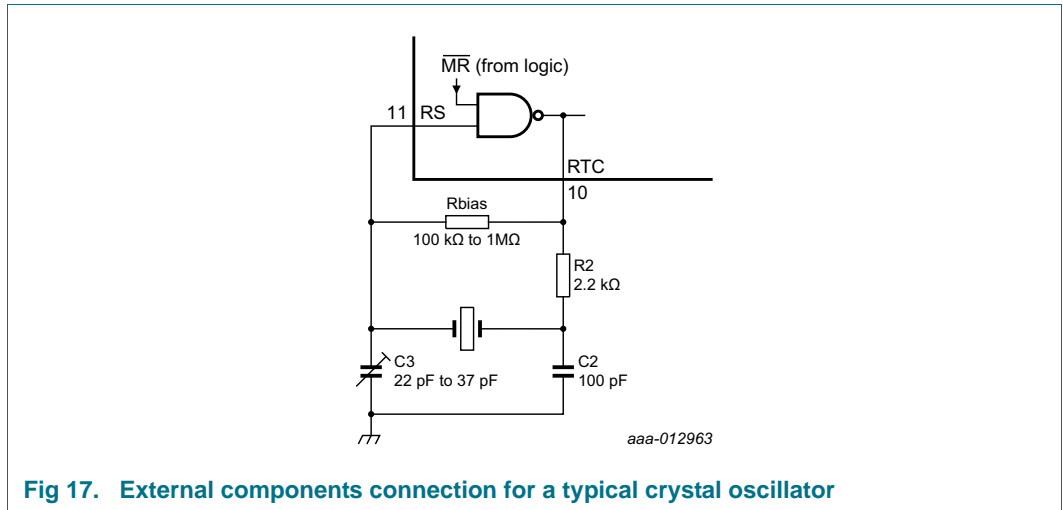


Fig 17. External components connection for a typical crystal oscillator

15. Package outline

SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1

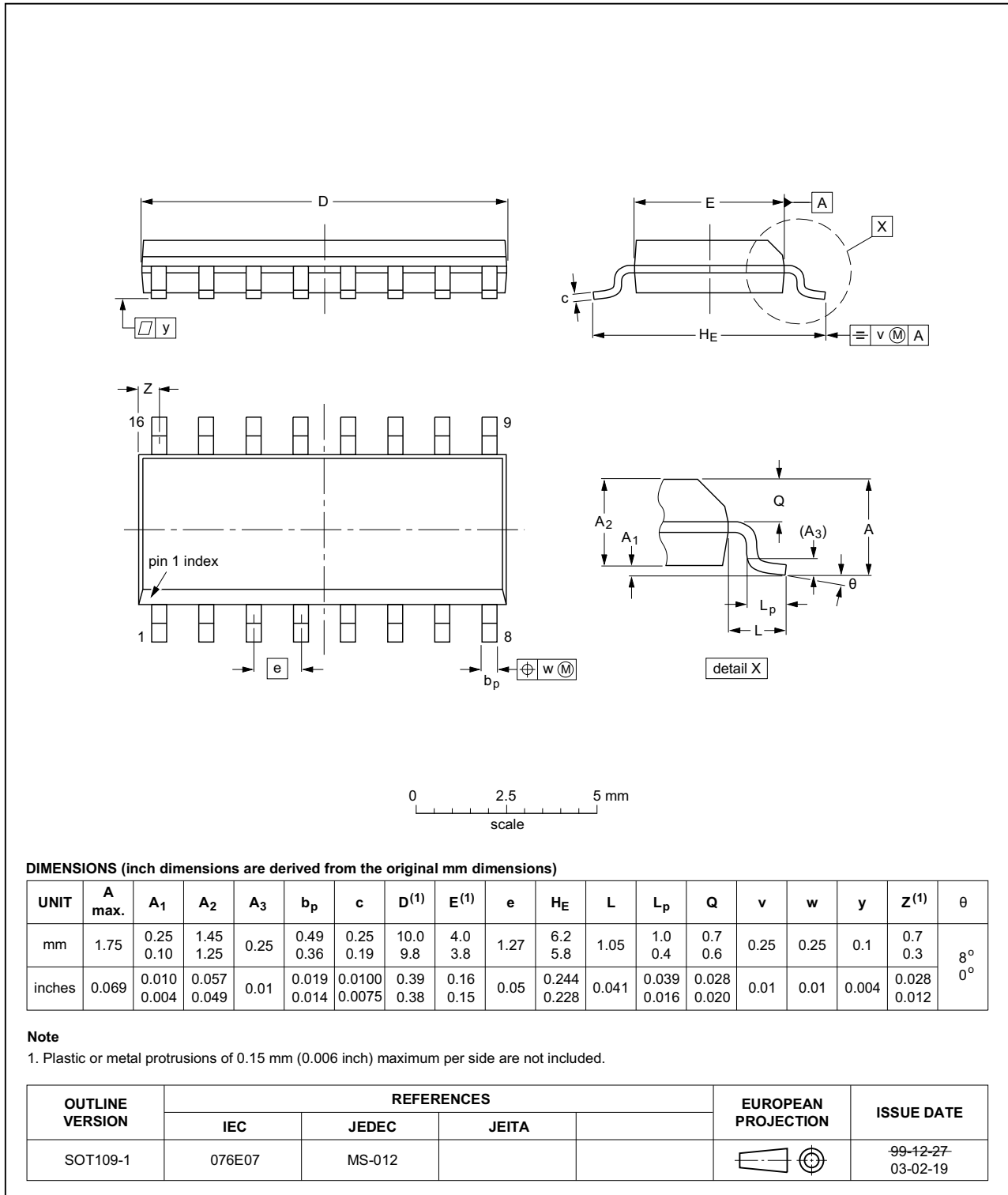


Fig 18. Package outline SOT109-1 (SO16)



TSSOP16: plastic thin shrink small outline package; 16 leads; body width 4.4 mm

SOT403-1

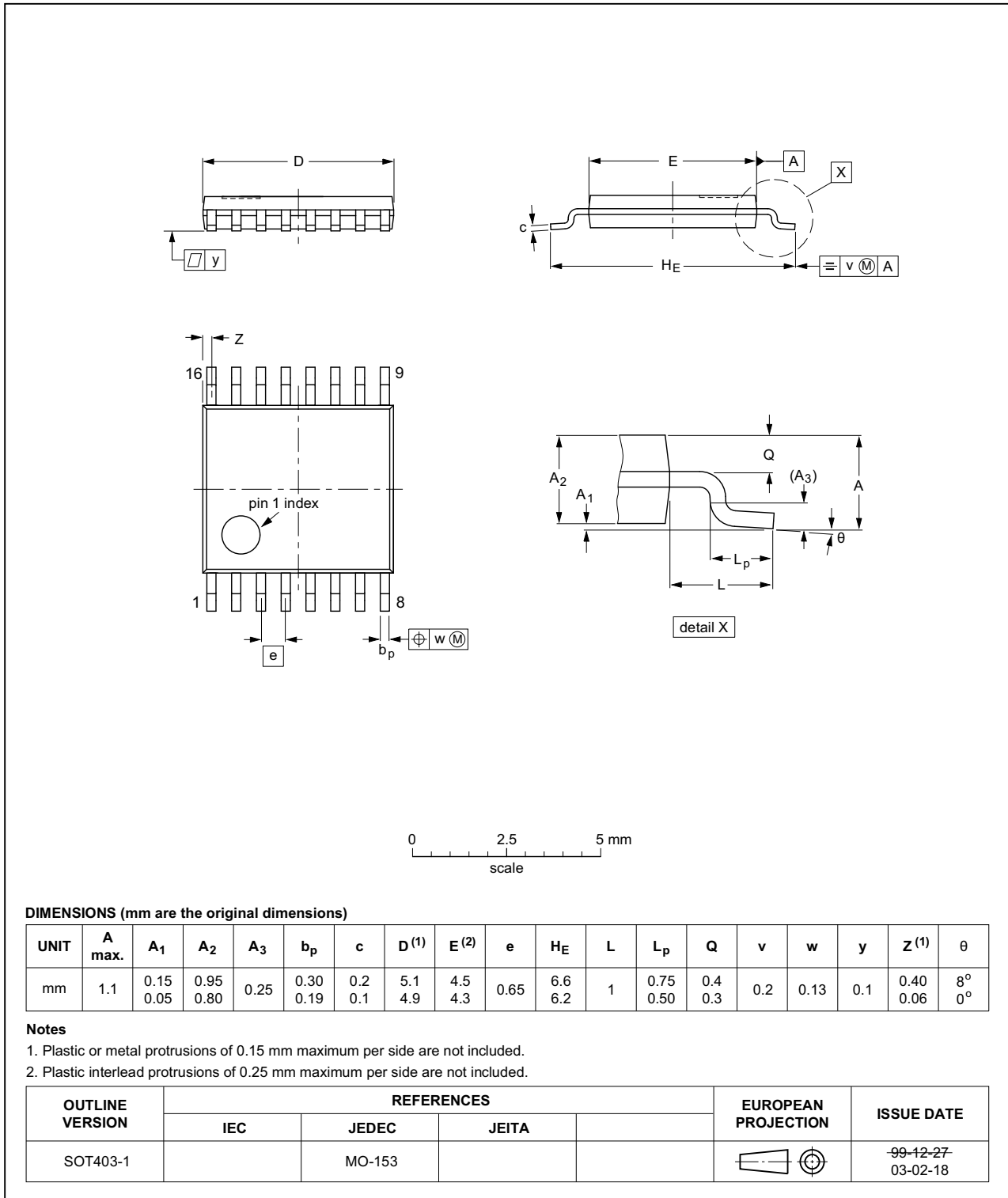


Fig 19. Package outline SOT403-1 (TSSOP16)

## 16. Abbreviations

Table 9. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MIL	Military
MM	Machine Model
TTL	Transistor-Transistor Logic

## 17. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LV4060_Q100 v.1	20140725	Product data sheet	-	-

## 18. Legal information

### 18.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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[2] The term 'short data sheet' is explained in section "Definitions".

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