



Low-Voltage Dual SPST Analog Switch

FEATURES

- Low Voltage Operation (1.8 V to 5.5 V)
- Low On-Resistance - $r_{DS(on)}$: 1.2 Ω
- Fast Switching - 14 ns
- Low Charge Injection - Q_{INJ} : 1 pC
- Low Power Consumption
- TTL/CMOS Compatible
- MSOP-8 Package

BENEFITS

- Reduced Power Consumption
- Simple Logic Interface
- High Accuracy
- Reduce Board Space

APPLICATIONS

- Cellular Phones
- Communication Systems
- Portable Test Equipment
- Battery Operated Systems
- Sample and Hold Circuits

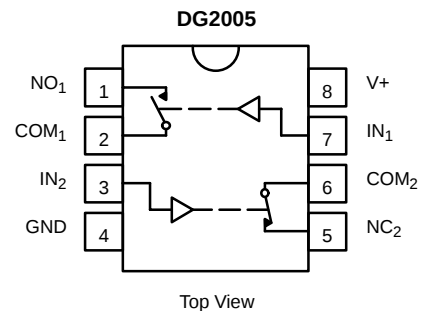
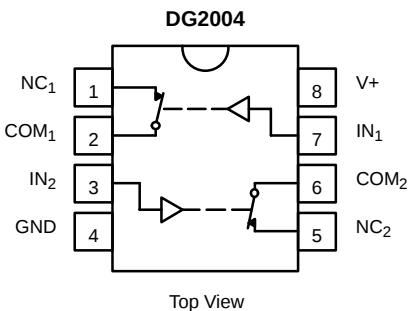
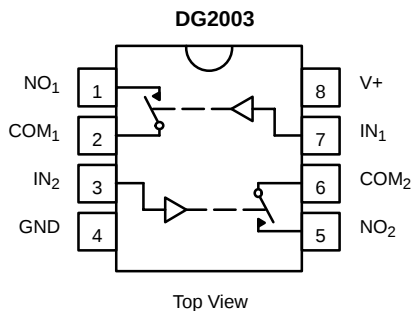
DESCRIPTION

The DG2003/2004/2005 are dual single-pole/single-throw monolithic CMOS analog switch designed for high performance switching of analog signals. Combining low power, fast switching, low on-resistance ($r_{DS(on)}$: 1.2 Ω) and small physical size (MSOP-8), the DG2003/2004/2005 are ideal for portable and battery powered applications requiring high performance and efficient use of board space.

The DG2003/2004/2005 are built on Vishay Siliconix's low voltage J12 process. An epitaxial layer prevents latchup.

Each switch conducts equally well in both directions when on, and blocks up to the power supply level when off.

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION

**TRUTH TABLE - DG2003**

Logic	NO
0	Off
1	On

TRUTH TABLE - DG2004

Logic	NC
0	On
1	Off

TRUTH TABLE - DG2005

Logic	NO ₁	NC ₂
0	Off	On
1	On	Off

ORDERING INFORMATION

Temp Range	Package	Part Number
-40 to 85°C	MSOP-8	DG2003DQ
		DG2004DQ
		DG2005DQ

ABSOLUTE MAXIMUM RATINGS

Reference to GND

V+	-0.3 to +6 V
IN, COM, NC, NO ^a	-0.3 to (V+ + 0.3 V)
Continuous Current (Any terminal)	±50 mA
Peak Current (Pulsed at 1 ms, 10% duty cycle)	±200 mA
Storage Temperature (D Suffix)	-65 to 150°C

Power Dissipation (Packages)^b

MSOP-8 ^c	320 mW
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Notes:

- Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
- All leads welded or soldered to PC Board.
- Derate 6.5 mW/°C above 25°C

SPECIFICATIONS (V+ = 2.0 V)							
Parameter	Symbol	Test Conditions Otherwise Unless Specified V+ = 2.0 V, ±10%, VIN = 0.4 or 1.6 V ^e	Temp ^a	Limits −40 to 85°C			Unit
				Min ^b	Typ ^c	Max ^b	
Analog Switch							
Analog Signal Range ^d	VNO, VNC, VCOM		Full	0		V+	V
On-Resistance	rON	V+ = 2.0 V, VCOM = 1.0 V, INO, INC = 1 mA	Room Full ^d		7.0 12.5	10.0 16.0	Ω
rON Flatness ^d	rON Flatness	V+ = 2.0 V, VCOM = 0 to V+, INO, INC = 1 mA	Room		5		
Switch Off Leakage Current ^f	INO(off), INC(off)	V+ = 2.2 V VNO, VNC = 0.5 V/1.5 V, VCOM = 1.5 V/0.5 V	Room Full ^d	−500 −4.0		500 4.0	pA nA
	ICOM(off)		Room Full ^d	−500 −4.0		500 4.0	pA nA
Channel-On Leakage Current ^f	ICOM(on)	V+ = 2.2 V, VNO, VNC = VCOM = 0.5 V/1.5 V	Room Full ^d	−500 −4.0		500 4.0	pA nA
Digital Control							
Input High Voltage	VINH		Full	1.6			V
Input Low Voltage	VINL		Full			0.4	
Input Capacitance ^d	Cin		Full		5		pF
Input Current	IINL or IINH	VIN = 0 or V+	Full	−1		1	μA
Dynamic Characteristics							
Turn-On Time	tON	VNO or VNC = 1.5 V, RL = 300 Ω, CL = 35 pF Figures 1 and 2	Room Full ^d		30	47 48	ns
Turn-Off Time	tOFF		Room Full ^d		22	37 48	
Charge Injection ^d	QINJ	CL = 1 nF, VGEN = 0 V, RGEN = 0 Ω, Figure 3	Room		2		pC
Off-Isolation ^d	OIRR	RL = 50 Ω, CL = 5 pF, f = 1 MHz	Room		−61		dB
Crosstalk ^d	XTALK		Room		−67		
NO, NC Off Capacitance ^d	CNO(off), CNC(off)	VIN = 0 or V+, f = 1 MHz	Room		53		pF
Channel-On Capacitance ^d	CON		Room		110		
Power Supply							
Power Supply Range	V+			1.8		2.2	V
Power Supply Current ^d	I+	VIN = 0 or V+			0.02	1.0	μA
Power Consumption	PC					2.2	μW

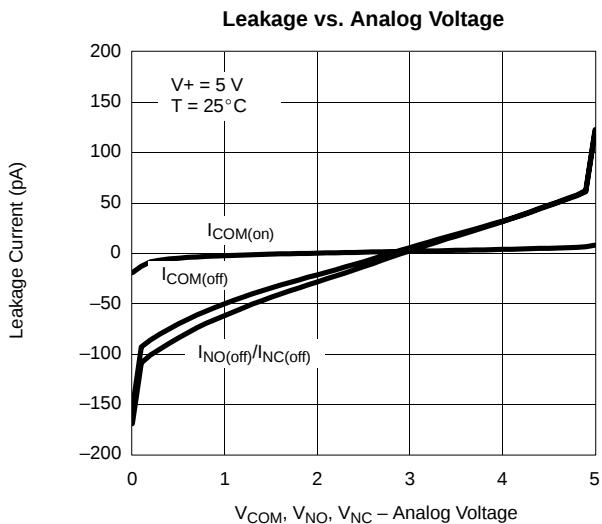
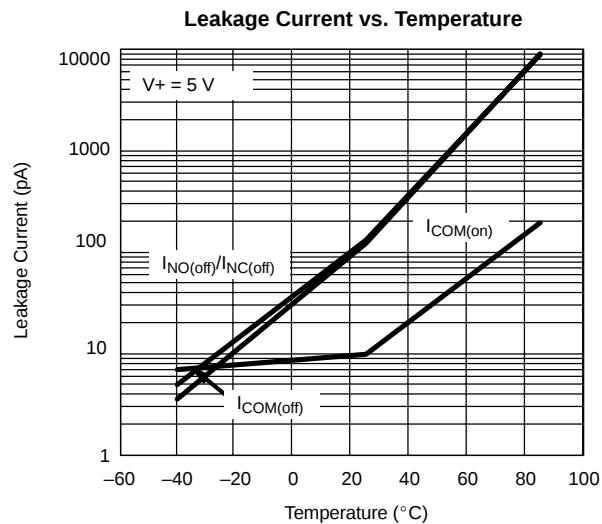
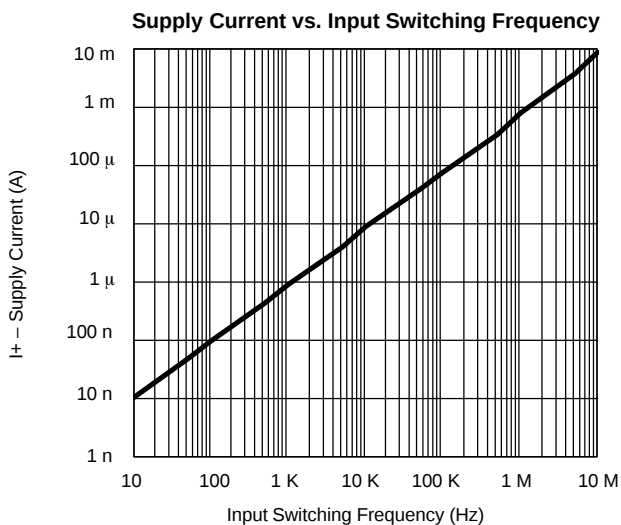
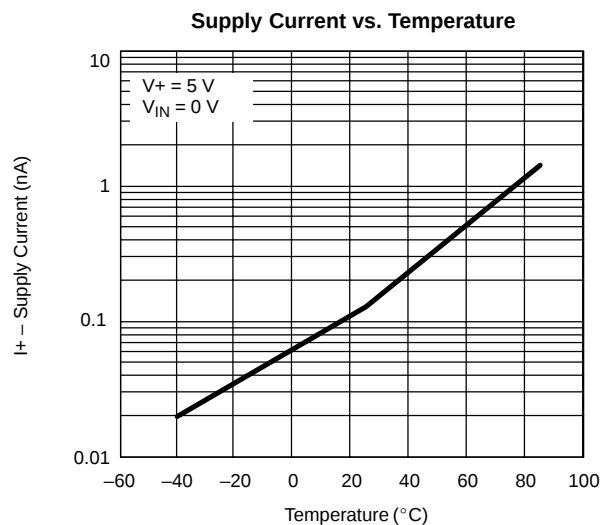
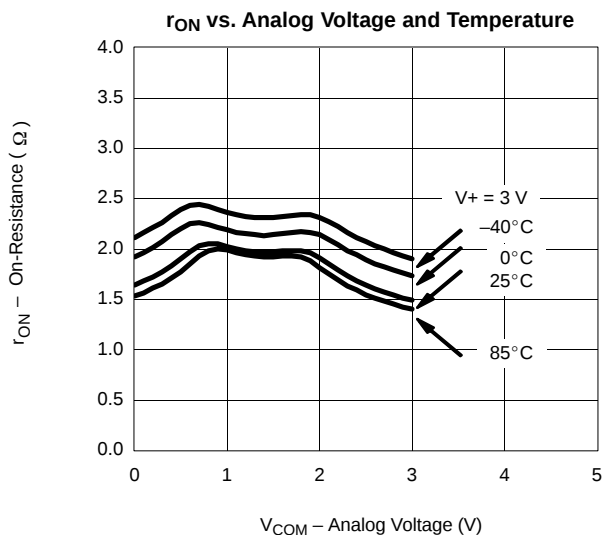
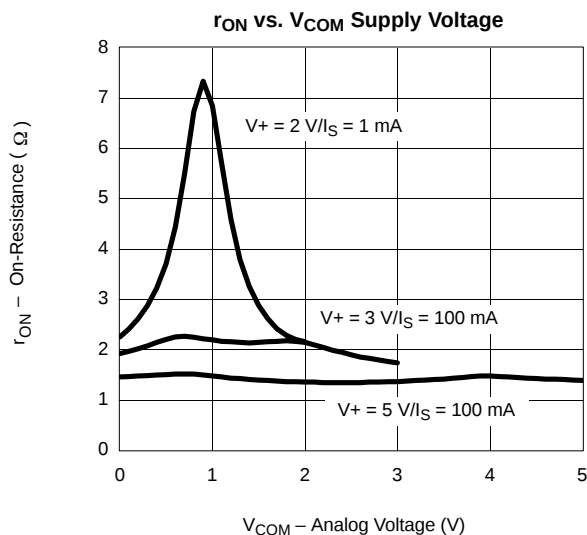
**SPECIFICATIONS (V+ = 3.0 V)**

Parameter	Symbol	Test Conditions Otherwise Unless Specified V+ = 3 V, ± 10%, VIN = 0.4 or 2.0 V ^e	Temp ^a	Limits –40 to 85°C			Unit
				Min ^b	Typ ^c	Max ^b	
Analog Switch							
Analog Signal Range ^d	VNO, VNC, VCOM		Full	0		V+	V
On-Resistance	rON	V+ = 2.7 V, VCOM = 1.5 V, INO, INC = 10 mA	Room Full		2.2 2.4	3.5 3.7	Ω
rON Flatness ^d	rON Flatness	V+ = 2.7 V, VCOM = 0 to V+, INO, INC = 10 mA	Room		0.5		
Switch Off Leakage Current ^f	INO(off), INC(off)	V+ = 3.3 V VNO, VNC = 1 V/3 V, VCOM = 3 V/1 V	Room Full	–500 –6.0		500 6.0	pA nA
	ICOM(off)		Room Full	–500 –6.0		500 6.0	pA nA
Channel-On Leakage Current ^f	ICOM(on)	V+ = 3.3 V, VNO, VNC = VCOM = 1 V/3 V	Room Full	–500 –6.0		500 6.0	pA nA
Digital Control							
Input High Voltage	VINH		Full	2			V
Input Low Voltage	VINL		Full			0.4	
Input Capacitance ^d	Cin		Full		5		pF
Input Current	INIL or IINH	VIN = 0 or V+	Full	–1		1	μA
Dynamic Characteristics							
Turn-On Time ^d	tON	VNO or VNC = 2.0 V, RL = 300 Ω, CL = 35 pF Figure 1 and 2	Room Full		19	35 36	ns
Turn-Off Time ^d	tOFF		Room Full		17	31 34	
Charge Injection ^d	QINJ	CL = 1 nF, VGEN = 0 V, RGEN = 0 Ω, Figure 3	Room		1		pC
Off-Isolation ^d	OIRR	RL = 50 Ω, CL = 5 pF, f = 1 MHz	Room		–61		dB
Crosstalk ^d	XTALK		Room		–67		
NO, NC Off Capacitance ^d	CNO(off), CNC(off)	VIN = 0 or V+, f = 1 MHz	Room		53		pF
Channel-On Capacitance ^d	CON		Room		110		
Power Supply							
Power Supply Range	V+			2.7		3.3	V
Power Supply Current	I+	VIN = 0 or V+			0.02	1.0	μA
Power Consumption	PC					3.3	μW

SPECIFICATIONS (V+ = 5.0 V)							
Parameter	Symbol	Test Conditions Otherwise Unless Specified V+ = 5 V, ± 10%, VIN = 0.8 or 2.4 V ^e	Temp ^a	Limits –40 to 85°C			Unit
				Min ^b	Typ ^c	Max ^b	
Analog Switch							
Analog Signal Range ^d	VNO, VNC, VCOM		Full	0		V+	V
On-Resistance	rON	V+ = 4.5 V, VCOM = 3 V, INO, INC = 10 mA	Room Full		1.2 1.6	2.5 2.7	Ω
rON Flatness ^d	rON Flatness	V+ = 4.5 V, VCOM = 0 to V+, INO, INC = 10 mA	Room		0.2		
Switch Off Leakage Current	INO(off), INC(off)	V+ = 5.5 V VNO, VNC = 1 V/4.5 V, VCOM = 4.5 V/1 V	Room Full	–1.0 –8.0		1.0 8.0	nA
	ICOM(off)		Room Full	–1.0 –8.0		1.0 8.0	
Channel-On Leakage Current	ICOM(on)	V+ = 5.5 V, V+ = 5.5 V VNO, VNC = VCOM = 1 V/4.5 V	Room Full	–1.0 –8.0		1.0 8.0	
Digital Control							
Input High Voltage	VINH		Full	2.4			V
Input Low Voltage	VINL		Full			0.8	
Input Capacitance	Cin		Full		5		pF
Input Current	IINL or IINH	VIN = 0 or V+	Full	–1		1	μA
Dynamic Characteristics							
Turn-On Time ^d	tON	VNO or VNC = 3 V, RL = 300 Ω, CL = 35 pF Figure 1 and 2	Room Full		13	28 31	ns
Turn-Off Time ^d	tOFF		Room Full		19	22 31	
Charge Injection ^d	QINJ	CL = 1 nF, VGEN = 0 V, RGEN = 0 Ω, Figure 3	Room		1		pC
Off-Isolation ^d	OIRR	RL = 50 Ω, CL = 5 pF, f = 1 MHz	Room		–61		dB
Crosstalk ^d	XTALK		Room		–67		
Source-Off Capacitance ^d	CNO(off), CNC(off)	VIN = 0 or V+, f = 1 MHz	Room		51		pF
Channel-On Capacitance ^d	CON		Room		110		
Power Supply							
Power Supply Range	V+			4.5		5.5	V
Power Supply Current	I+	VIN = 0 or V+			0.02	1.0	μA
Power Consumption	PC					5.5	μW

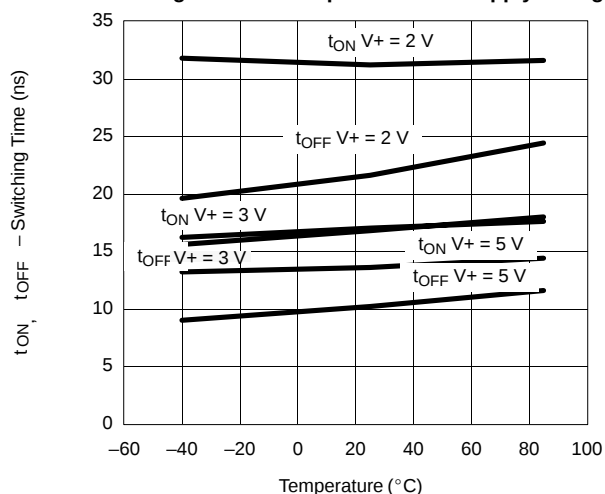
Notes:

- Room = 25°C, Full = as determined by the operating suffix.
- The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- Typical values are for design aid only, not guaranteed nor subject to production testing.
- Guarantee by design, nor subjected to production test.
- V_{IN} = input voltage to perform proper function.
- Guaranteed by 5-V leakage testing, not production tested.

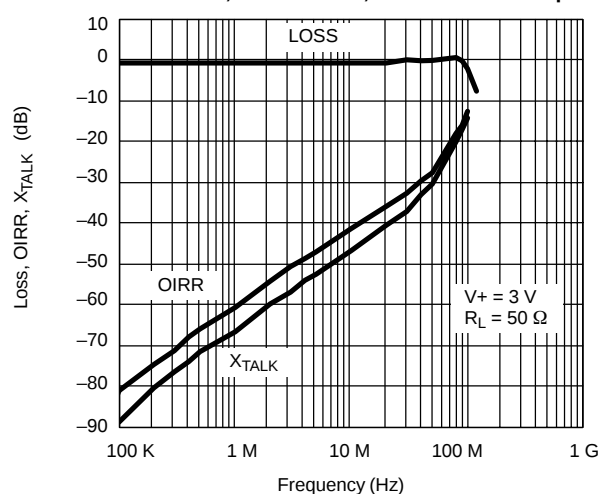
**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**

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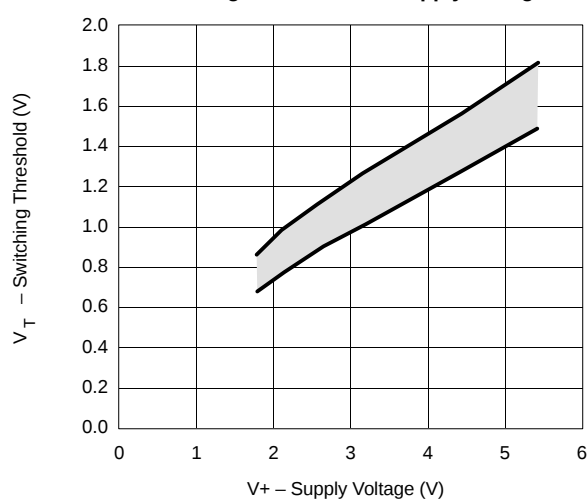
Switching Time vs. Temperature and Supply Voltage



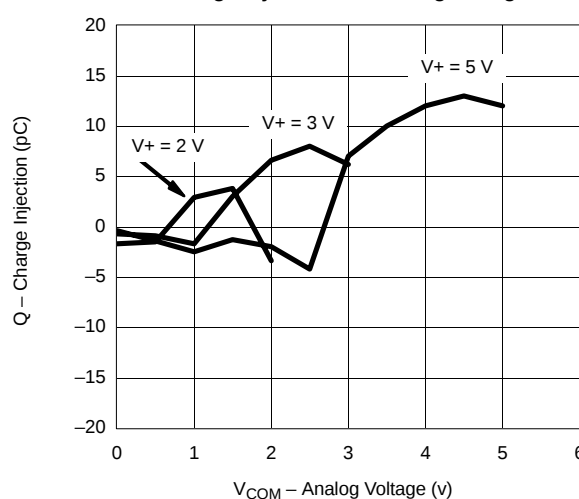
Insertion Loss, Off-Isolation, Crosstalk vs. Frequency



Switching Threshold vs. Supply Voltage



Charge Injection vs. Analog Voltage



TEST CIRCUITS

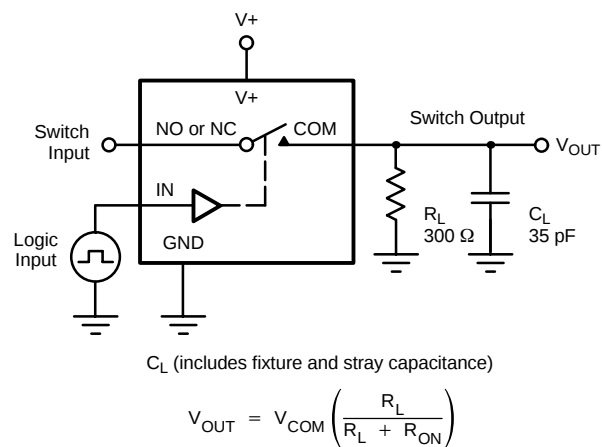


FIGURE 1. Switching Time

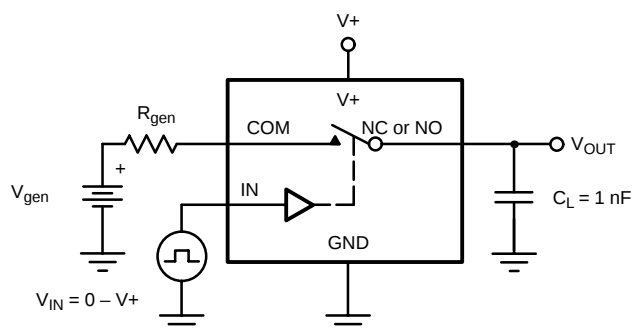


FIGURE 2. Charge Injection

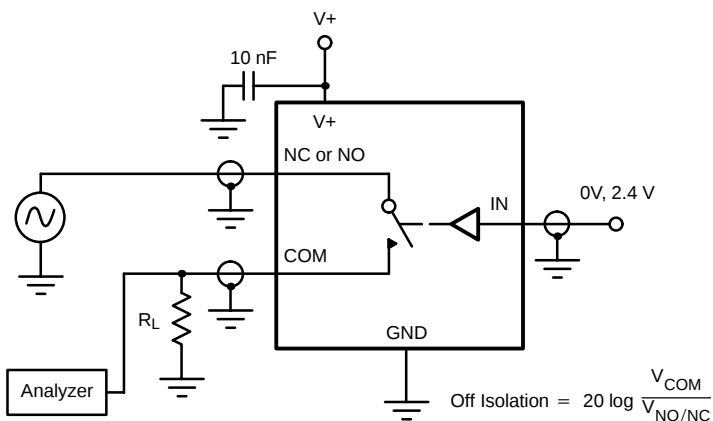
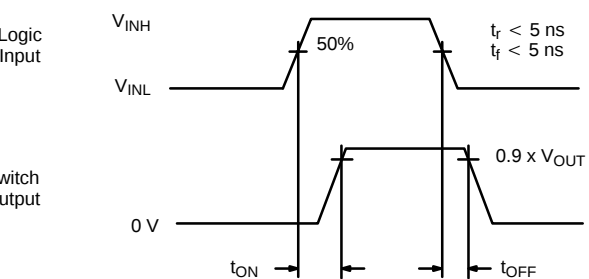
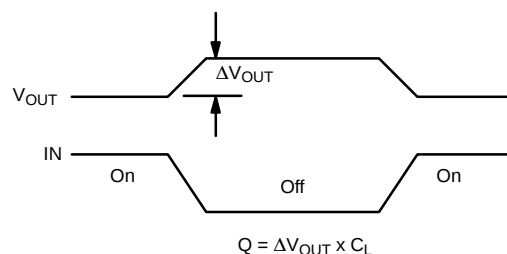


FIGURE 3. Off-Isolation



Logic "1" = Switch On
Logic input waveforms inverted for switches that have the opposite logic sense.



IN depends on switch configuration: input polarity determined by sense of switch.

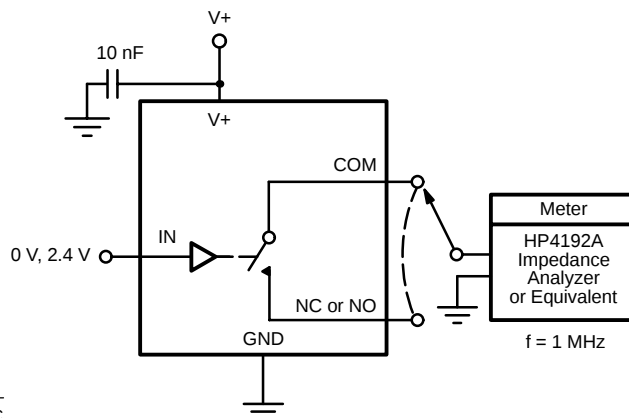


FIGURE 4. Channel Off/On Capacitance



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