TOSHIBA Diode Silicon Epitaxial Planar Type

1SS190

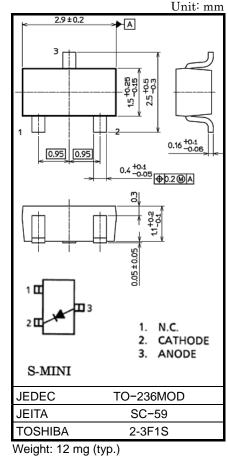
Ultra High Speed Switching Application

- AEC-Q101 Qualified (Note1)
- Small package : SC-59
- Low forward voltage : $V_{F(3)} = 0.92V$ (typ.)
- Fast reverse recovery time: trr = 1.6ns (typ.)
- Small total capacitance $: C_T = 2.2 pF (typ.)$

Note1: For detail information, please contact to our sales.

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	V _{RM}	85	V
Reverse voltage	VR	80	V
Maximum (peak) forward current	IFM	300	mA
Average forward current	IO	100	mA
Surge current (10ms)	IFSM	2	А
Power dissipation	Р	150	mW
Junction temperature	Tj	125	°C
Storage temperature range	T _{stg}	-55 to 125	°C



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly meven if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 1982-06

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Forward voltage	VF (1)	I _F = 1mA	_	0.61	_		
	VF (2)	I _F = 10mA	_	0.74	—	V	
	VF (3)	I _F = 100mA	_	0.92	1.20		
Reverse current	I _{R (1)}	V _R = 30V	_	_	0.1		
	I _{R (2)}	V _R = 80V	_	_	0.5	μΑ	
Total capacitance	CT	$V_R = 0V, f = 1MH_Z$	_	2.2	4.0	pF	
Reverse recovery time	t _{rr}	I _F = 10mA (Fig.1)		1.6	4.0	ns	

Marking

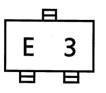
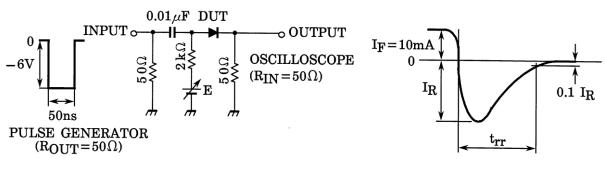


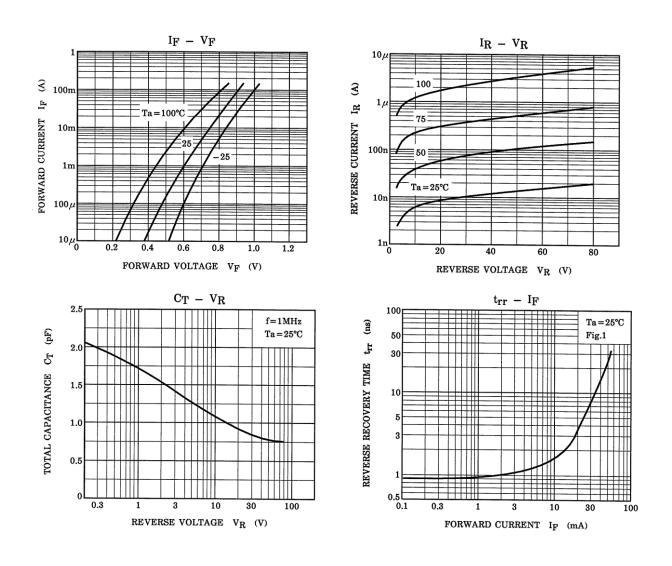
Fig.1 Reverse recovery time (trr) test circuit

INPUT WAVEFORM

OUTPUT WAVEFORM



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