# Quad Fault-Protected RS-485/RS-422 Receiver with Fault Detection

#### **General Description**

The MAX14891E quad fault-protected RS-485/RS-422 receiver is ideal for applications requiring high data rates and reduced noise in rugged environments. Each receiver features a wide common-mode input range of -20V to +20V and is guaranteed to receive data at speeds up to 35Mbps.

All receiver inputs are fault-protected against voltage shorts in the  $\pm 40 \text{V}$  range. Per-channel fault detection provides warning of irregular conditions, such as short circuits and open connections.

The MAX14891E is available in a 20-pin TQFN-EP (4mm x 4mm) and operates over the -40°C to +125°C temperature range.

#### **Applications**

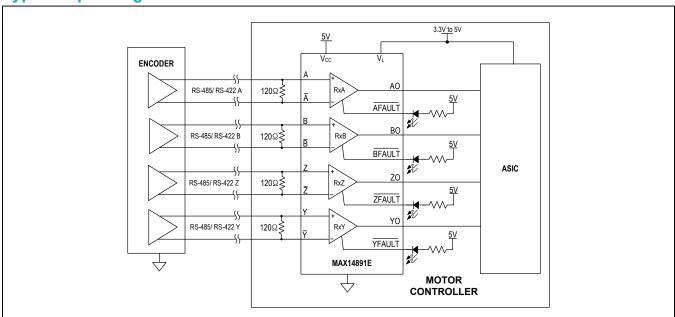
- Encoder Interfaces
- Motor Controllers
- Servo Control Commutation

#### **Benefits and Features**

- High Flexibility Simplifies Design
  - Switching Rates up to 35Mbps
  - · 1.62V to 5.5V Logic Interface
- Integrated Fault Detection Reduces Downtime
  - · Open-Wire and Short-Circuit Detection
  - · Overvoltage and Undervoltage Fault Detection
- Integrated Protection Ensures Robust Communication
  - · ±40V Fault Protection Range
  - ±20V Common Mode Range
  - ±25kV HBM ESD
  - ±7kV Air-Gap IEC 61000-4-2 ESD
  - ±10kV Contact IEC 61000-4-2 ESD
  - -40°C to +125°C Operating Temperature Range

Ordering Information appears at end of data sheet.

### **Typical Operating Circuit**





# Quad Fault-Protected RS-485/RS-422 Receiver with Fault Detection

#### **Absolute Maximum Ratings**

(All voltages referenced to GND)	Continuous Power Dissipation (T <sub>A</sub> = +70°C)
V <sub>CC</sub> 0.3V to +6V	TQFN (derate 25.6mW/°C above +70°C)2051mW
V <sub>L</sub> 0.3V to (V <sub>CC</sub> + 0.3V)	Operating Temperature Range40°C to +125°C
AO, BO, ZO, YO0.3V to (V <sub>L</sub> + 0.3V)	Junction Temperature+150°C
AFAULT, BFAULT, ZFAULT, YFAULT,0.3V to +6V	Storage Temperature Range65°C to +150°C
A, $\overline{A}$ , B, $\overline{B}$ , Z, $\overline{Z}$ , Y, $\overline{Y}$ 40V to +40V	Lead Temperature (soldering, 10s)+300°C
Short-Circuit Duration (_O, _FAULT to GND)Continuous	Soldering Temperature (reflow)+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **Package Thermal Characteristics (Note 1)**

Junction-to-Ambient Thermal Resistance ( $\theta_{JA}$ )..............39°C/W Junction-to-Case Thermal Resistance ( $\theta_{JC}$ )................6°C/W

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to <a href="https://www.maximintegrated.com/thermal-tutorial">www.maximintegrated.com/thermal-tutorial</a>.

#### **Electrical Characteristics**

 $(V_{CC} = 5V \pm 10\%, V_L = 1.62V \text{ to } V_{CC}, T_A = -40^{\circ}\text{C} \text{ to } +125^{\circ}\text{C}, \text{ unless otherwise noted.}$  Typical values are at  $V_{CC} = 5V, V_L = 3.3V, T_A = +25^{\circ}\text{C}.$ ) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
POWER SUPPLY							
Supply Voltage	V <sub>CC</sub>					5.5	V
Supply Current	I <sub>CC</sub>	Outputs not switchin	g, no load		9	14	mA
Logic Supply Voltage	V <sub>L</sub>			1.62		5.5	V
RS-485/RS-422 RECEIVERS (RxA, RxB, RxZ, RxY)							
Differential Threshold Voltage	V <sub>TH</sub>	-20V ≤ V <sub>CM</sub> ≤ +20V	-20V ≤ V <sub>CM</sub> ≤ +20V			+200	mV
Differential Input Hysteresis	$\Delta V_{TH}$	-20V ≤ V <sub>CM</sub> ≤ +20V	-20V ≤ V <sub>CM</sub> ≤ +20V		230		mV
Single Ended Innut Comment	_	\\ = 0\\ o= 5\\	V <sub>IN</sub> = -10V	-270	-170		
Single-Ended Input Current	I <sub>IN</sub>	V <sub>CC</sub> = 0V or 5V	V <sub>IN</sub> = +10V		+100	+160	μΑ
Low Differential Voltage Fault	V <sub>TH_DFP</sub>	-20V ≤ V <sub>CM</sub> ≤ +20V, positive		+270		+460	mV
Threshold	V <sub>TH_DFN</sub>	$-20V \le V_{CM} \le +20V$ ,	negative	-460		-270	IIIV

# **Electrical Characteristics (continued)**

( $V_{CC}$  = 5V±10%,  $V_L$  = 1.62V to  $V_{CC}$ ,  $T_A$  = -40°C to +125°C, unless otherwise noted. Typical values are at  $V_{CC}$  = 5V,  $V_L$  = 3.3V,  $T_A$  = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Single-Ended Input Fault	V <sub>TH_SELP</sub>	Positive	+15		+18	V
Threshold	V <sub>TH_SELN</sub>	Negative	-18		-15	ľ
LOGIC INTERFACE (AO, AFAULT	, BO, BFAULT	, ZO, <del>ZFAULT</del> , YO, <del>YFAULT</del> )				
Output High Voltage	V <sub>OH</sub>	AO, BO, ZO, YO, I <sub>OUT</sub> = -3mA	V <sub>L</sub> - 0.4V			V
Output Low Voltage	V <sub>OL</sub>	AO, BO, ZO, YO, AFAULT, BFAULT, ZFAULT, YFAULT, I <sub>OUT</sub> = +3mA			0.4	V
PROTECTION						
Thermal-Shutdown Threshold	T <sub>SHDN</sub>	Temperature rising		+160		°C
Thermal-Shutdown Hysteresis	T <sub>HYST</sub>			10		°C
Fault-Protected Input Voltage Range (A, $\overline{A}$ , B, $\overline{B}$ , Z, $\overline{Z}$ , Y, $\overline{Y}$ )	V <sub>IN_F</sub>		-40		+40	V
		IEC 61000-4-2 air-gap discharge to GND		±7		
ESD Protection (A, $\overline{A}$ , B, $\overline{B}$ , Z, $\overline{Z}$ , DIY, $\overline{Y}$ )		IEC 61000-4-2 contact discharge to GND		±10		kV
		Human Body Model		±25		
ESD Protection (All Other Pins)		Human Body Model		±2		kV

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## **Switching Characteristics**

 $(V_{CC} = 5V \pm 10\%, V_L = 1.62V \text{ to } V_{CC}, T_A = -40^{\circ}\text{C to } +125^{\circ}\text{C}, \text{ unless otherwise noted.}$  Typical values are at  $V_{CC} = 5V, V_L = 3.3V, V_C = 5V \pm 10\%$  $T_A = +25^{\circ}C.$ ) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
RECEIVER (RxA, RxB, RxZ, RxY) (Note 4)						
Maximum Data Rate	DR <sub>MAX</sub>		35			Mbps
Receiver Propagation Delay	t <sub>DPLH</sub>	C. = 15pE V.= = ±3V Figure 1 Figure 2			25	ns
Receiver Propagation Delay	t <sub>DPHL</sub>	C <sub>L</sub> = 15pF, V <sub>ID</sub> = ±3V, <u>Figure 1</u> , <u>Figure 2</u>			25	ns
Receiver Propagation Delay Skew   tDPLH - tDPHL	tHLSKEW	C <sub>L</sub> = 15pF, V <sub>ID</sub> = ±3V, <u>Figure 1</u> , <u>Figure 2</u>	0		5	ns
Receiver Channel-to-Channel Skew	tcskew	C <sub>L</sub> = 15pF, V <sub>ID</sub> = ±3V, <u>Figure 1</u> , <u>Figure 2</u>	0		8	ns
FAULT DETECTION (AFAULT, BFA	AULT, ZFAUL	Γ) (Note 4)				
Differential Fault Propagation		$R_{FAULT} = 5k\Omega$ , $C_{FAULT} = 15pF$ ,			18	
Delay to FAULT Output Active	<sup>t</sup> DFLH	Figure 1, Figure 3			6	μs
Differential Slew Rate to Avoid Fault Alarm Output		$R_{FAULT}$ = 5kΩ, $C_{FAULT}$ = 15pF, Figure 1, Figure 3	1			V/µs
Single-Ended Propagation Delay	tSEFLH	970				
to FAULT Output Active	t <sub>SEFHL</sub>	$R_{FAULT} = 5k\Omega$ , $C_{FAULT} = 15pF$		970		μs

Note 2: All devices 100% production tested at  $T_A = 25$ °C. Specifications over temperature are guaranteed by design. Note 3: Currents into the device are positive; all currents out of the device are negative. All voltages are referenced to ground, unless otherwise noted.

Note 4: Capacitive load includes test prove and fixture capacitance.

## **Test Circuits and Waveforms**

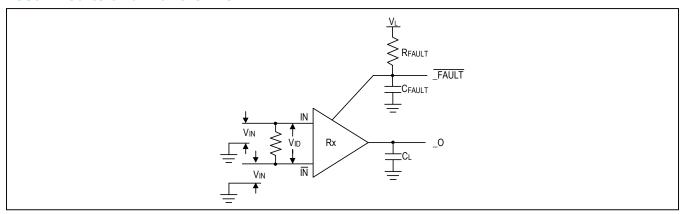


Figure 1. Receiver Test Circuit

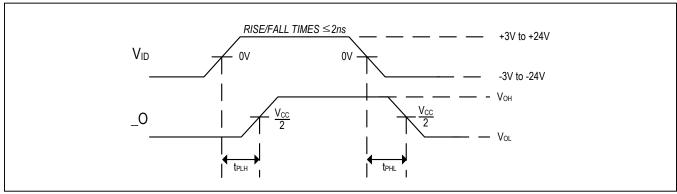


Figure 2. Receiver Propagation Delay

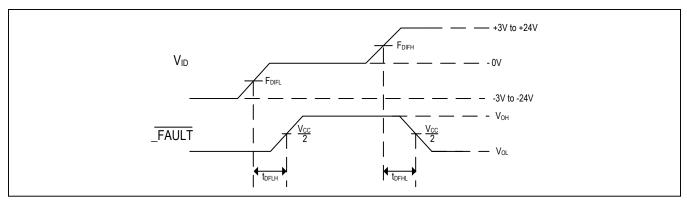
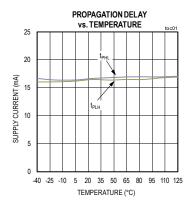
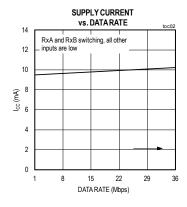


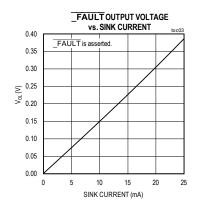
Figure 3. Fault Detection Timing

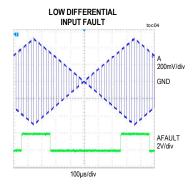
# **Typical Operating Characteristics**

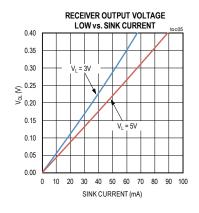
 $(V_{CC} = 5V, V_L = 3.3V, T_A = +25^{\circ}C, unless otherwise noted.)$ 

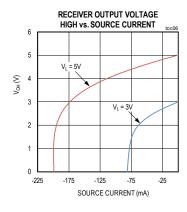


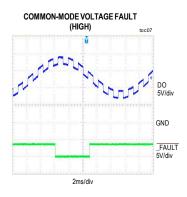


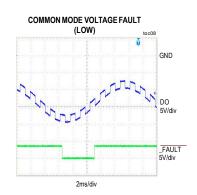


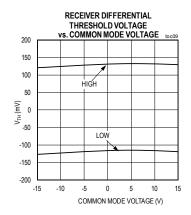




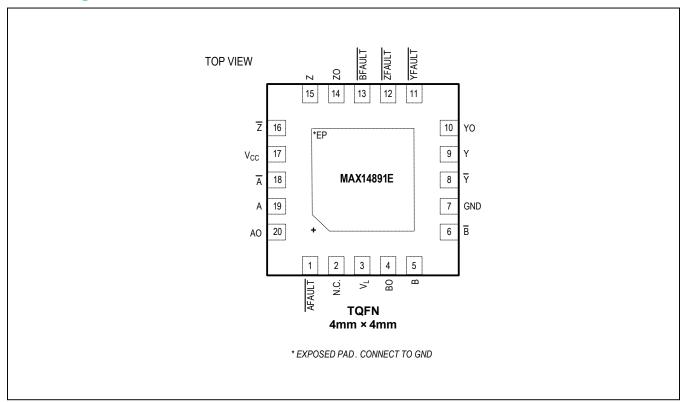








# **Pin Configuration**



# **Pin Description**

PIN	NAME	FUNCTION
1	AFAULT	Open-Drain Fault Output for Receiver A. AFAULT asserts low during a fault condition on Receiver A (RxA). See the FAULT CONDITION section for more information.
2	N.C.	No Connection. Not internally connected.
3	V <sub>L</sub>	Logic Interface Supply Input. $V_{CC}$ must always be greater than or equal to $V_L$ .
4	во	Receiver B Output
5	В	Non-Inverting Input for Receiver B
6	B	Inverting Input for Receiver B
7	GND	Ground

# **Pin Description (continued)**

PIN	NAME	FUNCTION
8	Y	Inverting Input for Receiver Y
9	Y	Non-Inverting Input for Receiver Y
10	YO	Receiver Y Output
11	YFAULT	Open-Drain Fault Output for Receiver Y. \( \overline{YFAULT} \) asserts low during a fault condition on receiver Y. See the \( \overline{FAULT CONDITION} \) section for more information.
12	ZFAULT	Open-Drain Fault Output for Receiver Z. ZFAULT asserts low during a fault condition on receiver Z. See the FAULT CONDITION section for more information.
13	BFAULT	Open-Drain Fault Output for Receiver B. BFAULT asserts low during a fault condition on receiver B. See the <u>FAULT CONDITION</u> section for more information.
14	ZO	Receiver Z Output
15	Z	Non-Inverting Input for Receiver Z
16	Z	Inverting Input for Receiver Z
17	V <sub>CC</sub>	Supply Input. Bypass $V_{CC}$ to ground through a $0.1\mu F$ capacitor as close as possible to the device. $V_{CC}$ must always be greater than or equal to $V_L$ .
18	Ā	Inverting Input for Receiver A
19	А	Non-Inverting Input for Receiver A
20	AO	Receiver A Output
_	EP	Exposed Pad. Connect EP to ground. Not intended as the primary ground connection.

#### **Truth Tables**

# Receiver Fault Logic (RxA, RxB, RxZ, RxY)

DIFFERENTIAL INPUT VOLTAGE	SINGLE-ENDED INPUT VOLTAGE (A, $\overline{A}$ , B, $\overline{B}$ , Z, $\overline{Z}$ , Y, $\overline{Y}$ )	OUTPUT STATE (AO, BO, ZO, YO)	_FAULT	FAULT CONDITION
V <sub>ID</sub> > +0.45V		Н	Н	No Fault
+0.27V < V <sub>ID</sub> < +0.45V	]	Н	Indeterminate	Indeterminate
-0.2V ≤ V <sub>ID</sub> ≤ +0.2V	-20V ≤ V <sub>SE</sub> ≤ +20V	Indeterminate	L	Low Differential Input Voltage Fault
-0.45V ≤ V <sub>ID</sub> ≤ -0.27V		L	Indeterminate	Indeterminate
V <sub>ID</sub> ≤ -0.45V		L	Н	No Fault
X	-40V < V <sub>SE</sub> < -18.5V	VALID*	L	Single-Ended Voltage Fault
Х	-18.5V < V <sub>SE</sub> < -14.5V	VALID*	Indeterminate	Single-Ended Indeterminate Voltage
Х	+14.5V < V <sub>SE</sub> < +18.5V	VALID*	Indeterminate	Single-Ended Indeterminate Voltage
Х	+18.5V < V <sub>SE</sub> < +40V	VALID*	L	Single-Ended Voltage Fault

X = Don't care

<sup>\*</sup>Receiver operates normally, although thresholds may deviate from limits in the Electrical Characteristics table.

# Quad Fault-Protected RS-485/RS-422 Receiver with Fault Detection

#### **Detailed Description**

The MAX14891E quad fault-protected RS-485/RS-422 receiver is ideal for applications requiring high data rates and reduced noise in rugged environments. Each receiver features a wide common-mode input range of -20V to +20V and is guaranteed to receive data at speeds up to 35Mbps

All receiver inputs are fault-protected against voltage shorts in the ±40V range. Per-channel fault-detection provides warning of irregular conditions such as short circuits and open connections.

#### **Detecting Faults**

Signal integrity is essential for reliable system operation. Degraded signals could cause problems ranging from simple errors to loss of data. The MAX14891E detects common RS-4845/RS-422 faults. These faults include low differential input signals, open-wire, short-circuits, and input voltages that are outside normal operating voltage ranges (below -18V and above +18V). See the *Truth Tables* for more information.

#### **Detecting Small Differential Signals**

Each receiver on the MAX14891E is capable of detecting small DC and AC signals. Small DC signals can occur due to open wires or shorts, both of which are explained in the <u>Detecting Short-Circuit and Open-Circuit Faults</u> sections. Small differential AC signals can result from either cable attenuation of long or inadequate cables, or due to poor wiring. The fault comparators are high speed and will detect low AC signal amplitudes up to 35Mbps

#### **Detecting Short-Circuit and Open-Circuit Faults**

The MAX14891E receivers detect short circuits on the inputs. When the A and  $\overline{A}$  inputs are shorted together, the differential input voltage is 0V, generating a small-signal input voltage fault (Figure 4).

Open-circuit detection is similar to detecting a short-circuit condition and relies on the differential termination resistor across the receiver inputs. When an input is open, the termination resistor pulls the non-inverting and inverting inputs to the same voltage, generating a fault condition.

The FAULT output asserts when either a short-circuit or open-circuit condition is detected.

#### **Thermal Shutdown**

The MAX14891E enters thermal shutdown when the chip temperature rises to above 160°C (typ). Receiver outputs are undefined and the \_FAULT outputs are off when the device is in thermal shutdown.

#### **Applications Information**

#### **Cable Termination**

Transmission line termination is required for RS-485/RS-422 high-speed signals on long cables.  $120\Omega$  termination resistors are commonly used to match the characteristic impedance of the cable.

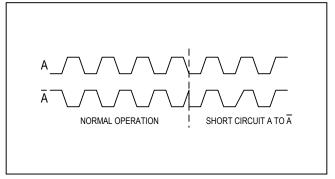
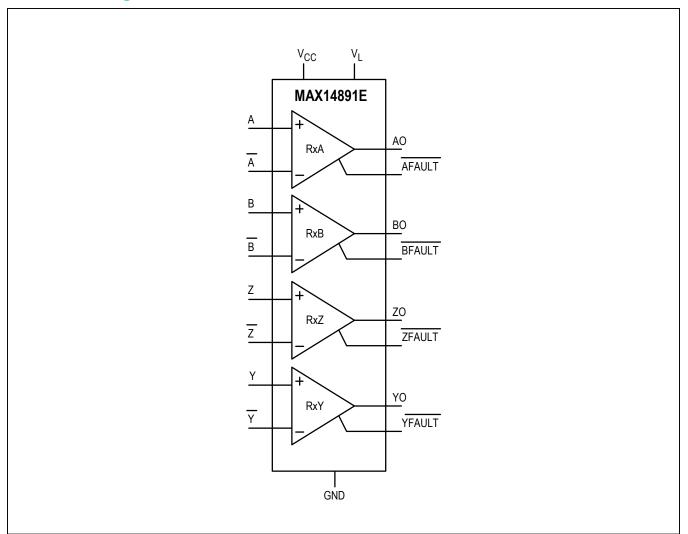


Figure 4. Short-Circuit Detection

# **Functional Diagram**



# **Ordering Information**

	PART	TEMP RANGE	PIN PACKAGE
N	1AX14891EATP+	-40°C to +125°C	20 TQFN-EP
N	1AX14891EATP+T	-40°C to +125°C	20 TQFN-EP

<sup>+</sup>Denotes a lead(Pb)-free/RoHS-compliant package.

T = Tape and reel.

# **Chip Information**

PROCESS: BICMOS

#### **Package Information**

For the latest package outline information and land patterns (footprints), go to <a href="www.maximintegrated.com/packages">www.maximintegrated.com/packages</a>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE	PACKAGE	OUTLINE	LAND
TYPE	CODE	NO.	PATTERN NO.
20 TQFN-EP	T2044-3C	21-0139	90-0037

<sup>\*</sup>EP = Exposed pad.

# Quad Fault-Protected RS-485/RS-422 Receiver with Fault Detection

# **Revision History**

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	12/14	Initial release	_
1	3/15	Added MAX14891EATP+ part number and tightened Electrical Characteristics limits.	1-4, 11

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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