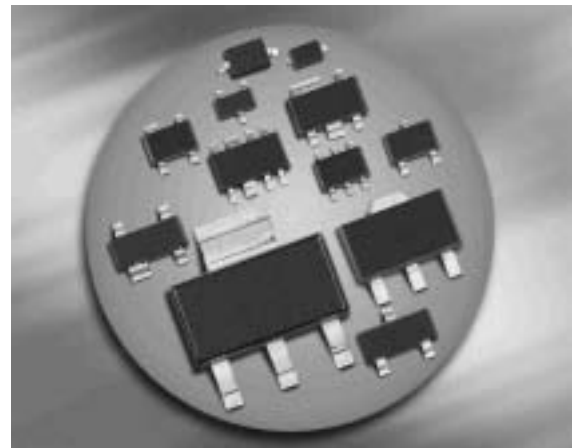


NPN Silicon Digital Transistor

- Switching circuit, inverter, interface circuit, driver circuit
- Built in bias resistor ($R_1=4.7k\Omega$, $R_2=4.7k\Omega$)
- BCR112U: Two internally isolated transistors with good matching in one multichip package
- BCR112U: For orientation in reel see package information below
- Pb-free (RoHS compliant) package¹⁾
- Qualified according AEC Q101



BCR112/F BCR112W



Type	Marking	Pin Configuration						Package
BCR112	WFs	1=B	2=E	3=C	-	-	-	SOT23
BCR112F	WFs	1=B	2=E	3=C	-	-	-	TSFP-3
BCR112W	WFs	1=B	2=E	3=C	-	-	-	SOT323

¹⁾Pb-containing package may be available upon special request

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	50	V
Collector-base voltage	V_{CBO}	50	
Input forward voltage	$V_{i(fwd)}$	30	
Input reverse voltage	$V_{i(rev)}$	10	
Collector current	I_C	100	mA
Total power dissipation- BCR112, $T_S \leq 102^\circ\text{C}$ BCR112F, $T_S \leq 128^\circ\text{C}$ BCR112W, $T_S \leq 124^\circ\text{C}$	P_{tot}	200 250 250	mW
Junction temperature	T_j	150	
Storage temperature	T_{stg}	-65 ... 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾ BCR112 BCR112F BCR112W	R_{thJS}	≤ 240 ≤ 90 ≤ 105 -	K/W

¹⁾For calculation of R_{thJA} please refer to Application Note Thermal Resistance

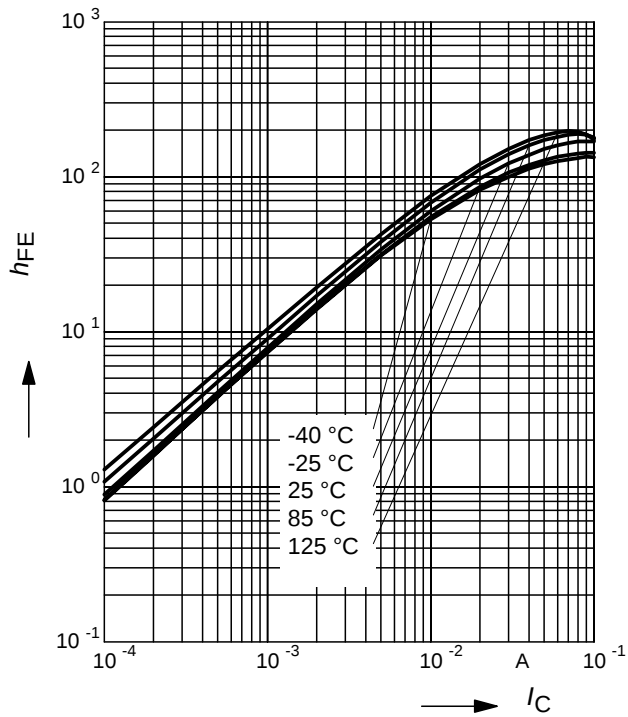
Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage $I_C = 100\ \mu\text{A}$, $I_B = 0$	$V_{(\text{BR})\text{CEO}}$	50	-	-	V
Collector-base breakdown voltage $I_C = 10\ \mu\text{A}$, $I_E = 0$	$V_{(\text{BR})\text{CBO}}$	50	-	-	
Collector-base cutoff current $V_{\text{CB}} = 40\ \text{V}$, $I_E = 0$	I_{CBO}	-	-	100	nA
Emitter-base cutoff current $V_{\text{EB}} = 10\ \text{V}$, $I_C = 0$	I_{EBO}	-	-	1.61	mA
DC current gain ¹⁾ $I_C = 5\ \text{mA}$, $V_{\text{CE}} = 5\ \text{V}$	h_{FE}	20	-	-	-
Collector-emitter saturation voltage ¹⁾ $I_C = 10\ \text{mA}$, $I_B = 0.5\ \text{mA}$	V_{CEsat}	-	-	0.3	V
Input off voltage $I_C = 100\ \mu\text{A}$, $V_{\text{CE}} = 5\ \text{V}$	$V_{\text{i(off)}}$	0.8	-	1.5	
Input on voltage $I_C = 2\ \text{mA}$, $V_{\text{CE}} = 0.3\ \text{V}$	$V_{\text{i(on)}}$	1	-	2.5	
Input resistor	R_1	3.2	4.7	6.2	kΩ
Resistor ratio	R_1/R_2	0.9	1	1.1	-
AC Characteristics					
Transition frequency $I_C = 10\ \text{mA}$, $V_{\text{CE}} = 5\ \text{V}$, $f = 100\ \text{MHz}$	f_{T}	-	140	-	MHz
Collector-base capacitance $V_{\text{CB}} = 10\ \text{V}$, $f = 1\ \text{MHz}$	C_{cb}	-	3	-	pF

¹⁾Pulse test: $t < 300\ \mu\text{s}$; $D < 2\%$

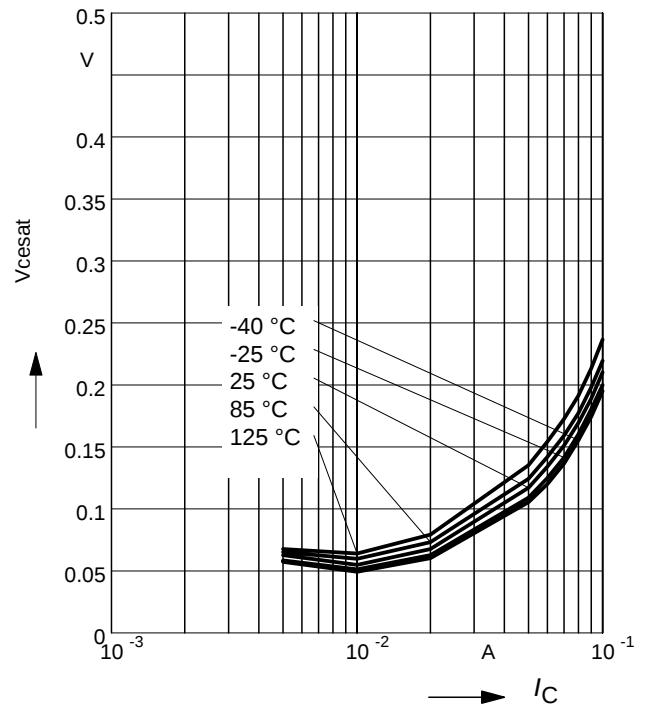
DC current gain $h_{FE} = f(I_C)$

$V_{CE} = 5 \text{ V}$ (common emitter configuration)



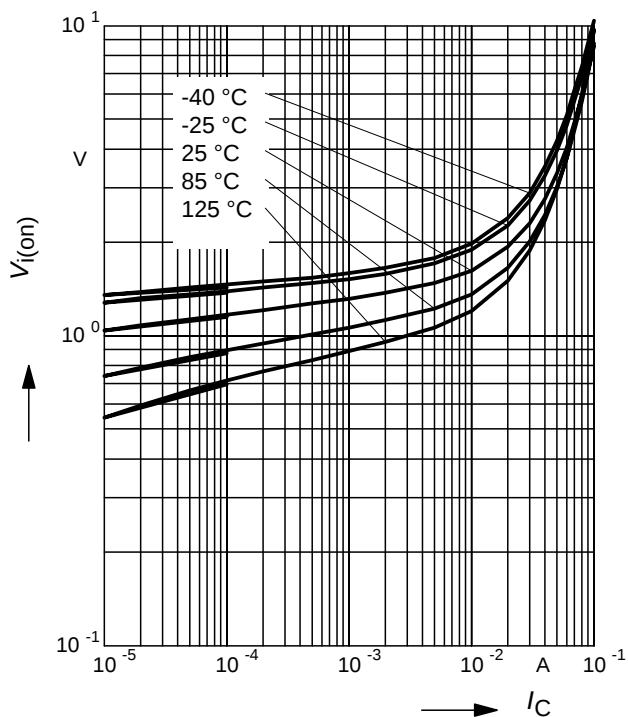
Collector-emitter saturation voltage

$V_{CEsat} = f(I_C), I_C/I_B = 20$



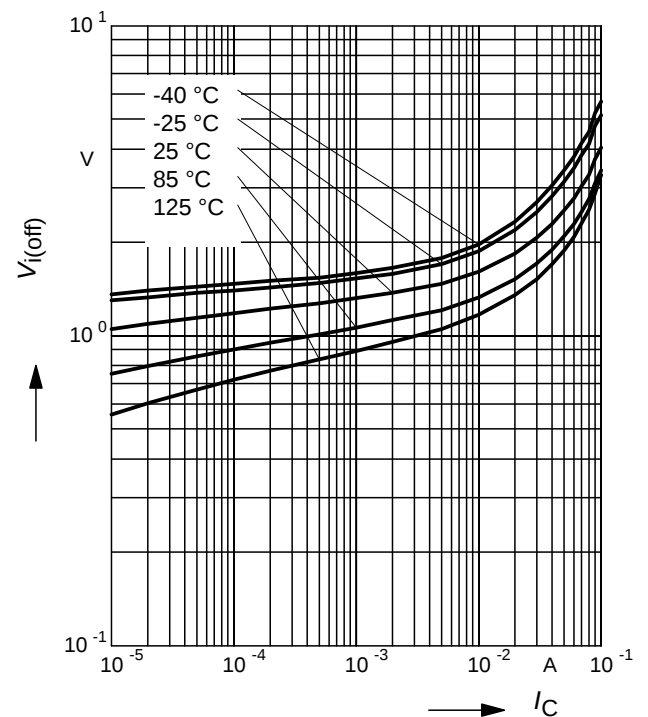
Input on Voltage $V_{i(on)} = f(I_C)$

$V_{CE} = 0.3 \text{ V}$ (common emitter configuration)



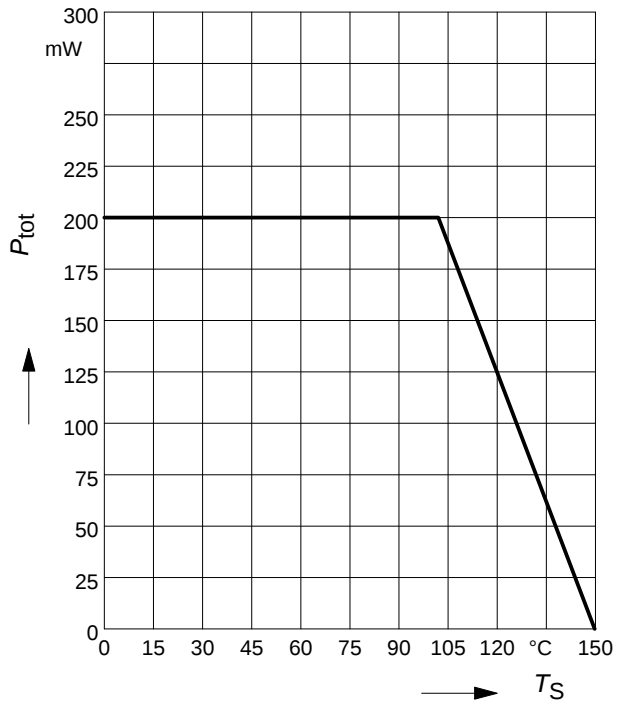
Input off voltage $V_{i(off)} = f(I_C)$

$V_{CE} = 5 \text{ V}$ (common emitter configuration)



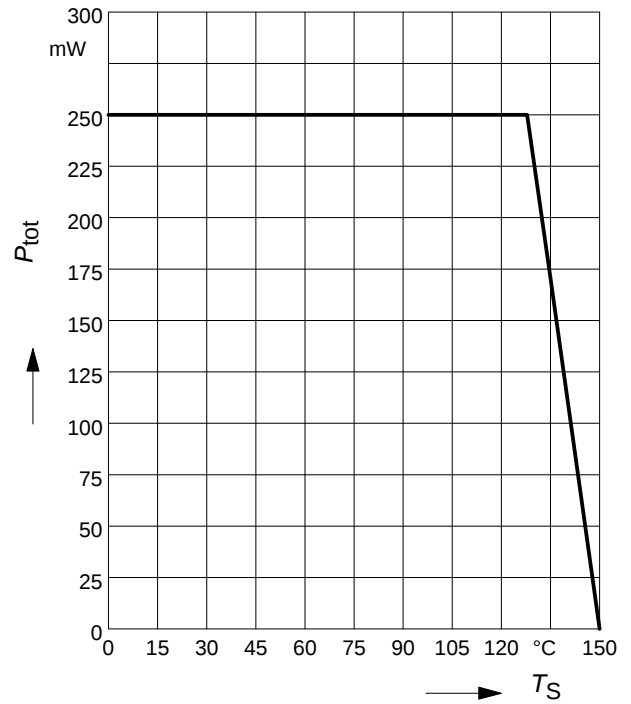
Total power dissipation $P_{\text{tot}} = f(T_S)$

BCR112



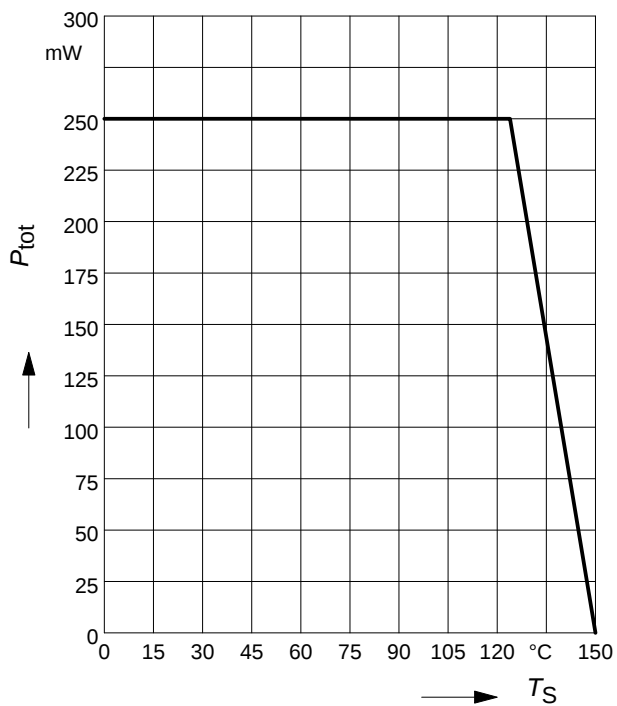
Total power dissipation $P_{\text{tot}} = f(T_S)$

BCR112F



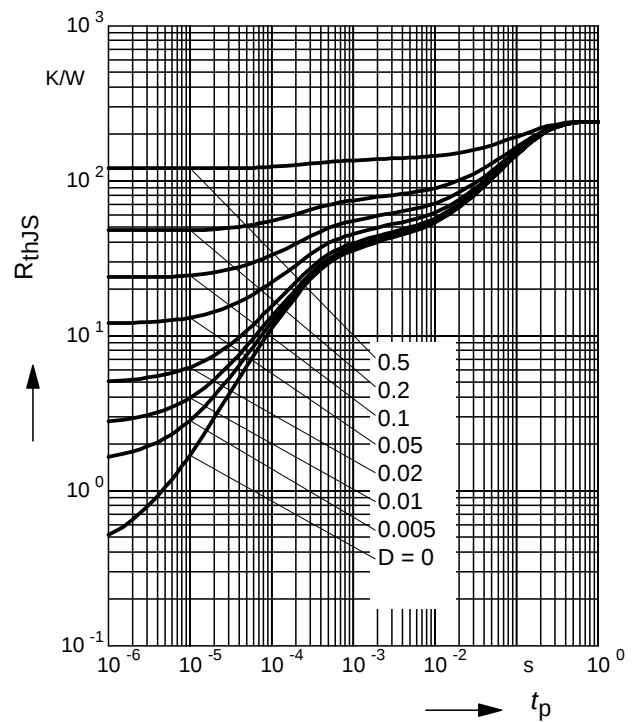
Total power dissipation $P_{\text{tot}} = f(T_S)$

BCR112W



Permissible Pulse Load $R_{\text{thJS}} = f(t_p)$

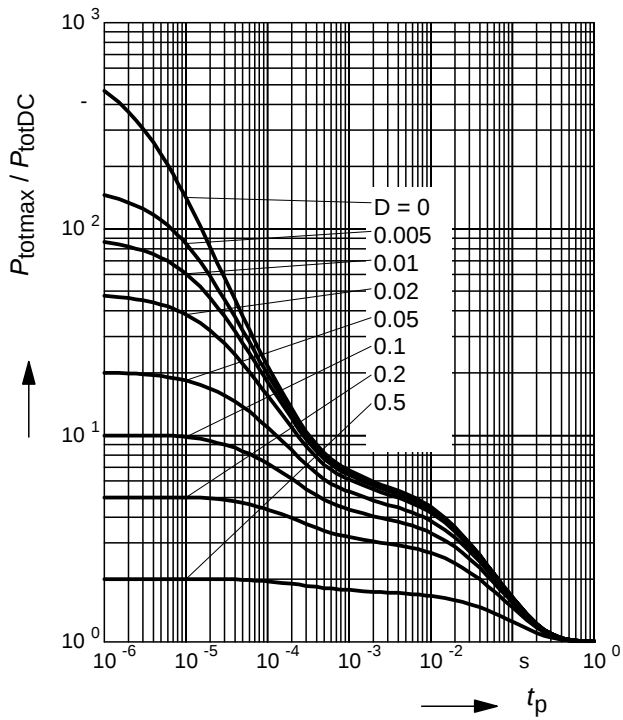
BCR112



Permissible Pulse Load

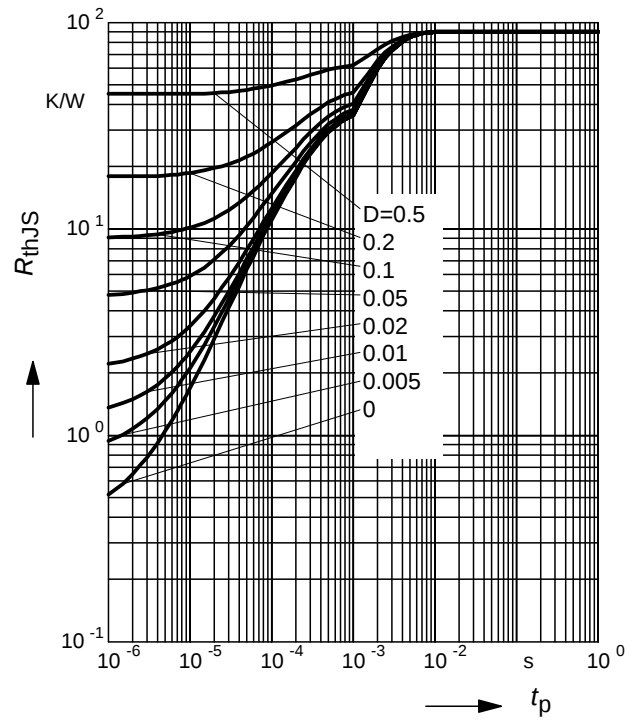
$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$$

BCR112



Permissible Puls Load $R_{\text{thJS}} = f(t_p)$

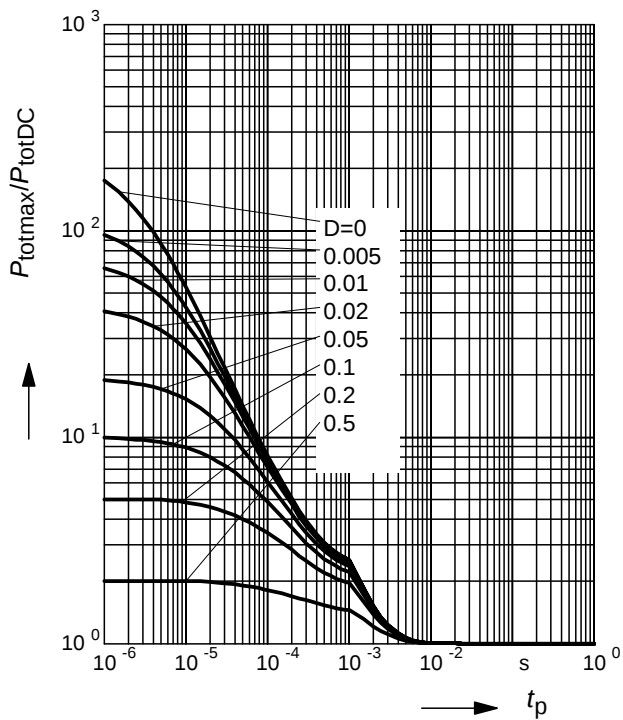
BCR112F



Permissible Pulse Load

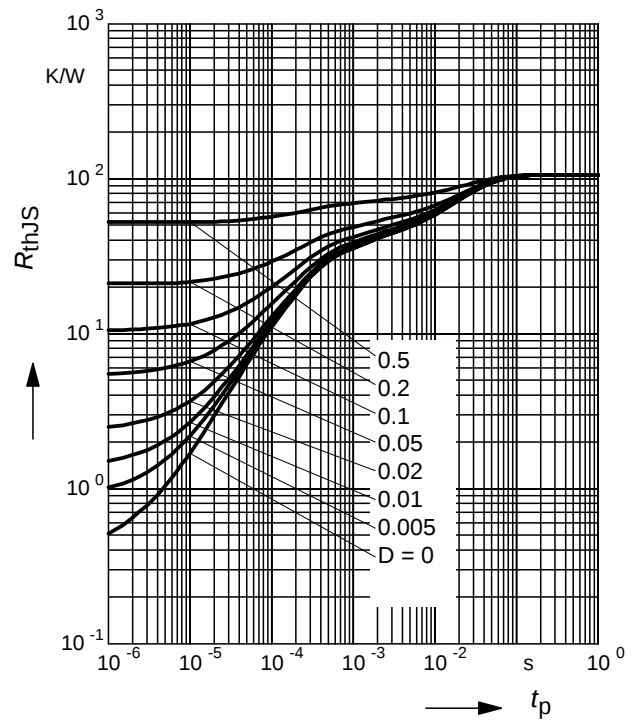
$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$$

BCR112F



Permissible Puls Load $R_{\text{thJS}} = f(t_p)$

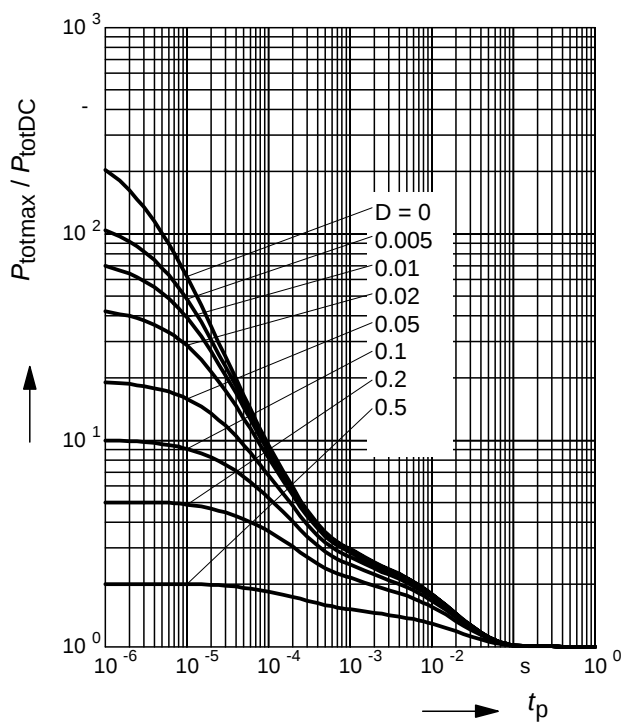
BCR112W

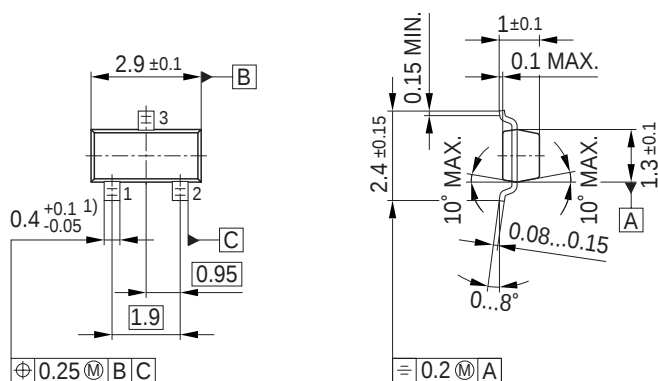


Permissible Pulse Load

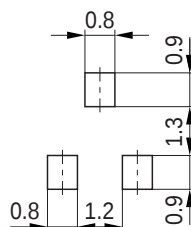
$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$$

BCR112W



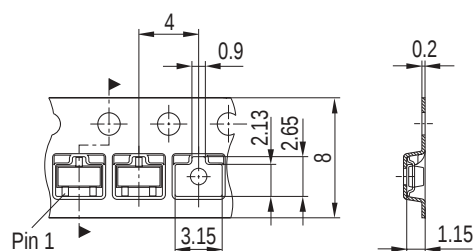


Foot Print

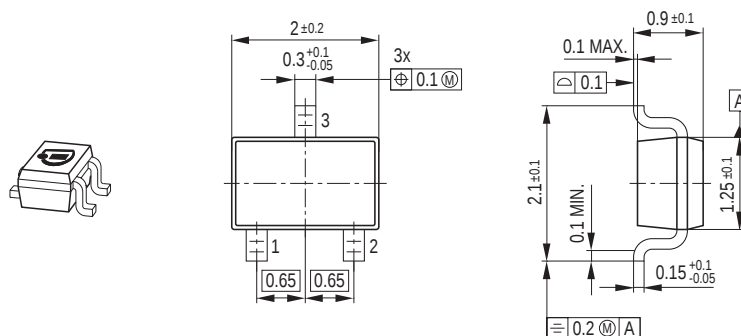


The diagram shows a top-down view of a BCW66 LED package. It is a rectangular component with four pins. The top pin is labeled 'Pin 1'. The package has a central rectangular area containing the text 'EHS' and a date code '05 05'. To the right of the package, the Infineon logo is shown with the text 'Manufacturer'. Below the logo, the text '2005, June' is shown with the text 'Date code (YM)'. Below that, the text 'BCW66' is shown with the text 'Type code'.

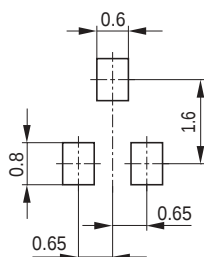
Reel ø180 mm = 3.000 Pieces/Reel
Reel ø330 mm = 10.000 Pieces/Reel



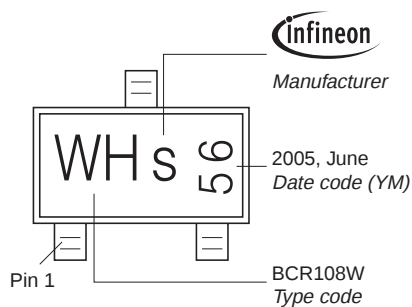
Package Outline



Foot Print

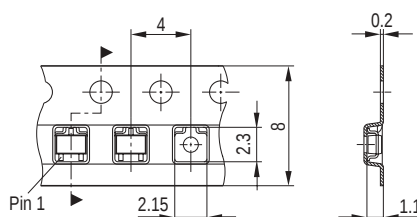


Marking Layout (Example)



Standard Packing

Reel $\varnothing 180$ mm = 3.000 Pieces/Reel
 Reel $\varnothing 330$ mm = 10.000 Pieces/Reel



[illegible]

Technical drawing of a mechanical part. The drawing shows a top view and a side view. The top view is a rectangle with a width of 0.4 and a height of 0.45. The side view is a rectangle with a width of 0.4 and a height of 1.05. The drawing is labeled with dimensions: 0.4, 0.45, 1.05, and 0.4.

Pin 1

BCR847BF

Manufacturer

Technical drawing of a mechanical part with dimensions. The drawing shows a side view and a cross-section. Dimensions include: 4, 0.3, 1.2, 1.5, 8, 0.2, 0.7, 1.35, and Pin 1.

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