

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$ max	I_D MAX $T_A = +25^\circ\text{C}$
-20V	70m Ω @ $V_{GS} = -4.5\text{V}$	-3.8A
	85m Ω @ $V_{GS} = -2.5\text{V}$	-3.3A

Description

This MOSFET is designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

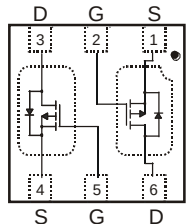
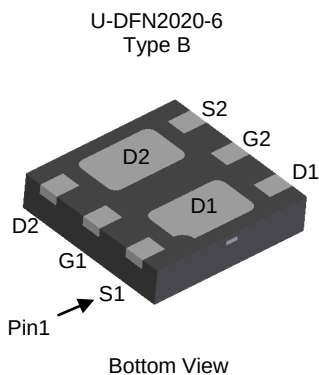
- Load Switch
- Power Management Functions
- Portable Power Adaptors

Features

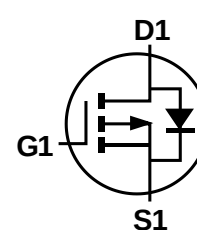
- Low On-Resistance
- Low Gate Threshold Voltage, -0.9V Max
- Fast Switching Speed
- Low Input/Output Leakage
- Low Profile, 0.5mm Max Height
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Mechanical Data

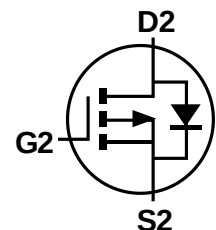
- Case: U-DFN2020-6 Type B
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – NiPdAu Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 Ⓔ4
- Weight: 0.0065 grams (Approximate)



Top View
Internal Schematic



Q1 P-CHANNEL



Q2 P-CHANNEL

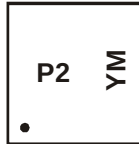
Internal Schematic

Ordering Information (Note 5)

Part Number	Case	Packaging
DMP2160UFDBQ-7	U-DFN2020-6 Type B	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



P2 = Marking Code
 YM = Date Marking
 Y = Year (ex: V = 2008)
 M = Month (ex: 9 = September)
 Dot denotes Pin 1

Date Code Key

Year	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018
Code	V	W	X	Y	Z	A	B	C	D	E	F

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	-20	V
Gate-Source Voltage	V _{GSS}	±12	V
Drain Current (Note 6)	I _D	-3.8	A
Pulsed Drain Current (Note 7)	I _{DM}	-13	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P _D	1.4	W
Thermal Resistance, Junction to Ambient	R _{θJA}	89	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100 ±800	nA	V _{GS} = ±8V, V _{DS} = 0V V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	-0.45	—	-0.9	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	54	70	mΩ	V _{GS} = -4.5V, I _D = -2.8A
		—	68	85		V _{GS} = -2.5V, I _D = -2.0A
		—	86	—		V _{GS} = -1.8V, I _D = -1.0A
Forward Transfer Admittance	Y _{fs}	—	8	—	S	V _{DS} = -5V, I _D = -2.8A
Diode Forward Voltage (Note 8)	V _{SD}	—	0.7	-1.2	V	V _{GS} = 0V, I _S = -1.6A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	536	—	pF	V _{DS} = -10V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	68	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	59	—	pF	
Gate Resistance	R _g	—	8.72	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	6.5	—	nC	V _{GS} = -4.5V, V _{DD} = -10V, I _D = -1.5A
Gate-Source Charge	Q _{gs}	—	0.8	—	nC	
Gate-Drain Charge	Q _{gd}	—	1.4	—	nC	
Turn-On Delay Time	t _{D(on)}	—	11.51	—	ns	V _{GEN} = -4.5V, V _{DD} = -10V, R _L = 10Ω, R _G = 6Ω
Turn-On Rise Time	t _r	—	12.09	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	55.34	—	ns	
Turn-Off Fall Time	t _f	—	27.54	—	ns	

- Notes:
- Device mounted on FR-4 PCB, on minimum recommended, 2oz Copper pad layout.
 - Repetitive rating, pulse width limited by junction temperature.
 - Short duration pulse test used to minimize self-heating effect.

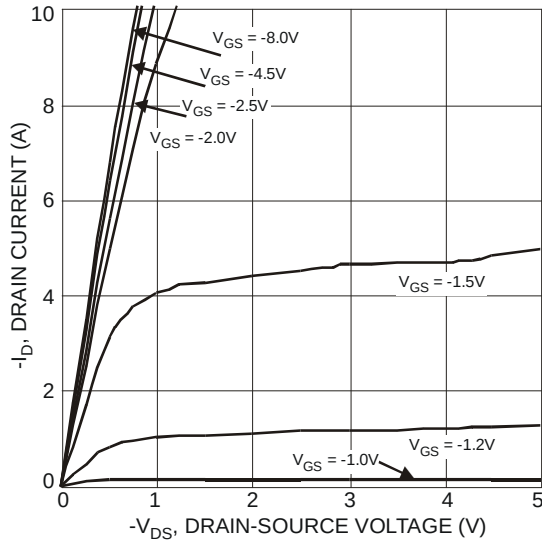


Fig. 1 Typical Output Characteristics

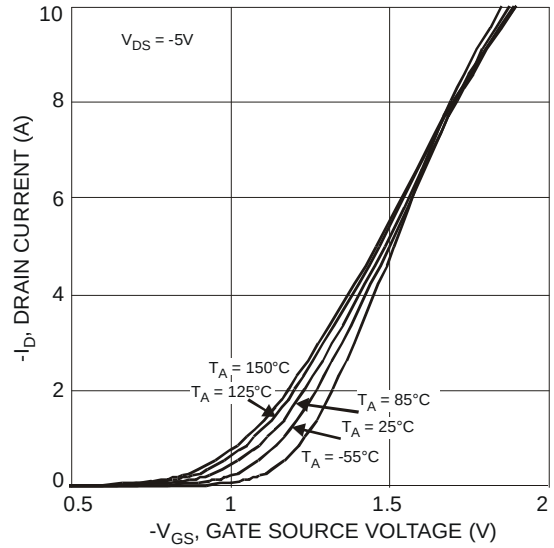


Fig. 2 Typical Transfer Characteristics

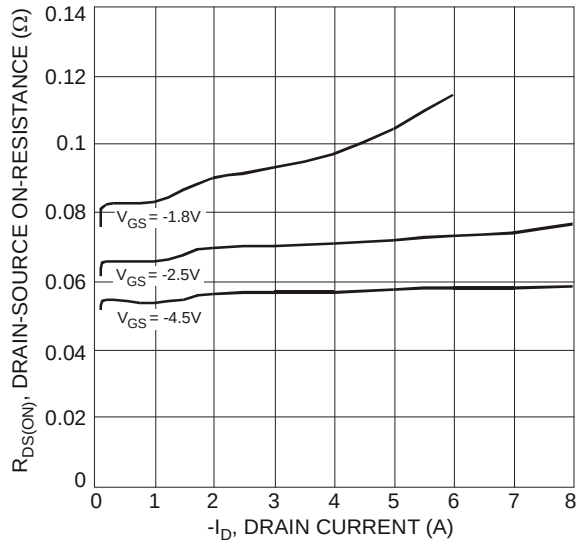


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

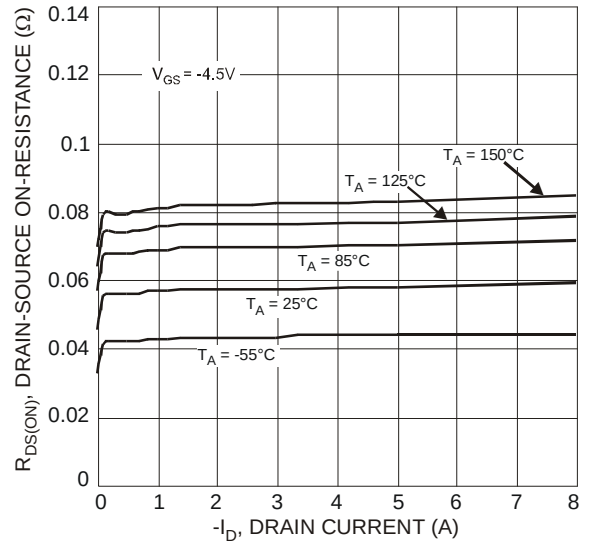


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

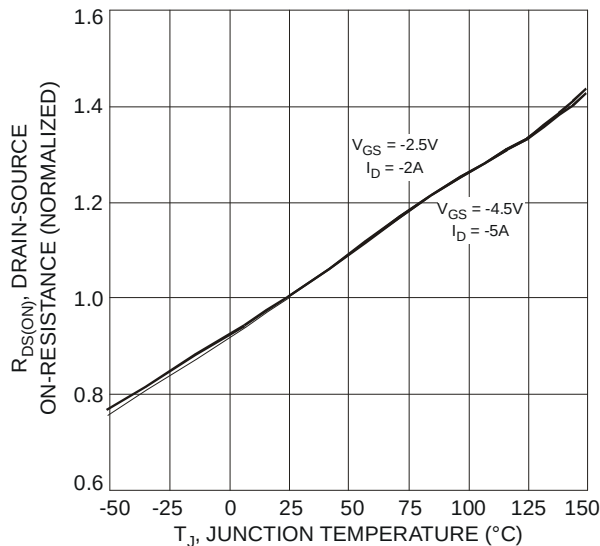


Fig. 5 On-Resistance Variation with Temperature

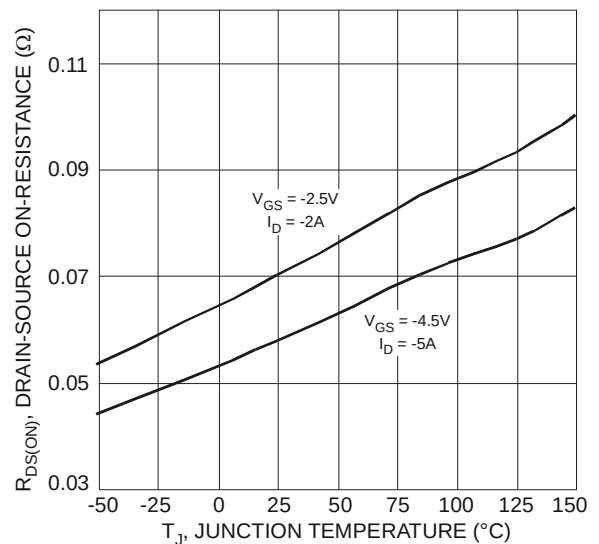
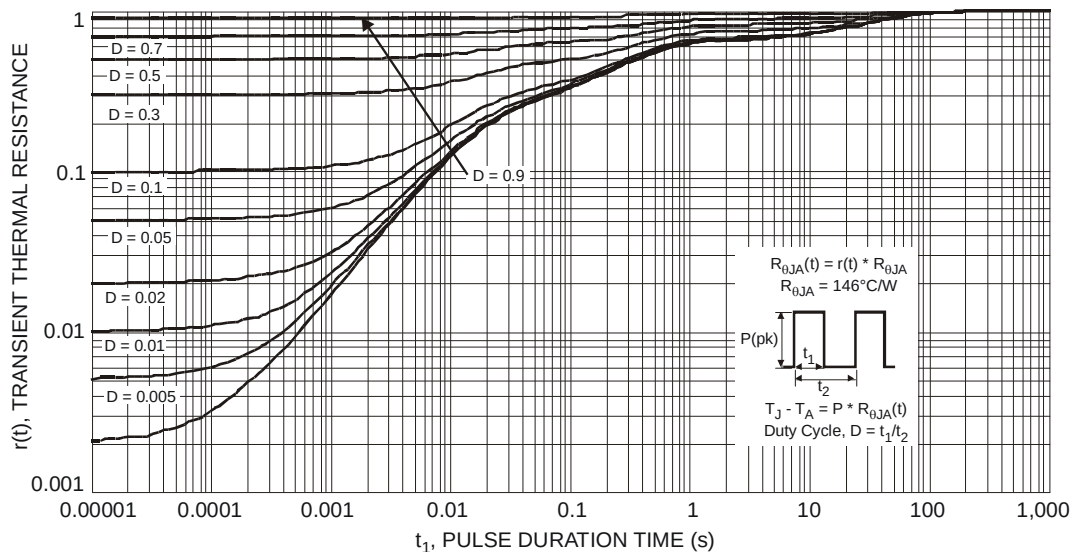
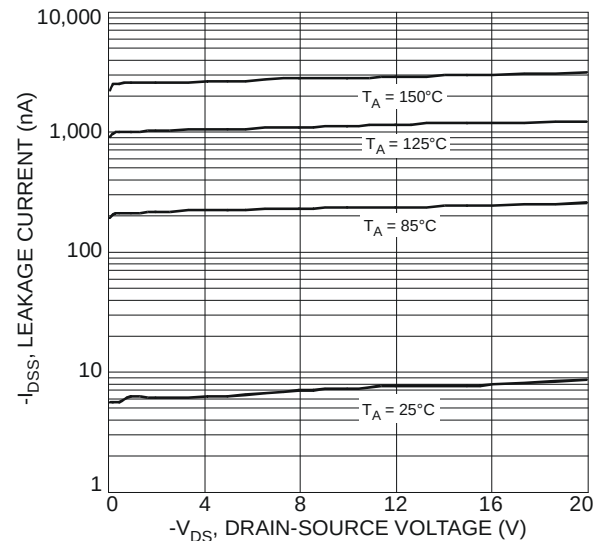
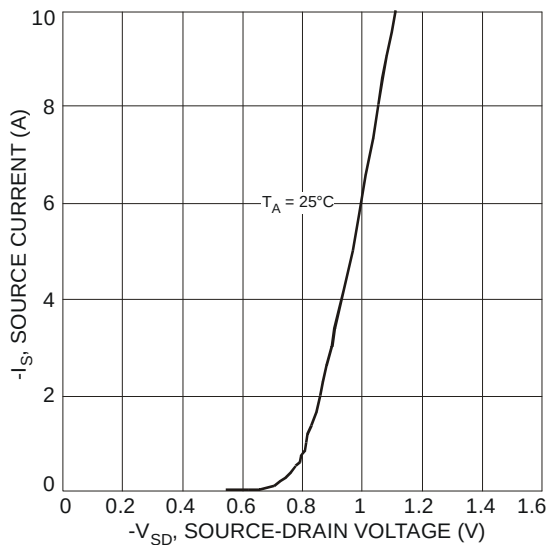
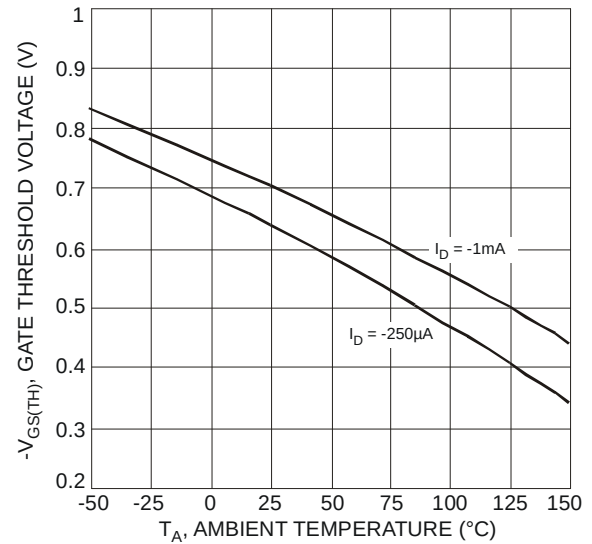
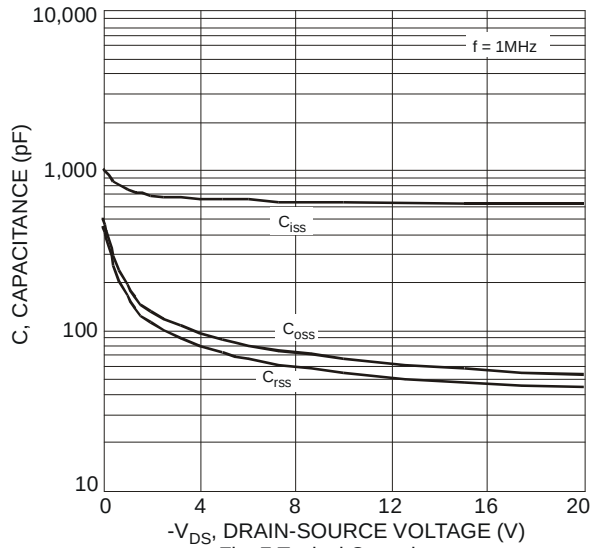
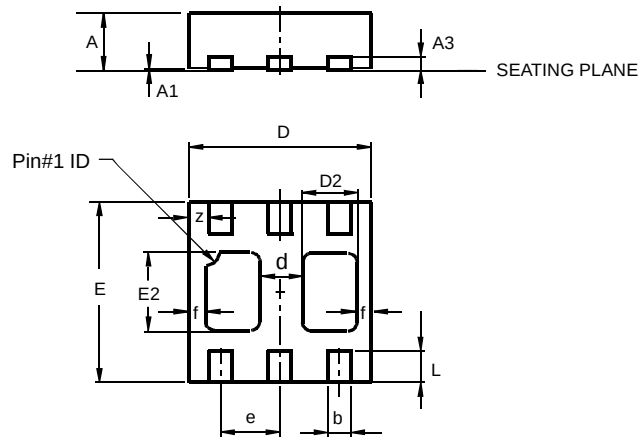


Fig. 6 On-Resistance Variation with Temperature



Package Outline Dimensions

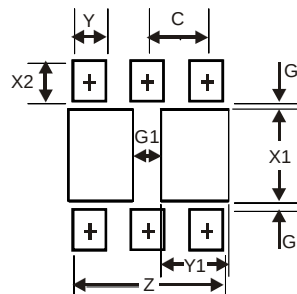
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



U-DFN2020-6 Type B			
Dim	Min	Max	Typ
A	0.545	0.605	0.575
A1	0	0.05	0.02
A3	—	—	0.13
b	0.20	0.30	0.25
D	1.95	2.075	2.00
d	—	—	0.45
D2	0.50	0.70	0.60
e	—	—	0.65
E	1.95	2.075	2.00
E2	0.90	1.10	1.00
f	—	—	0.15
L	0.25	0.35	0.30
z	—	—	0.225
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	1.67
G	0.20
G1	0.40
X1	1.0
X2	0.45
Y	0.37
Y1	0.70
C	0.65

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