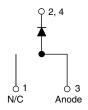


HALOGEN FREE

HEXFRED®, Ultrafast Soft Recovery Diode, 4 A

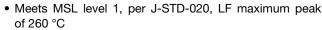




D-PAK	(TO-252AA)
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FEATURES

- Ultrafast recovery time
- Ultrasoft recovery
- Very low I_{RRM}
- Very low Q_{rr}
- Guaranteed avalanche
- Specified at operating temperature
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test



 Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

PRODUCT SUMMARY			
Package	TO-252AA (D-PAK)		
I _{F(AV)}	4 A		
V_{R}	600 V		
V _F at I _F	1.8 V		
t _{rr} typ.	17 ns		
T_{J} max.	150 °C		
Diode variation	Single die		

BENEFITS

- Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- Higher frequency operation
- Reduced snubbing
- · Reduced parts count

DESCRIPTION/APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for freewheeling, flyback, power converters, motor drives, and other applications where high speed and reduced switching losses are design requirements.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Cathode to anode voltage	V_{RRM}		600	V
Maximum continuous forward current	I _{F(AV)}	T _C = 100 °C	4	
Single pulse forward current	I _{FSM}		25	Α
Repetitive peak forward current	I _{FRM}	T _C = 116 °C	16	
Maximum power dissipation	P_{D}	T _C = 100 °C	10	W
Operating junction and storage temperatures	T_J, T_{Stg}		- 55 to 150	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-	
Forward voltage V _F	I _F = 4 A	-	1.5	1.8	V	
	I _F = 8 A	-	1.8	2.2		
See lig. 1		I _F = 4 A, T _J = 125 °C	-	1.4	1.7	
Maximum reverse		V _R = V _R rated	-	0.17	3.0	
leakage current	I _R	$T_J = 125 ^{\circ}\text{C}$, $V_R = 0.8 ^{\circ}\text{X}$ V_R rated	-	44	300	μΑ
Junction capacitance	C _T	V _R = 200 V	-	4	8	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH





DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt =$	200 A/ μ A, $V_R = 30 V$	-	17	-	
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 4 A	-	28	42	ns
		T _J = 125 °C		-	38	57	
Deal area area	I _{RRM}	T _J = 25 °C		-	2.9	5.2	^
Peak recovery current		T _J = 125 °C		-	3.7	6.7	Α
Reverse recovery charge	0	T _J = 25 °C	dl _F /dt = 200 A/μs V _R = 200 V	-	40	60	nC
Reverse recovery charge	Q_{rr} $T_J = 125 °C$		-	70	105	IIC IIC	
Data of fall of vectors as went	te of fall of recovery current dl _{(rec)M} /dt	T _J = 25 °C		-	280	-	Λ/110
Rate of fall of recovery current		T _J = 125 °C		-	235	-	A/μs

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55	-	150	°C
Thermal resistance, junction to case	R _{thJC}		-	-	5.0	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	80	- C/VV
Majaht			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D-PAK	HFA04SD60SH			

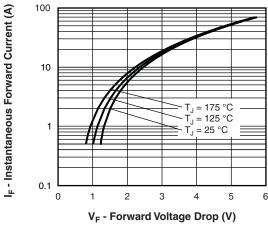


Fig. 1 - Typical Forward Voltage Drop Characteristics

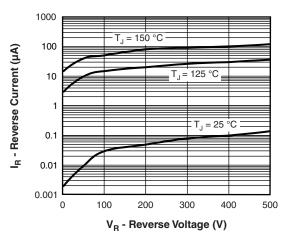


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

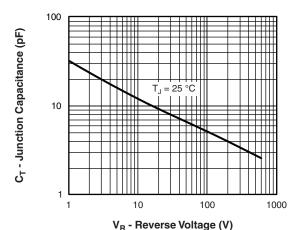


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

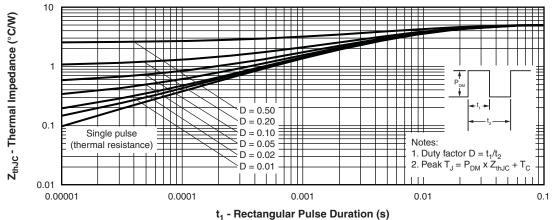


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





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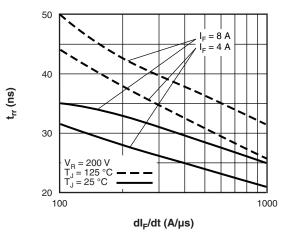


Fig. 5 - Typical Reverse Recovery Time vs. dl_F/dt

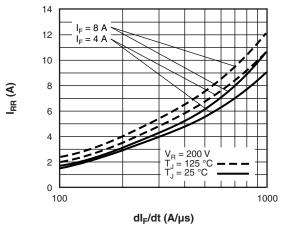


Fig. 6 - Typical Recovery Current vs. dl_F/dt

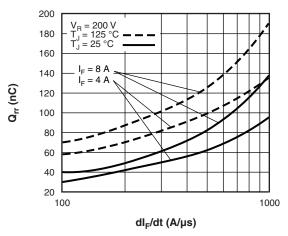


Fig. 7 - Typical Stored Charge vs. dl_F/dt

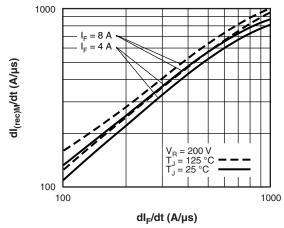


Fig. 8 - Typical dl_{(rec)M}/dt vs. dl_F/dt

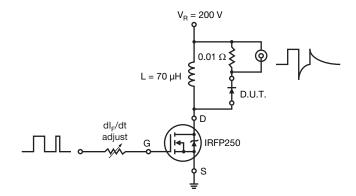
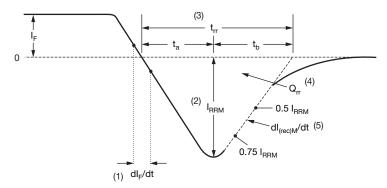


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_{F}$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

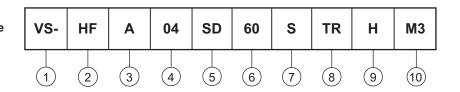
(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - HEXFRED® family

3 - Electron irradiated

- Current rating (04 = 4 A)

5 - D-PAK

6 - Voltage rating (60 = 600 V)

7 - S = D-PAK

8 - • TR = Tape and reel

• R = Tape and reel (right oriented)

• L = Tape and reel (left oriented)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-HFA04SD60SHM3	75	3000	Antistatic plastic tube	
VS-HFA04SD60STRHM3	2000	2000	13" diameter reel	
VS-HFA04SD60STRRHM3	3000	3000	13" diameter reel	
VS-HFA04SD60STRLHM3	3000	3000	13" diameter reel	

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?95033			



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Vishay

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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Revision: 02-Oct-12 Document Number: 91000

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