


## "Half-Bridge" IGBT INT-A-PAK (Ultrafast Speed IGBT), 75 A



INT-A-PAK

**FEATURES**

- Generation 4 IGBT technology
- Ultrafast: Optimized for high speed 8 kHz to 40 kHz in hard switching, > 200 kHz in resonant mode
- Very low conduction and switching losses
- HEXFRED® antiparallel diodes with ultrasoft recovery
- Industry standard package
- UL approved file E78996 
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level


**RoHS**  
COMPLIANT

**PRODUCT SUMMARY**

$V_{CES}$	1200 V
$I_C$ DC	110 A
$V_{CE(on)}$ at 75 A, 25 °C	2.5 V

**BENEFITS**

- Increased operating efficiency
- Direct mounting to heatsink
- Performance optimized for power conversion: UPS, SMPS, welding
- Lower EMI, requires less snubbing

**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	$V_{CES}$		1200	V
Continuous collector current	$I_C$	$T_C = 25\text{ °C}$	110	A
		$T_C = 76\text{ °C}$	75	
Pulsed collector current	$I_{CM}$	Repetitive rating; $V_{GE} = 20\text{ V}$ , pulse width limited by maximum junction temperature	150	
Peak switching current See fig. 17	$I_{LM}$		150	
Peak diode forward current	$I_{FM}$		150	V
Gate to emitter voltage	$V_{GE}$		$\pm 20$	
RMS isolation voltage	$V_{ISOL}$	Any terminal to case, $t = 1\text{ minute}$	2500	W
Maximum power dissipation	$P_D$	$T_C = 25\text{ °C}$	390	
		$T_C = 85\text{ °C}$	200	
Operating junction temperature range	$T_J$		- 40 to + 150	°C
Storage temperature range	$T_{Stg}$		- 40 to + 125	

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ELECTRICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA	1200	-	-	V
Collector to emitter voltage	V <sub>CE(on)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 75 A	-	2.5	3.7	
		I <sub>C</sub> = 75 A, V <sub>GE</sub> = 15 V, T <sub>J</sub> = 125 °C	-	2.25	3.3	
Gate threshold voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> = 6.0 V, I <sub>C</sub> = 750 μA	3.0	4.5	6.0	
Temperature coefficient of threshold voltage	ΔV <sub>GE(th)/ΔT<sub>J</sub></sub>		-	- 14	-	mV/°C
Forward transconductance	g <sub>fe</sub>	V <sub>CE</sub> = 25 V, I <sub>C</sub> = 75 A Pulse width 50 μs, single shot	-	107	-	S
Collector to emitter leaking current	I <sub>CES</sub>	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V	-	0.03	1.0	mA
		V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>J</sub> = 125 °C	-	4.3	10	
Diode forward voltage	V <sub>F</sub>	V <sub>GE</sub> = 0 V, I <sub>F</sub> = 75 A	-	3	3.6	V
		I <sub>F</sub> = 75 A, V <sub>GE</sub> = 0 V, T <sub>J</sub> = 125 °C	-	2.83	3.3	
Gate to emitter leakage current	I <sub>GES</sub>	V <sub>GE</sub> = ± 20 V	-	-	250	nA

SWITCHING CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Q <sub>g</sub>	V <sub>CC</sub> = 400 V I <sub>C</sub> = 85 A	-	570	854	nC
Gate to emitter charge (turn-on)	Q <sub>ge</sub>		-	96	144	
Gate to collector charge (turn-on)	Q <sub>gc</sub>		-	189	283	
Turn-on delay time	t <sub>d(on)</sub>	R <sub>g1</sub> = 15 Ω R <sub>g2</sub> = 0 Ω I <sub>C</sub> = 75 A	-	437	-	ns
Rise time	t <sub>r</sub>		-	60	-	
Turn-off delay time	t <sub>d(off)</sub>	V <sub>CC</sub> = 720 V	-	395	-	
Fall time	t <sub>f</sub>		-	245	-	
Turn-on switching energy	E <sub>on</sub>	V <sub>GE</sub> = ± 15 V Inductor load T <sub>J</sub> = 25 °C	-	5	-	mJ
Turn-off switching energy	E <sub>off</sub> <sup>(1)</sup>		-	3	-	
Total switching energy	E <sub>ts</sub> <sup>(1)</sup>		-	8	-	
Turn-on delay time	t <sub>d(on)</sub>	R <sub>g1</sub> = 15 Ω R <sub>g2</sub> = 0 Ω I <sub>C</sub> = 75 A	-	453	-	ns
Rise time	t <sub>r</sub>		-	70	-	
Turn-off delay time	t <sub>d(off)</sub>	V <sub>CC</sub> = 720 V	-	415	-	
Fall time	t <sub>f</sub>		-	661	-	
Turn-on switching energy	E <sub>on</sub>	V <sub>GE</sub> = ± 15 V Inductor load T <sub>J</sub> = 125 °C	-	8	-	mJ
Turn-off switching energy	E <sub>off</sub> <sup>(1)</sup>		-	11	-	
Total switching energy	E <sub>ts</sub> <sup>(1)</sup>		-	19	32	
Input capacitance	C <sub>ies</sub>	V <sub>GE</sub> = 0 V	-	12 815	-	pF
Output capacitance	C <sub>oes</sub>	V <sub>CC</sub> = 30 V	-	570	-	
Reverse transfer capacitance	C <sub>res</sub>	f = 1 MHz	-	110	-	
Diode reverse recovery time	t <sub>rr</sub>	R <sub>g1</sub> = 15 Ω R <sub>g2</sub> = 0 Ω I <sub>C</sub> = 75 A	-	174	-	ns
Diode peak reverse current	I <sub>rr</sub>		-	107	-	A
Diode recovery charge	Q <sub>rr</sub>	V <sub>CC</sub> = 720 V	-	9367	-	nC
Diode peak rate of fall of recovery during t <sub>b</sub>	di <sub>(rec)</sub> /dt		di/dt = 1300 A/μs	-	1491	-

**Note**

<sup>(1)</sup> Repetitive rating; V<sub>GE</sub> = 20 V, pulse width limited by maximum junction temperature

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R <sub>thJC</sub>	IGBT	-	0.32	°C/W
		Diode	-	0.35	
Thermal resistance, case to sink per module	R <sub>thCS</sub>		0.1	-	
Mounting torque	case to heatsink		-	4.0	Nm
	case to terminal 1, 2 and 3	For screws M5 x 0.8	-	3.0	
Weight of module			200	-	g

# "Half-Bridge" IGBT INT-A-PAK Vishay High Power Products (Ultrafast Speed IGBT), 75 A

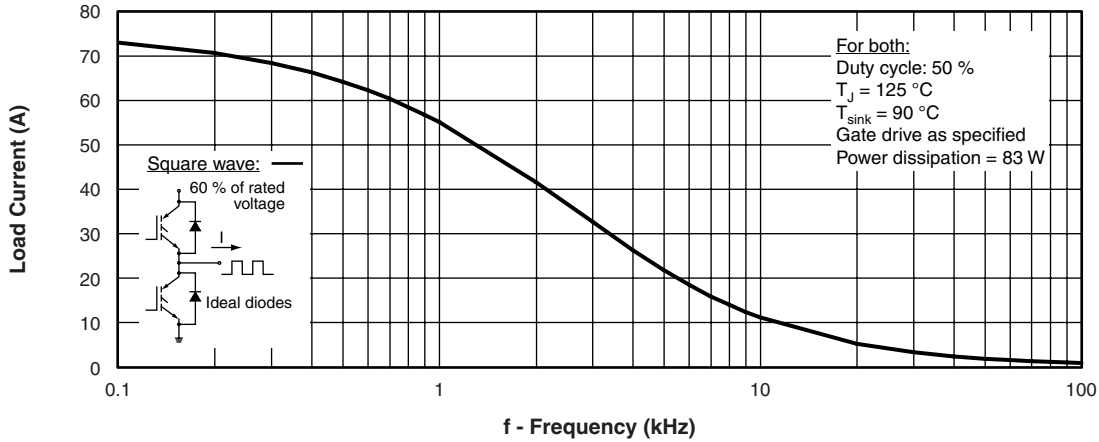


Fig. 1 - Typical Load Current vs. Frequency  
(Load Current =  $I_{RMS}$  of Fundamental)

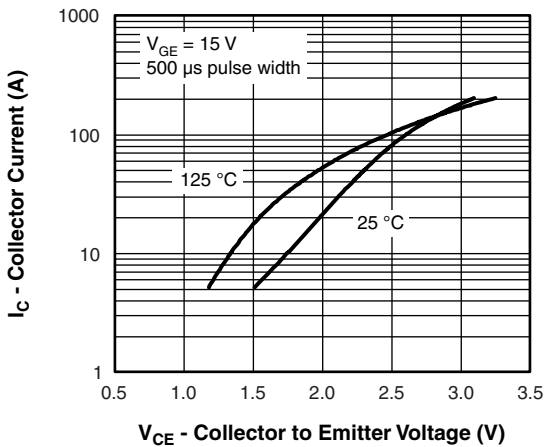


Fig. 2 - Typical Output Characteristics

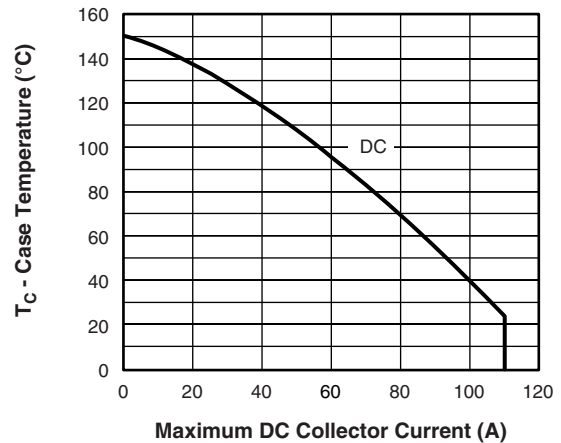


Fig. 4 - Case Temperature vs.  
Maximum Collector Current

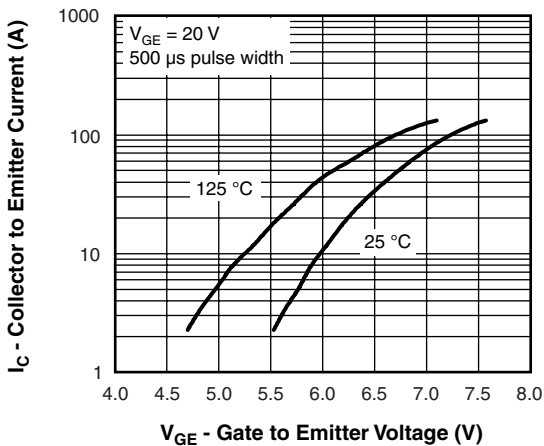


Fig. 3 - Typical Transfer Characteristics

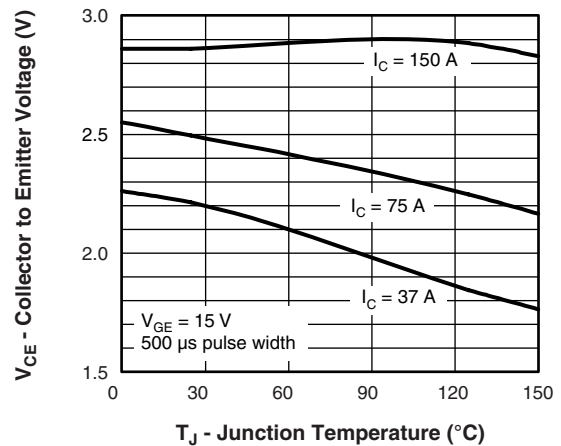


Fig. 5 - Typical Collector to Emitter Voltage vs.  
Junction Temperature

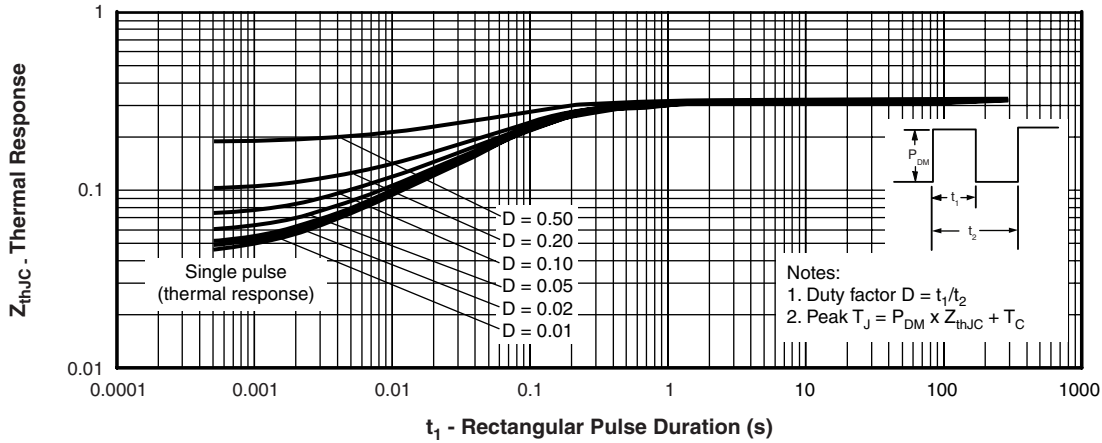


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction to Case

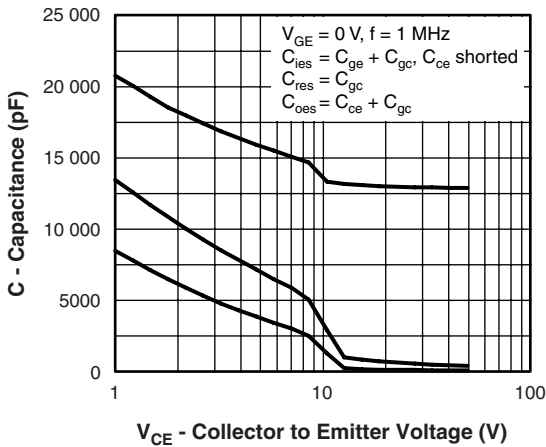


Fig. 7 - Typical Capacitance vs. Collector to Emitter Voltage

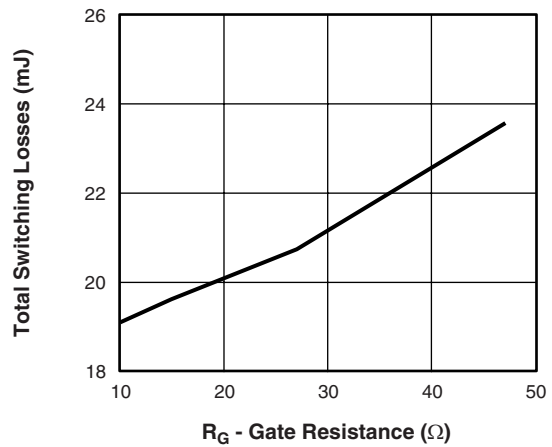


Fig. 9 - Typical Switching Losses vs. Gate Resistance

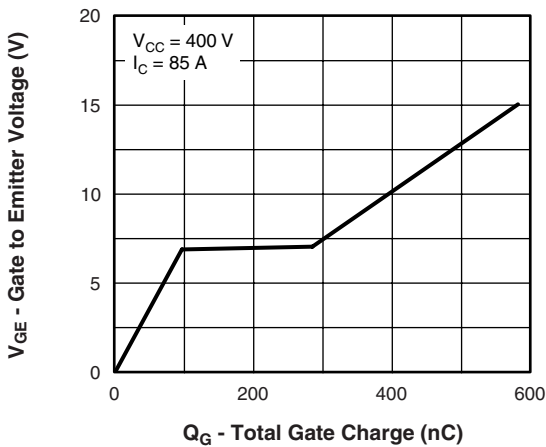


Fig. 8 - Typical Gate Charge vs. Gate to Emitter Voltage

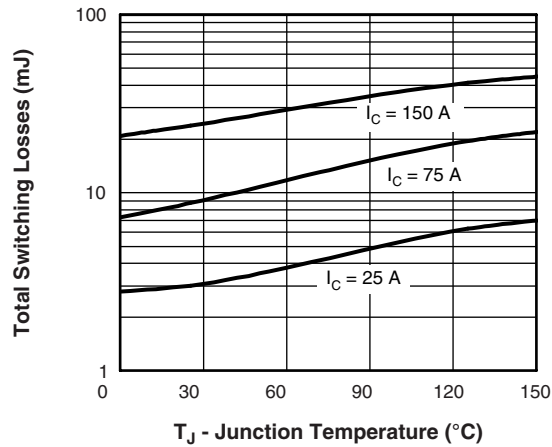


Fig. 10 - Typical Switching Losses vs. Junction Temperature

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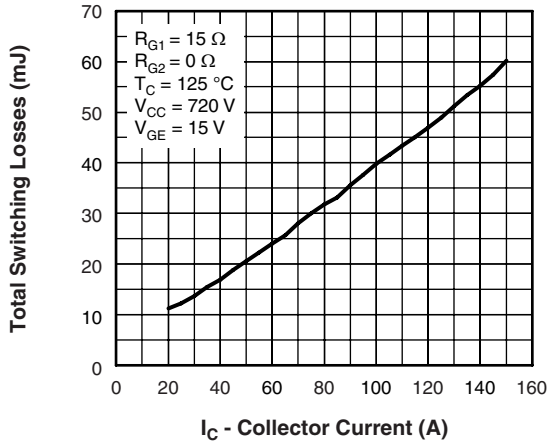


Fig. 11 - Typical Switching Losses vs. Collector to Emitter Current

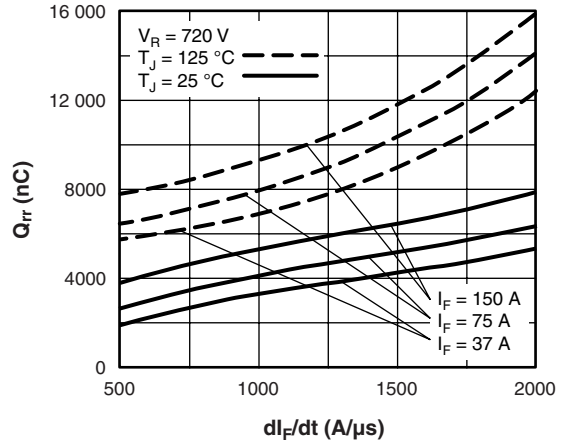


Fig. 14 - Typical Stored Charge vs.  $di_F/dt$

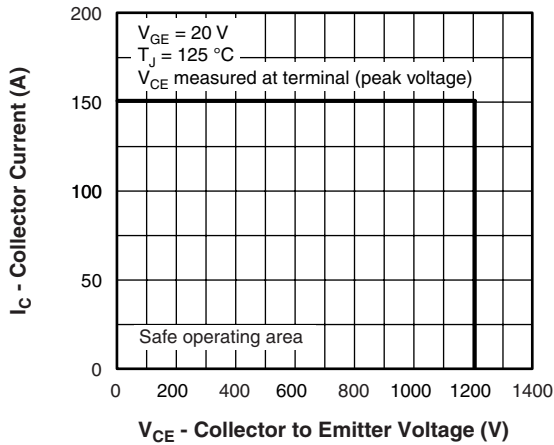


Fig. 12 - Reverse Bias SOA

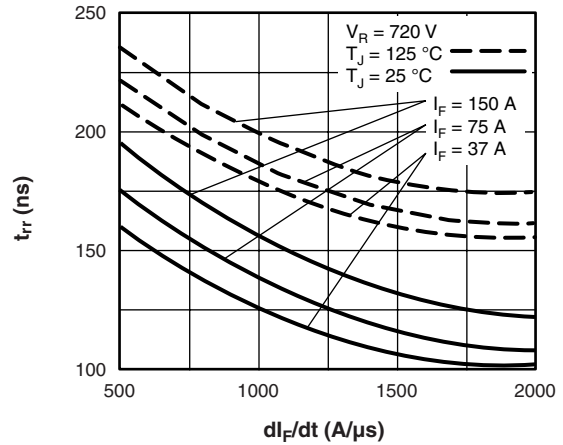


Fig. 15 - Typical Reverse Recovery Time vs.  $di_F/dt$

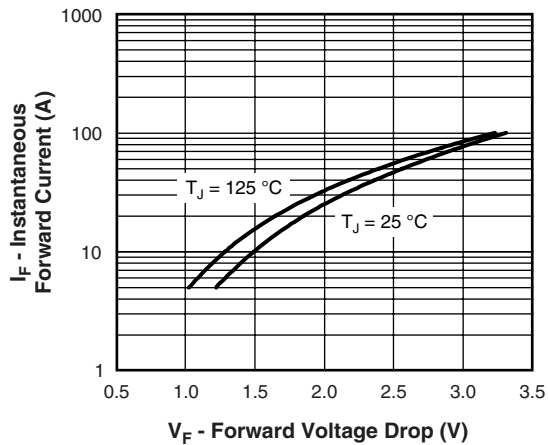


Fig. 13 - Typical Forward Voltage Drop vs. Instantaneous Forward Current

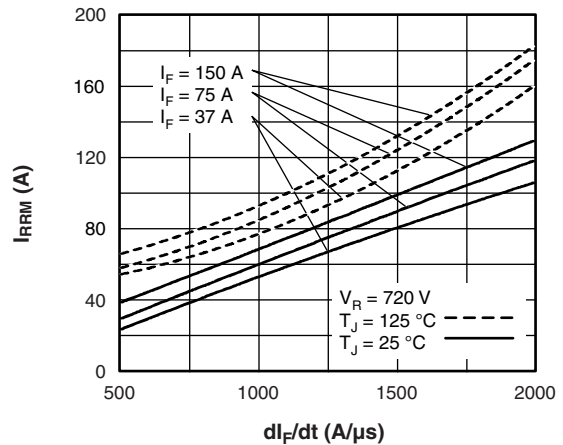


Fig. 16 - Typical Recovery Current vs.  $di_F/dt$

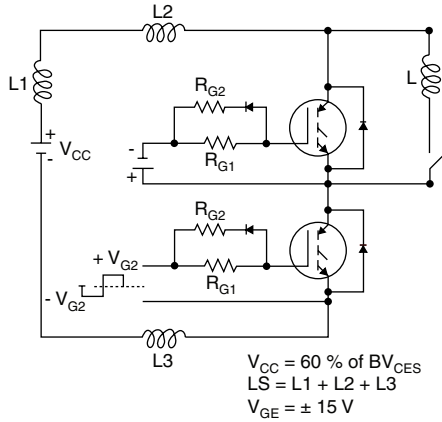


Fig. 17a - Test Circuit for Measurement of  $I_{LM}$ ,  $E_{on}$ ,  $E_{off}(\text{diode})$ ,  $t_{rr}$ ,  $Q_{rr}$ ,  $I_{rr}$ ,  $t_{d(on)}$ ,  $t_r$ ,  $t_{d(off)}$ ,  $t_f$

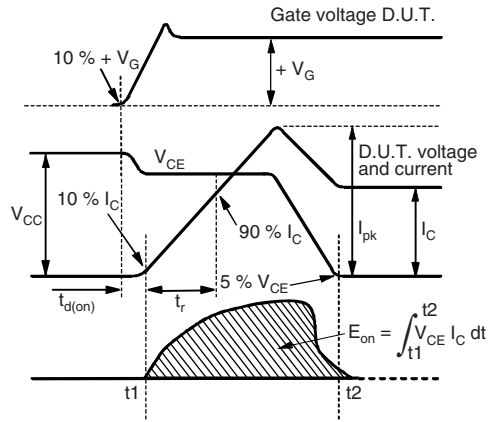


Fig. 17c - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{on}$ ,  $t_{d(on)}$ ,  $t_r$

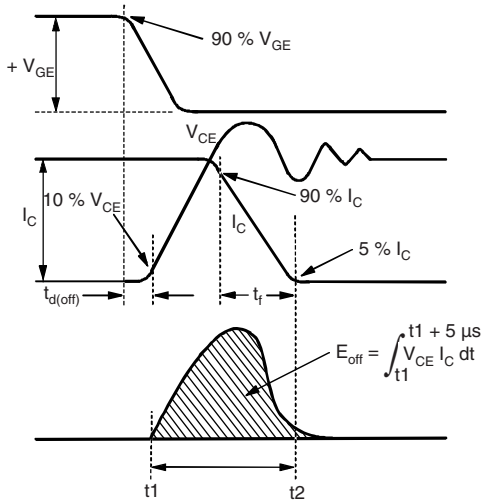


Fig. 17b - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{off}$ ,  $t_{d(off)}$ ,  $t_f$

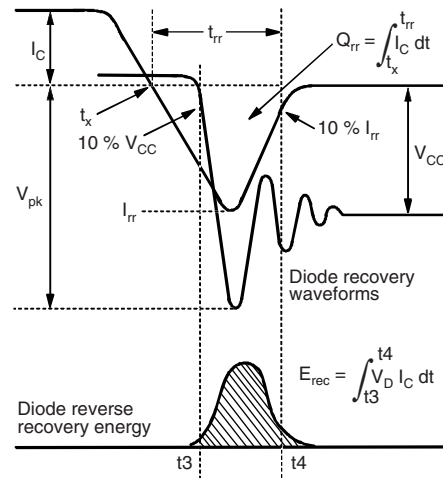


Fig. 17d - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{rec}$ ,  $t_{rr}$ ,  $Q_{rr}$ ,  $I_{rr}$

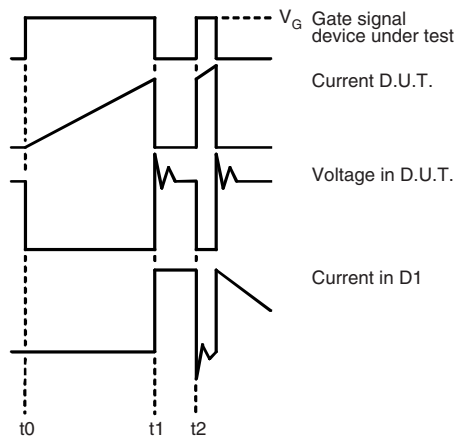
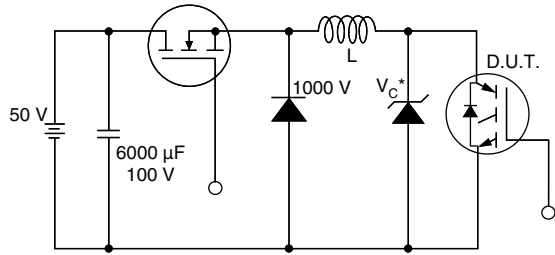


Fig. 17e - Macro Waveforms for Figure 18a's Test Circuit



\* Driver same type as D.U.T.;  $V_C = 80\%$  of  $V_{CE}$  (max)  
**Note:** Due to the 50 V power supply, pulse width and inductor will increase to obtain rated  $I_d$

Fig. 18 - Clamped Inductive Load Test Circuit

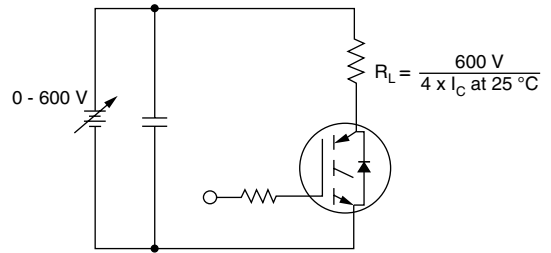


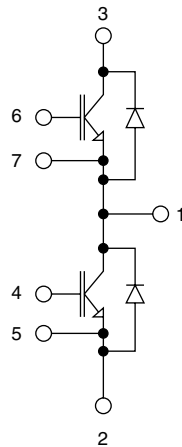
Fig. 19 - Pulsed Collector Current Test Circuit

### ORDERING INFORMATION TABLE

Device code	<b>G</b>	<b>A</b>	<b>75</b>	<b>T</b>	<b>S</b>	<b>120</b>	<b>U</b>	<b>PbF</b>
	①	②	③	④	⑤	⑥	⑦	⑧

- 1** - Insulated gate bipolar transistor (IGBT)
- 2** - Generation 4, IGBT silicon, DBC construction
- 3** - Current rating (75 = 75 A)
- 4** - Circuit configuration (T = Half-bridge)
- 5** - Package indicator (INT-A-PAK)
- 6** - Voltage rating (120 = 1200 V)
- 7** - Speed/type (U = Ultrafast)
- 8** - PbF = Lead (Pb)-free

### CIRCUIT CONFIGURATION

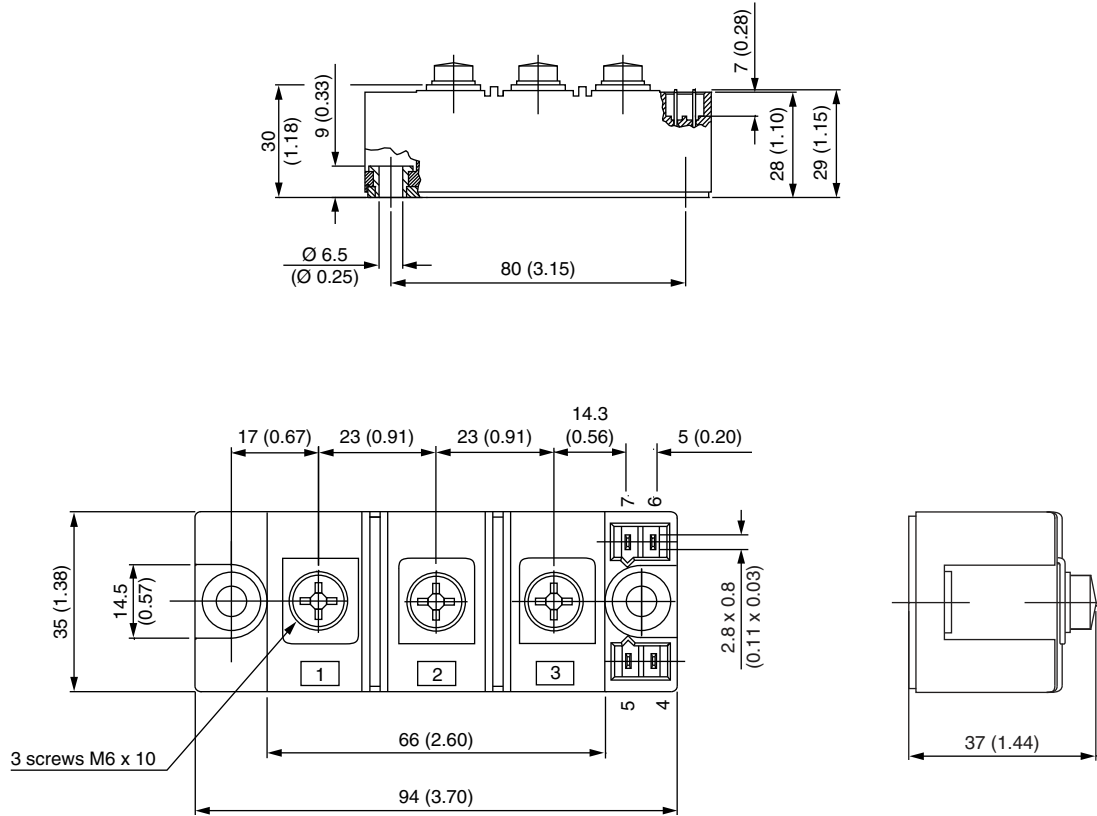


#### LINKS TO RELATED DOCUMENTS

Dimensions	<a href="http://www.vishay.com/doc?95173">www.vishay.com/doc?95173</a>
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## INT-A-PAK IGBT

**DIMENSIONS** in millimeters (inches)







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**Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.**

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Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

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