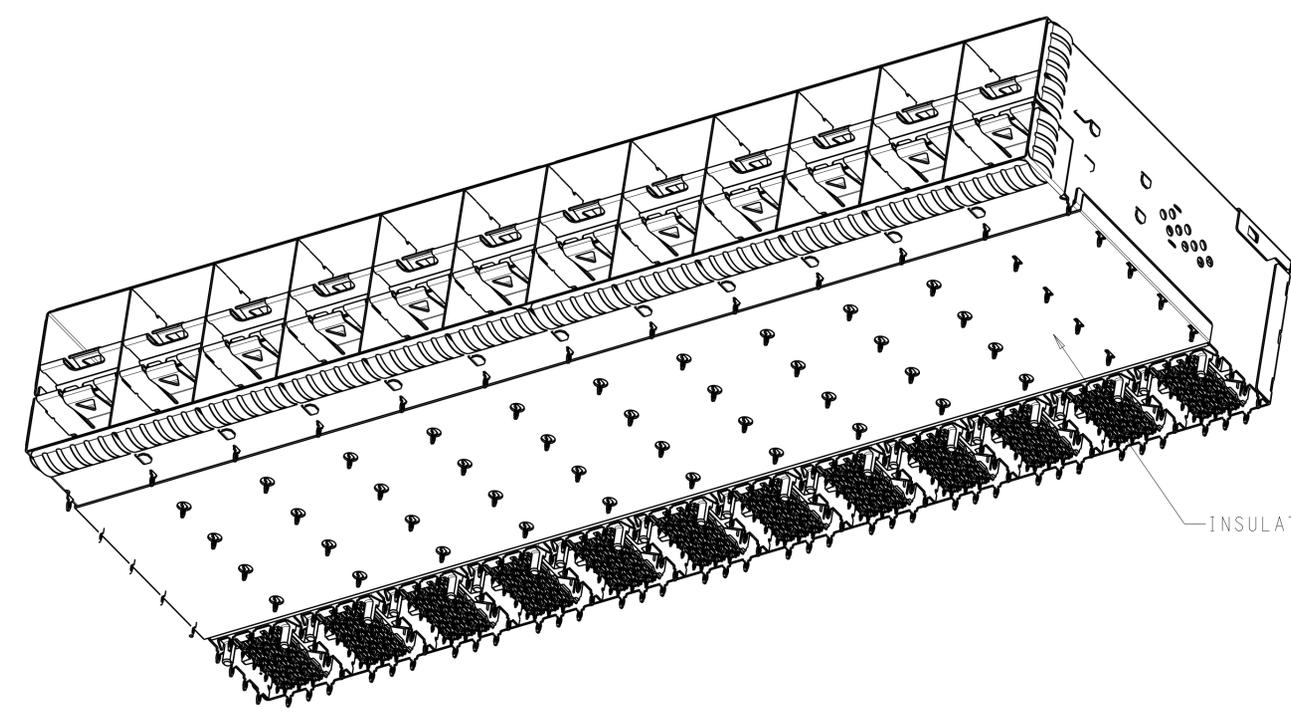
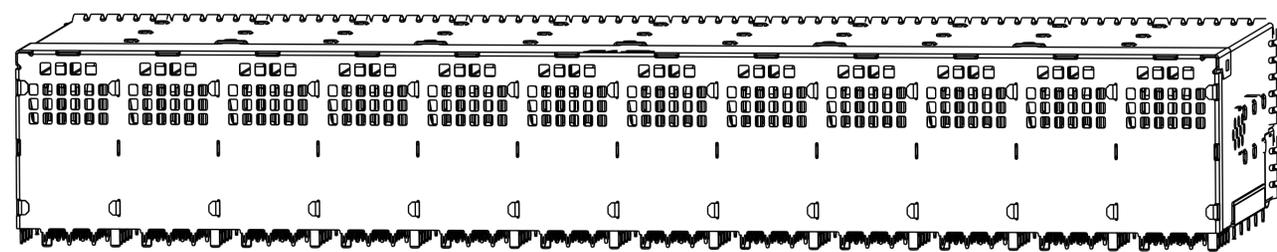
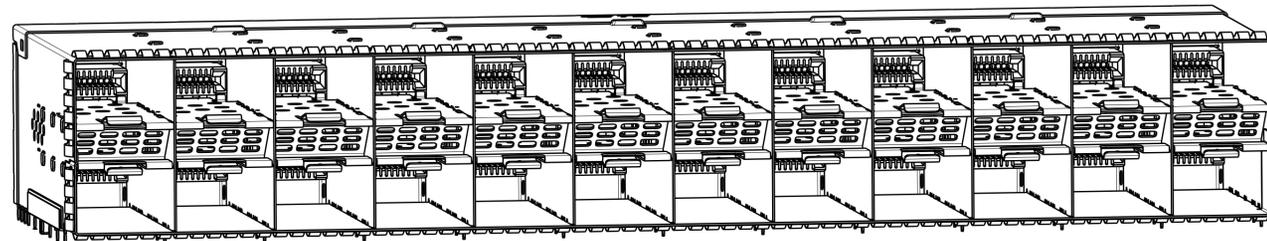


REVISIONS				
P.	LTN	DESCRIPTION	DATE	DWN. APVD.
A		MASS PRODUCTION	20FEB2017	PP SH

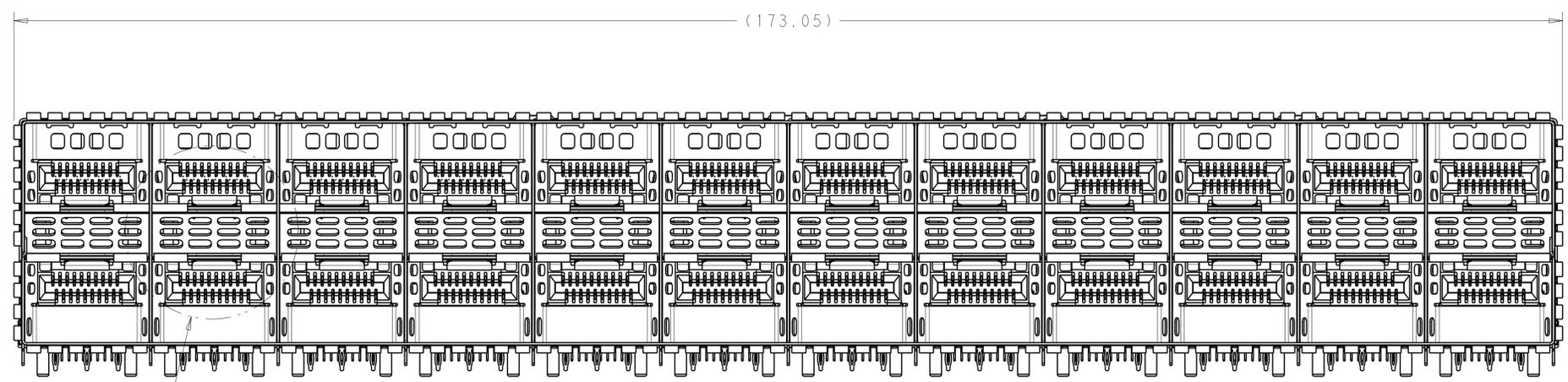


- MATERIALS:**  
 CAGE ASSEMBLY - NICKEL-SILVER ALLOY PER ASTM B 122  
 RECEPTACLE HOUSING - LCP, BLACK, UL 94V-0 RATED  
 RECEPTACLE HOUSING SHIELD - COPPER ALLOY  
 CHICKLET - LCP, BLACK, UL 94V-0 RATED  
 CHICKLET ALIGNMENT CLIP - PBT  
 CONTACT - COPPER ALLOY  
 EMI SPRINGS - PHOSPHOR BRONZE PER ASTM B103  
 INSULATOR TAPE:KAPTON
- CONTACT FINISH:**  
 0.76µm MIN GOLD PER ASTM B 844 AT MATING INTERFACE,  
 1.25µm MIN MATTE TIN PER ASTM B 545 AT PCB INTERFACE,  
 ALL OVER 2.54µm MIN NICKEL PER QQ-N-290
- PCB MINIMUM THICKNESS = 1.5mm
- △ FOR HOLE SIZE AND PLATINGS,  
 SEE APPLICATION SPECIFICATION 114-13319
- △ LIGHT PIPE PAD LAYOUT IS FOR 0805 LOW PROFILE  
 LED PACKAGE WITH A HEIGHT OF 0.8mm
- △ THE ENTIRE AREA OF THE CONNECTOR FOOTPRINT, INDICATED BY THE  
 DASHED LINE, TO BE CONSIDERED THE KEEPOUT AREA FOR COMPONENTS  
 AND SIGNAL TRACES, TOP SIDE ONLY, TOP SIDE TRACES ALLOWED  
 WITHIN CONNECTOR HOLE PATTERN
- △ DIMENSION APPLIES FROM EMI SPRINGS ONLY.

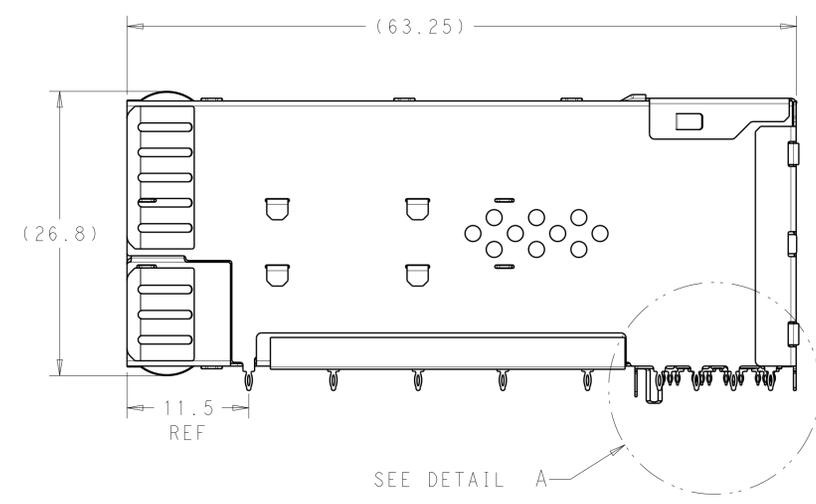
EMI SPRINGS	W/O INSULATOR	2301210-2
EMI SPRINGS	W/ INSULATOR	2301210-1
CAGE TYPE	INSULATOR OPTION	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009		DWN: PETER PAN 02MAR2016	TE Connectivity
DIMENSIONS: mm		CHK: SEAN HAN 02MAR2016	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD: SEAN HAN 02MAR2016	NAME: RECEPTACLE ASSEMBLY, 2X12, STACKED, zSFP+ THERMALLY ENHANCED SIZE: CAGE CODE DRAWING NO: A100779C=2301210 RESTRICTED TO:
0 PLC ±0.10 1 PLC ±0.10 2 PLC ±0.10 3 PLC ±0.10 4 PLC ±0.10 ANGLES ±°		PRODUCT SPEC: 108-2481 APPLICATION SPEC: 114-13319 WEIGHT: - CUSTOMER DRAWING	
MATERIAL: SEE NOTES		SCALE: 2:1 SHEET 1 OF 4 REV A	

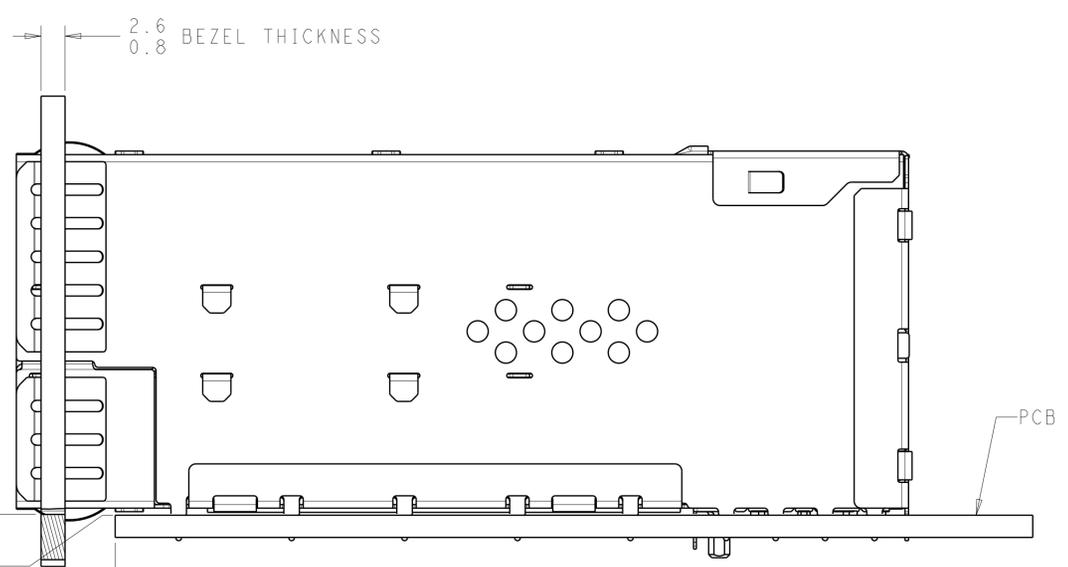
REVISIONS				
P.	LTN	DESCRIPTION	DATE	OWN APVD
-	-	SEE SHEET 1	-	-



SEE DETAIL B

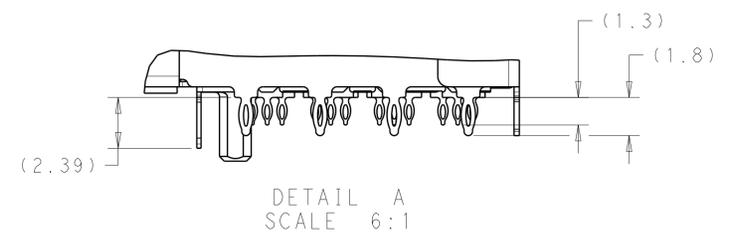


SEE DETAIL A

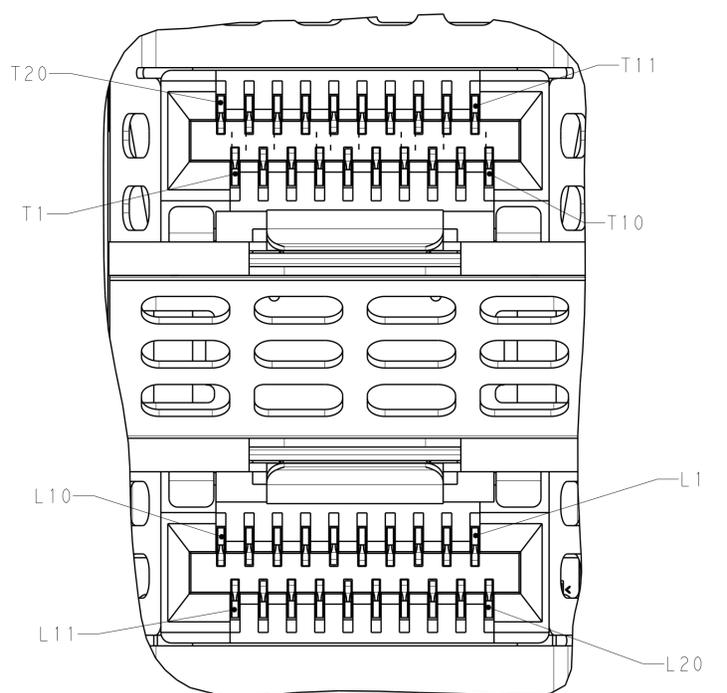


0±0.1  
BEZEL OPENING  
TO TOP OF PCB

RECOMMENDED BEZEL LOCATION WITH EMI SPRINGS  
 SCALE 4:1



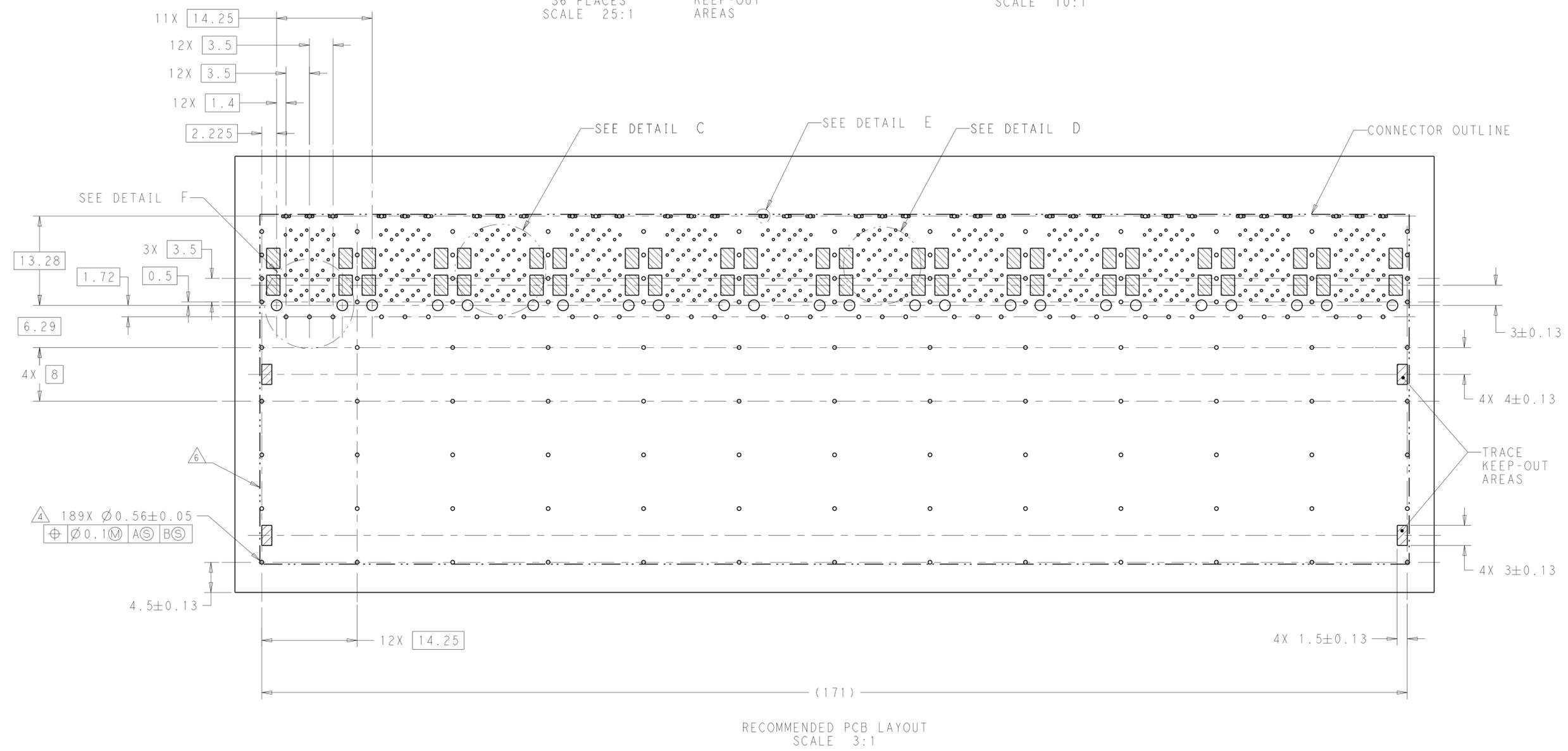
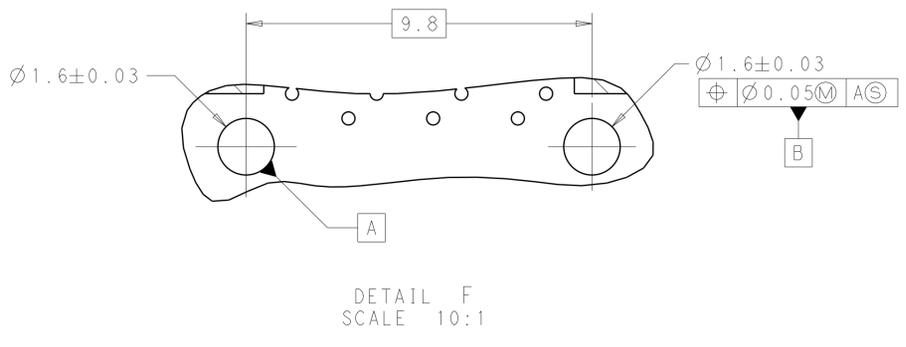
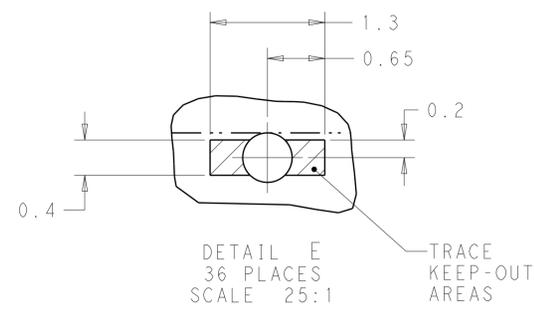
DETAIL A  
 SCALE 6:1



DETAIL B  
 SCALE 10:1

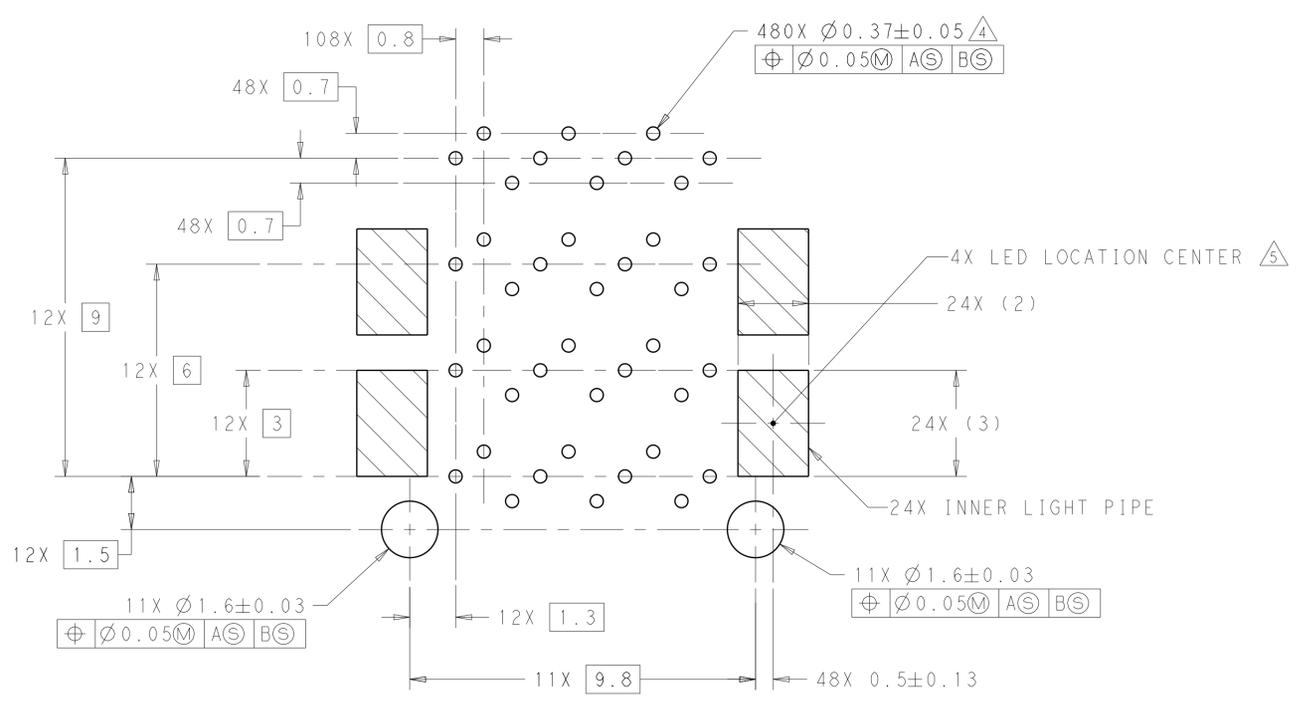
THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN: PETER PAN 02MAR2016	TE Connectivity
DIMENSIONS: mm		CHK: SEAN HAN 02MAR2016	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD: SEAN HAN 02MAR2016	NAME: RECEPTACLE ASSEMBLY, 2X12 STACKED, zSFP+ THERMALLY ENHANCED SIZE: CAGE CODE DRAWING NO: A100779C-2301210 RESTRICTED TO:
0 PLC ±0.10 1 PLC ±0.10 2 PLC ±0.10 3 PLC ±0.10 4 PLC ±0.10 ANGLES ±°		PRODUCT SPEC: 108-2481 APPLICATION SPEC: 114-13319 WEIGHT:	
MATERIAL: SEE NOTES		FINISH: SEE NOTES	CUSTOMER DRAWING SCALE: 2:1 SHEET 2 OF 4 REV A

REVISIONS				
P.	LTN	DESCRIPTION	DATE	APP'D
-	-	SEE SHEET 1	-	-

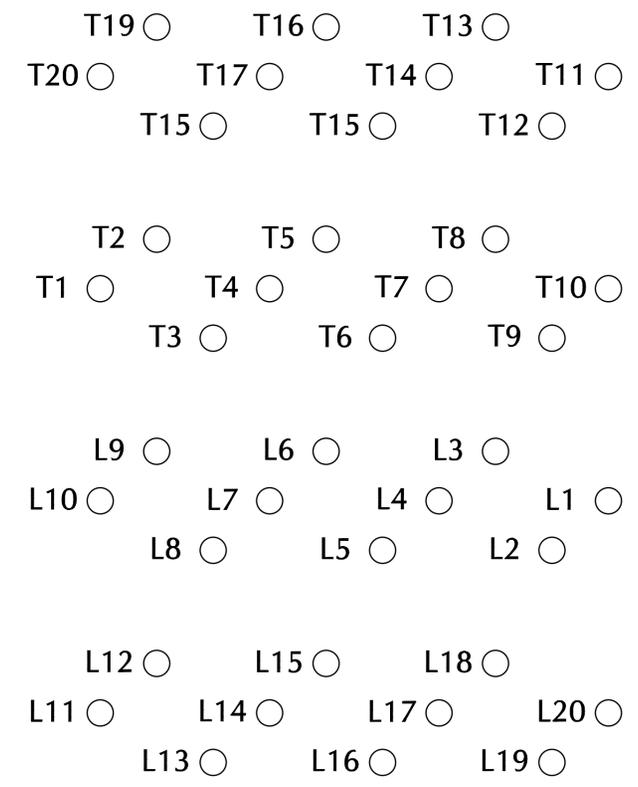


THIS DRAWING IS A CONTROLLED DOCUMENT. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.		OWN: PETER PAN 02MAR2016	TE Connectivity
DIMENSIONS: mm		CHK: SEAN HAN 02MAR2016	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APP'D: SEAN HAN 02MAR2016	
0 PLC ±0.10 1 PLC ±0.10 2 PLC ±0.10 3 PLC ±0.10 4 PLC ±0.10 ANGLES ±°		NAME: RECEPTACLE ASSEMBLY, 2X12, STACKED, zSFP+ THERMALLY ENHANCED PRODUCT SPEC: 108-2481 APPLICATION SPEC: 114-13319 WEIGHT: - CUSTOMER DRAWING	
MATERIAL: - FINISH: - SEE NOTES		SIZE: A100779C=2301210	RESTRICTED TO: -
SCALE: 2:1		SHEET: 3 OF 4	REV: A

REVISIONS				
P.	LTN	DESCRIPTION	DATE	APP'D
-	-	SEE SHEET 1	-	-

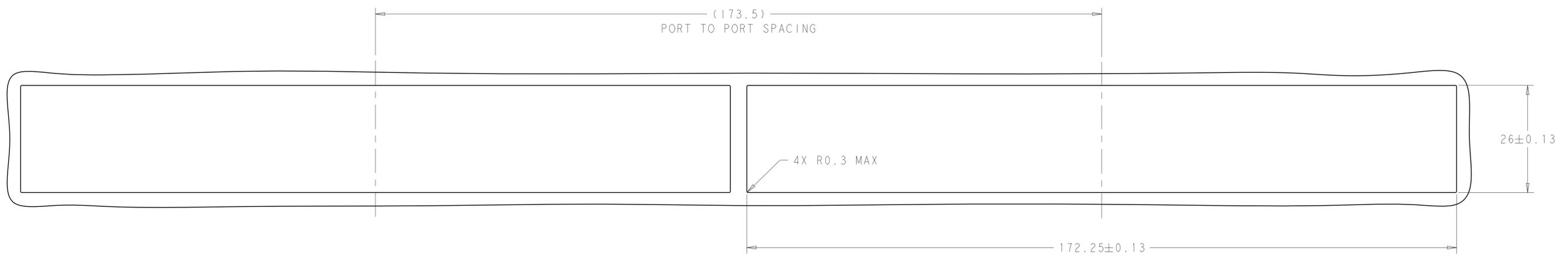


DETAIL C  
 RECOMMENDED PIN AND LIGHT PIPE LAYOUT  
 SCALE 10:1



PIN MAP LEGEND	
PIN NUMBER	FUNCTION
L1/T1	VEET
L2/T2	TX-FAULT
L3/T3	TX-DISABLE
L4/T4	SDA
L5/T5	SCL
L6/T6	MOD_ABS
L7/T7	RSO
L8/T8	RX_LOS
L9/T9	RS1
L10/T10	VEER
L11/T11	VEER
L12/T12	RD-
L13/T13	RD+
L14/T14	VEER
L15/T15	VCCR
L16/T16	VCCT
L17/T17	VEET
L18/T18	TD+
L19/T19	TD-
L20/T20	VEET

DETAIL D  
 PIN MAP  
 12 PLACES  
 SCALE 20:1



RECOMMENDED BEZEL CUT OUT DETAIL

THIS DRAWING IS A CONTROLLED DOCUMENT. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009		DWN: PETER PAN 02MAR2016 CHK: SEAN HAN 02MAR2016 APP'D: SEAN HAN 02MAR2016	TE Connectivity
DIMENSIONS: mm 	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.10 1 PLC ±0.10 2 PLC ±0.10 3 PLC ±0.10 4 PLC ±0.10 ANGLES ±°	NAME: RECEPTACLE ASSEMBLY, 2X12, STACKED, zSFP+ THERMALLY ENHANCED PRODUCT SPEC: 108-2481 APPLICATION SPEC: 114-13319 WEIGHT: - CUSTOMER DRAWING	

## Данный компонент на территории Российской Федерации

### Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

<http://moschip.ru/get-element>

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

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