NXP Semiconductors

MPXV5050VC6T1 Rev 3, 12/2018

High Temperature Accuracy Integrated Silicon Pressure Sensor for Measuring Vacuum/Gauge Pressure, On-Chip Signal Conditioned, Temperature Compensated and Calibrated

The MPXV5050V series sensor integrates on-chip, bipolar op amp circuitry and thin film resistor networks to provide a high output signal and temperature compensation. The small form factor and high reliability of on-chip integration make the NXP Semiconductor pressure sensor a logical and economical choice for the system designer.

The MPXV5050V series piezoresistive transducer is a state-of-the-art, monolithic, signal conditioned, silicon pressure sensor. This sensor combines advanced micromachining techniques, thin film metallization, and bipolar semiconductor processing to provide an accurate, high level analog output signal that is proportional to applied pressure.

Features

- 2.5% Maximum Error over 0° to 85°C
- · Ideally suited for Microprocessor or Microcontroller-Based Systems
- Temperature Compensated from Over -40° to +125°C
- · Patented Silicon Shear Stress Strain Gauge
- Durable Thermoplastic (PPS) Surface Mount Package
- · Easy-to-Use Chip Carrier Option
- Ideal for Automotive and Non-Automotive Applications

MPXV5050V Series

-50 to 0 kPa (-7.25 to 0 psi) 0.1 to 4.6 V Output

Application Examples

Vacuum Pump Monitoring

ORDERING INFORMATION								
Device Name	Case # of P		# of Ports	Ports Pre		essure Type		Device Marking
Device Name	No.	None Single		Dual Gauge		Differential Absolute		
Small Outline Package								
MPXV5050VC6T1	482A		•		Vacuum/Gauge			MPXV5050V

SMALL OUTLINE PACKAGE





Operating Characteristics

Table 1. Operating Characteristics ($V_S = 5.0 \text{ Vdc}$, $T_A = 25^{\circ}\text{C}$ unless otherwise noted, P1 > P2)

	Characteristic	Symbol	Min	Тур	Max	Unit
Pressure Range		P _{OP}	-50	_	0	kPa
Supply Voltage ⁽¹⁾		Vs	4.75	5.0	5.25	Vdc
Supply Current		Io	_	7.0	10	mAdc
Full Scale Output ⁽²⁾ @ V _S = 5.0 Volts	(0 to 85°C)	V _{FSO}	4.488	4.6	4.713	Vdc
Full Scale Span ⁽³⁾ @ V _S = 5.0 Volts	(0 to 85°C)	V _{FSS}	_	4.5	_	Vdc
Accuracy ⁽⁴⁾	(0 to 85°C)	_	_	_	±2.5	%V _{FSS}
Sensitivity		V/P	_	90	_	mV/kPa
Response Time ⁽⁵⁾		t _R	_	1.0	_	ms
Warm-Up Time ⁽⁶⁾		_	_	20	_	ms
Offset Stability ⁽⁷⁾		_	_	±0.5	_	%V _{FSS}
Pressure Offset ⁽⁸⁾	(0 to 85°C)	V _{off}	0	0.100	0.213	Vdc

- 1. Device is ratiometric within this specified excitation range.
- 2. Full Scale Output (V_{FSO}) is defined as the output voltage at the maximum or full rated pressure.
- 3. Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- 4. Accuracy is the deviation in actual output from nominal output over the entire pressure range and temperature range as a percent of span at 25°C due to all sources of error including the following:

Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.

Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.

Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from minimum or maximum rated pressure at 25°C.

- TcSpan: Output deviation over the temperature range of 0° to 85°C, relative to 25°C.
- TcOffset: Output deviation with minimum pressure applied, over the temperature range of 0° to 85°C, relative to 25°C.
- 5. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 6. Warm-up Time is defined as the time required for the product to meet the specified output voltage after the pressure has been stabilized.
- 7. Offset Stability is the product's output deviation when subjected to 1000 cycles of Pulsed Pressure, Temperature Cycling with Bias Test.
- 8. Offset (Voff) is defined as the output voltage at the minimum rated pressure.

Maximum Ratings

Table 2. Maximum Ratings⁽¹⁾

Rating	Symbol	Value	Units
Maximum Pressure (P1 > P2)	P _{max}	200	kPa
Storage Temperature	T _{stg}	-40 to +125	°C
Operating Temperature	T _A	-40 to +125	°C

^{1.} Exposure beyond the specified limits may cause permanent damage or degradation to the device.

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip.

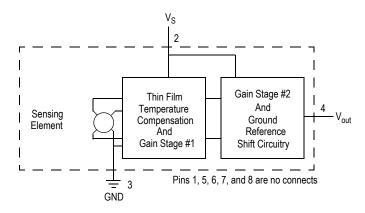


Figure 1. Fully integrated pressure sensor schematic Figure 2. Full device pinout (top view)

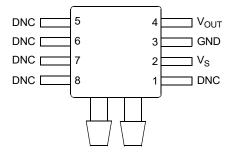


Table 3. Pin functions

Pin	Name	Function
1	DNC	Do not connect to external circuitry or ground.
2	V _S	Voltage supply
3	GND	Ground
4	V _{OUT}	Output voltage
5	DNC	Do not connect to external circuitry or ground.
6	DNC	Do not connect to external circuitry or ground.
7	DNC	Do not connect to external circuitry or ground.
8	DNC	Do not connect to external circuitry or ground.

On-chip Temperature Compensation and Calibration

Figure 3 illustrates the gauge sensing chip in the basic Super Small Outline chip carrier (Case 482A).

Figure 4 shows a typical application circuit (output source current operation).

Figure 5 shows the sensor output signal relative to pressure input. Typical minimum and maximum output curves are shown for operation over 0 °C to 85 °C temperature range. The output will saturate outside of the rated pressure range.

A fluorosilicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the silicon diaphragm. The MPXV5050 series pressure sensor operating characteristics, internal reliability and qualification tests are based on use of dry air as the pressure media. Media other than dry air may have adverse effects on sensor performance and long-term reliability. Contact the factory for information regarding media compatibility in your application.

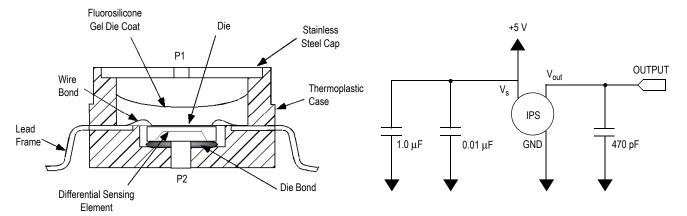


Figure 3. Cross Sectional Diagram SSOP (not to scale)

Figure 4. Typical Application Circuit (Output Source Current Operation)

Transfer Function MPXV5050VC Series

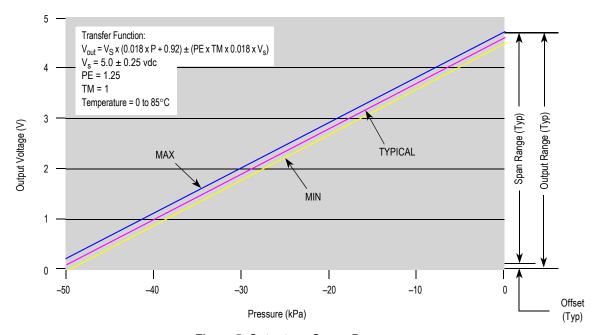


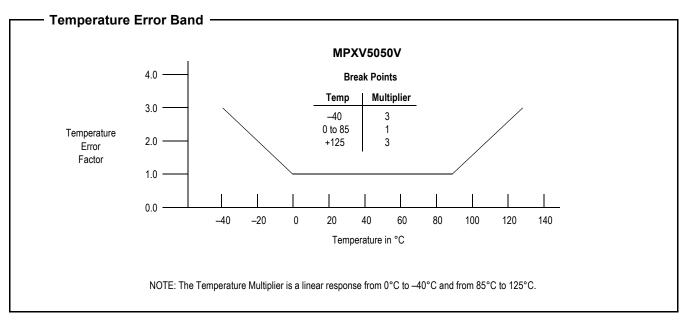
Figure 5. Output vs. Gauge Pressure

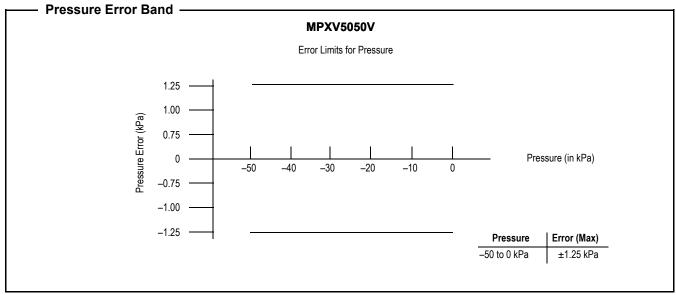
Transfer Function (MPXV5050V)

Nominal Transfer Value: $V_{OUT} = V_S x (0.018 x P + 0.92)$

± (Pressure Error x Temp Multi x 0.018 x V_S)

 V_S = 5.0 \pm 0.25 V





SURFACE MOUNTING INFORMATION

MINIMUM RECOMMENDED FOOTPRINT FOR SMALL OUTLINE PACKAGE

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor package must be the correct size to ensure proper solder connection interface between the board and the package. With the correct pad geometry, the packages will self-align when subjected to a

solder reflow process. It is always recommended to fabricate boards with a solder mask layer to avoid bridging and/or shorting between solder pads, especially on tight tolerances and/or tight layouts.

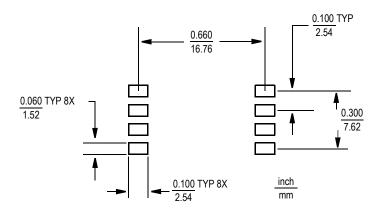
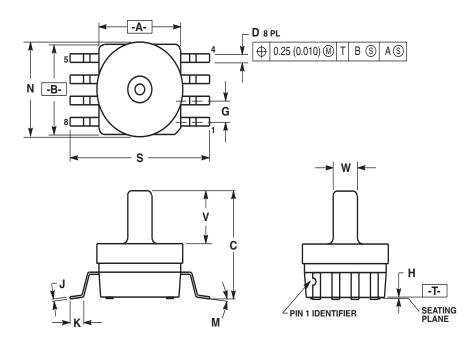


Figure 6. SOP Footprint (Case 482A)

PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006).
 5. ALL VERTICAL SURFACES 5' TYPICAL DRAFT.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.415	0.425	10.54	10.79	
В	0.415	0.425	10.54	10.79	
С	0.500	0.520	12.70	13.21	
D	0.038	0.042	0.96	1.07	
G	0.100	BSC	2.54 BSC		
Н	0.002	0.010	0.05	0.25	
J	0.009	0.011	0.23	0.28	
K	0.061	0.071	1.55	1.80	
M	0°	7°	0°	7°	
N	0.444	0.448	11.28	11.38	
S	0.709	0.725	18.01	18.41	
٧	0.245	0.255	6.22	6.48	
W	0.115	0.125	2.92	3.17	

CASE 482A-01 ISSUE A SMALL OUTLINE PACKAGE

Table 4. Revision history

Revision number	Revision date	Description of changes
3	12/2018	 Revised "Freescale" references to "NXP" throughout document. Revised "absolute" references to "vacuum/gauge" or "gauge" throughout document. Inserted new figure titled "Full device pinout (topview)" as Figure 2. Inserted new table titled "Pin functions" as Table 3. Added the revision history as Table 4.

How to Reach Us:

Home Page: www.nxp.com

Web Support:

http://www.nxp.com/support

Information in this document is provided solely to enable system and software implementers to use NXP products. There are no expressed or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation, consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by the customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address:

http://www.nxp.com/terms-of-use.html.

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. All rights reserved.

© NXP B.V., 2009 - 2018. All Rights reserved.

Document Number: MPXV5050VC6T1

Rev. 3 12/2018



ПОСТАВКА ЭЛЕКТРОННЫХ КОМПОНЕНТОВ

Общество с ограниченной ответственностью «МосЧип» ИНН 7719860671 / КПП 771901001 Адрес: 105318, г.Москва, ул.Щербаковская д.3, офис 1107

Данный компонент на территории Российской Федерации Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

http://moschip.ru/get-element

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

Офис по работе с юридическими лицами:

105318, г. Москва, ул. Щербаковская д. 3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru moschip.ru_6 moschip.ru 4 moschip.ru 9