TSOP373.., TSOP375..

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



- Very low supply current
- Photo detectors and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- Improved immunity against ambient light
- Insensitive to supply voltage ripple and noise
- Material categorization:

For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

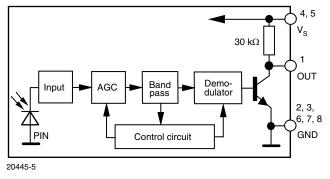
The TSOP373.., TSOP375.. series are miniaturized receiver modules for infrared remote control systems. A PIN diode and a preamplifier are assembled on a PCB, the epoxy lens cap is designed as an IR filter.

The demodulated output signal can be directly connected to a microprocessor for decoding. The TSOP373.. is optimized to better suppress spurious pulses from energy saving lamps. The TSOP375.. has an excellent noise suppression. It is immune to dimmed LCD backlighting and any fluorescent lamps. AGC3 and AGC5 may also suppress some data signals in case of continuous transmission.

This component has not been qualified according to automotive specifications.

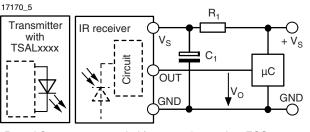
PARTS T	ABLE			
AGC		NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)	VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)	
	36 kHz	TSOP37336 ⁽¹⁾⁽²⁾	TSOP37536 ⁽¹⁾⁽²⁾	
Carrier	38 kHz	TSOP37338 ⁽³⁾⁽⁴⁾⁽⁵⁾⁽⁶⁾	TSOP37538 ⁽³⁾⁽⁴⁾⁽⁵⁾	
frequency	40 kHz	TSOP37340	TSOP37540	
	56 kHz	TSOP37356	TSOP37556	
Package		Belo	bbog	
Pinning		1 = OUT, 2, 3, 6, 7, 8 = GND, 4, 5 = V _S		
Dimensions (mm)		3.95 W x 3.95 H x 0.8 D		
Mounting		SMD		
Application		Remote control		
Best remote control code		⁽¹⁾ MCIR ⁽²⁾ RCMM ⁽³⁾ Mitsubishi ⁽⁴⁾ RECS-80 Code ⁽⁵⁾ r-map ⁽⁶⁾ XMP-1, XMP-2		

BLOCK DIAGRAM



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APPLICATION CIRCUIT



Document Number: 82446

 R_1 and C_1 are recommended for protection against EOS. Components should be in the range of 33 Ω < R_1 < 1 k Ω , C_1 > 0.1 $\mu F.$

RoHS COMPLIANT HALOGEN

e4



22531-1

Taping:

ORDERING CODE

TSOP37...TT - top view taped

TSOP37...TR - side view taped



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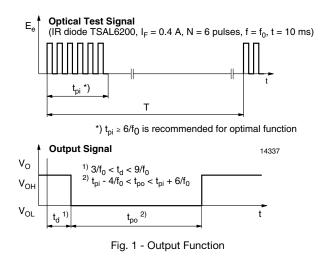
ABSOLUTE MAXIMUM F	IMUM RATINGS			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		V _S	-0.3 to +6	V
Supply current		I _S	3	mA
Output voltage		Vo	-0.3 to (V _S + 0.3)	V
Output current		Ι _Ο	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW

Note

• Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPT	CAL CHARACTERISTICS	(T _{amb} = 25 °	°C, unless o	otherwise s	pecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.5		5.5	V
Supply ourrent	$V_{\rm S} = 3.3 \text{ V}, \text{ E}_{\rm v} = 0$	I _{SD}	0.27	0.35	0.45	mA
Supply current	$E_v = 40$ klx, sunlight	I _{SH}		0.45		mA
Transmission distance	$E_v = 0$, IR diode TSAL6200, $I_F = 200$ mA, test signal see fig. 1	d		45		m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see fig. 1	V _{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: $t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_{o,}$ test signal see fig. 1	E _{e min.}		0.12	0.25	mW/m²
Maximum irradiance	$\begin{array}{c} t_{pi} \text{ - } 5/f_o < t_{po} < t_{pi} + 6/f_o, \\ \text{test signal see fig. 1} \end{array}$	E _{e max.}	30			W/m ²
Directivity	Angle of half transmission distance	Φ1/2		± 75		deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



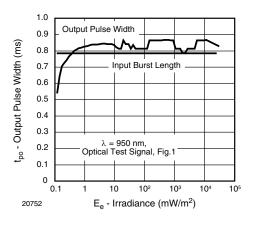


Fig. 2 - Output Pulse Width vs. Irradiance

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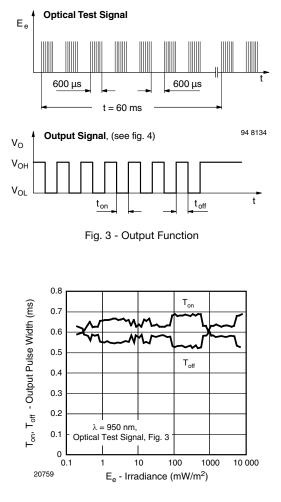


Fig. 4 - Output Pulse Diagram

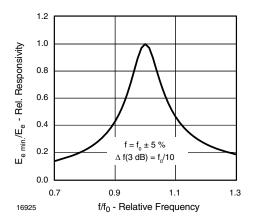


Fig. 5 - Frequency Dependance of Responsivity

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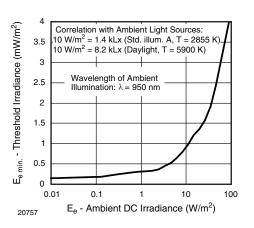


Fig. 6 - Sensitivity in Bright Ambient

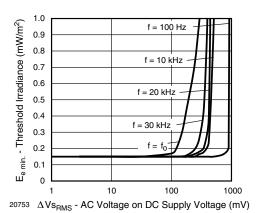


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

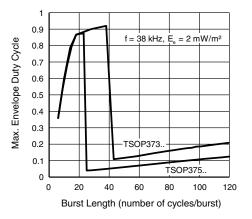


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

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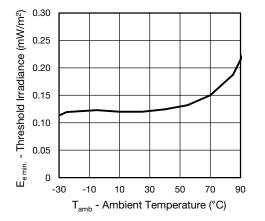


Fig. 9 - Sensitivity vs. Ambient Temperature

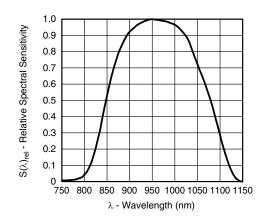
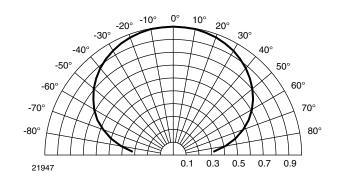


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength



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Fig. 11 - Directivity

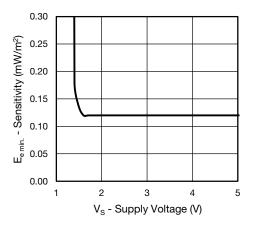


Fig. 12 - Sensitivity vs. Supply Voltage



SUITABLE DATA FORMAT

The TSOP373.., TSOP375.. series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP373.., TSOP375.. in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see figure 12 or figure 13)

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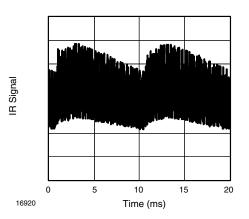


Fig. 13 - IR Signal from Fluorescent Lamp with Low Modulation

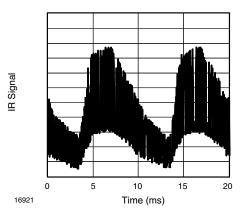


Fig. 14 - IR Signal from Fluorescent Lamp with High Modulation

	TSOP373	TSOP375
Minimum burst length	6 cycles/burst	6 cycles/burst
After each burst of length a minimum gap time is required of	6 to 35 cycles ≥ 10 cycles	6 to 24 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	35 cycles > 6 x burst length	24 cycles > 25 ms
Maximum number of continuous short bursts/second	2000	2000
MCIR code	preferred	yes
RCMM code	preferred	yes
XMP-1, XMP-2 code	preferred	yes
Suppression of interference from fluorescent lamps	Even critical disturbance patterns are suppressed (example: signal pattern of fig. 12 and fig. 13)	Even critical disturbance patterns are suppressed (example: signal pattern of fig. 12 and fig. 13)

Notes

• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP372.., TSOP374...

Best choice of AGC for some popular IR-codes:

- TSOP37336: MCIR, RCMM

- TSOP37538: Mitsubishi, RECS-80 Code

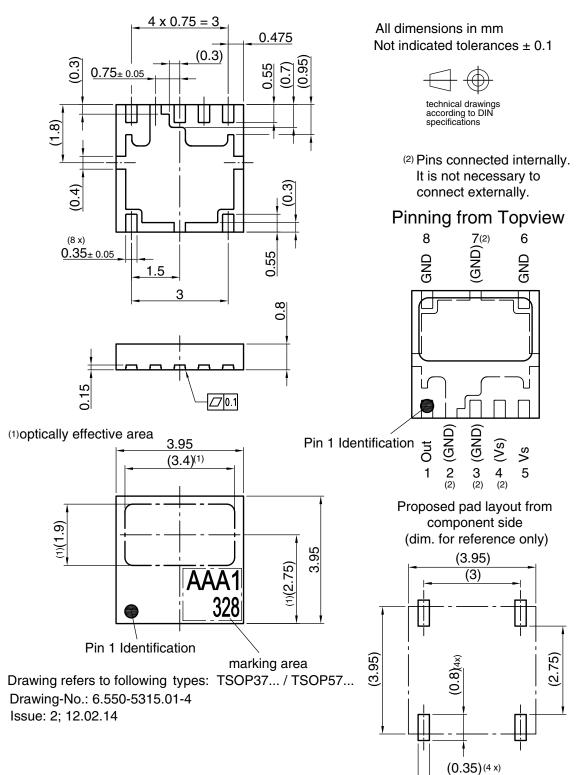
- TSOP37338: XMP-1, XMP-2, r-map

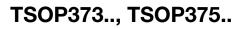
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PACKAGE DIMENSIONS in millimeters

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• Use a soldering iron of 25 W or less. Adjust the

· Handle products only after the temperature has cooled off

temperature of the soldering iron below 300 °C

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ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 168 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE

300 <u>m</u>ax. 260 °C 255 240 250 °C C° 245 °C 217 °C 200 max. 20 s T (°C) 150 max. 120 s max. 100 s 100 max. Ramp Up 3 °C/s max. Ramp Down 6 °C/s 50 0 100 0 50 150 200 250 300 t (s) 19800 max. 2 cycles allowed

Manual Soldering

• Finish soldering within 3 s

ORDERING INFORMATION			
ORDERING CODE	PACKAGING	VOLUME ⁽¹⁾	REMARKS
TSOP37TT1	Tape and reel	MOQ: 1800 pcs	3.95 mm x 3.95 mm x 0.75 mm
TSOP37TT2	rape and reel	MOQ: 7000 pcs	3.95 mm x 3.95 mm x 0.75 mm

Note

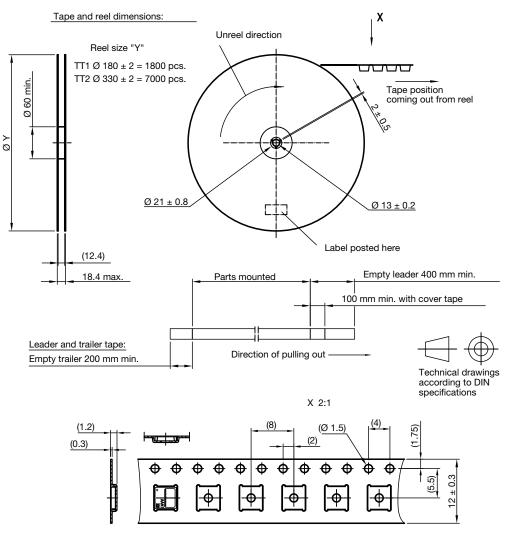
(1) MOQ: minimum order quantity

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TAPING VERSION TSOP37... DIMENSIONS in millimeters



Drawing-No.: 9.700-5347.01-4 Issue: 1; 14.11.11 Not indicated tolerances ± 0.1

TSOP373.., TSOP375..





LABEL

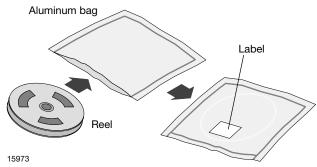
Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (Finished goo			
PLAIN WRITING	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	PCK	-	
Mixed code indicator	MIXED CODE	-	
Origin	XXXXXXX+	Company logo	
Long bar code top	Туре	Length	
Item-number	Ν	8	
Plant-code	Ν	2	
Sequence-number	Х	3	
Quantity	Ν	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	Х	3	
Data-code	Ν	3	
Batch-number	Х	10	
Filter	-	1	
Total length	-	17	

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

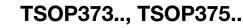
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC[®] standard J-STD-020 level 3 label is included on all dry bags.

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relative humidity (RH)

temperature process must be

b) Stored per J-STD-033

b) 3a or 3b are not met

Bag Seal Date:

4. Devices require bake, before mounting, if:

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VISHAY SEMICONDUCTORS STANDARD **BAR CODE LABELS**

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

BAR CODE PRODUCT LABEL (Example)



22178

22650

EIA JEDC standard J-STD-020 level 3 label is included on all dry bags

If blank, see adjacent bar code label Note: Level and body temperature defined by IPC/JEDEC J-STD-020

Caution

This bag contains MOISTURE-SENSITIVE DEVICES

3. After bag is opened, devices that will be subjected to reflow solder or other high

a) Humidity Indicator Card reads >10% for level 2a - 5a devices or $>\!\!60\%$ for level 2 devices when read at $23{\pm}5^{\circ}C$

a) Mounted within: $\frac{168}{^{11} {\rm f blank, see \ adjacent \ bar \ code \ label}} {\rm hours \ of \ factory \ conditions} \\ \leq 30^\circ C/60\% \ RH, \ or$ 168

5. If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure

260

1. Calculated shelf life in sealed bag: 12 months at ${<}40^{\circ}\mathrm{C}$ and ${<}90\%$

2. Peak package body temperature: <u>260</u> °C If blank, see adjacent bar code label

LEVEL

3

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.



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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.





Общество с ограниченной ответственностью «МосЧип» ИНН 7719860671 / КПП 771901001 Адрес: 105318, г.Москва, ул.Щербаковская д.З, офис 1107

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Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

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Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

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