

## 0.45 Ω, Low Voltage Dual SPDT Analog Switch with Negative Swing Audio Capability

### DESCRIPTION

The DG2750 is a dual SPDT low on-resistance switch designed to from a single 1.6 V to 5.5 V power supply. It is a bi-directional switch, and is capable of switching negative swing audio without the need for a coupling capacitor. With a single power supply, the audio signal can swing over the range from ((V+) - 5) to V+.

Guaranteed to operate with 1.4 V logic when V+ is in the range of 2.7 V to 5.5 V, the DG2750 will allow an easy interface with low voltage DSP or ASIC control logic.

The DG2750 is built on sub micron CMOS low voltage process technology, has very low quiescent current, and provides greater than 600 mA latch-up protection, as tested per JESD78.

The DG2750 is assembled in compact mQFN10, 1.4 mm x 1.8 mm x 0.55 mm and ultra thin UTMQFN of 0.35 mm thickness.

As a committed partner to the community and the environment, Vishay Siliconix manufactures this product with lead (Pb)-free device termination. The miniQFN-10 package has a nickel-palladium-gold device termination and is represented by the lead (Pb)-free “-E4” suffix to the ordering part number. The nickel-palladium-gold device terminations meet all JEDEC® standards for reflow and MSL rating.

As a further sign of Vishay Siliconix's commitment, the DG2750 is fully RoHS-compliant and halogen-free.

### FEATURES

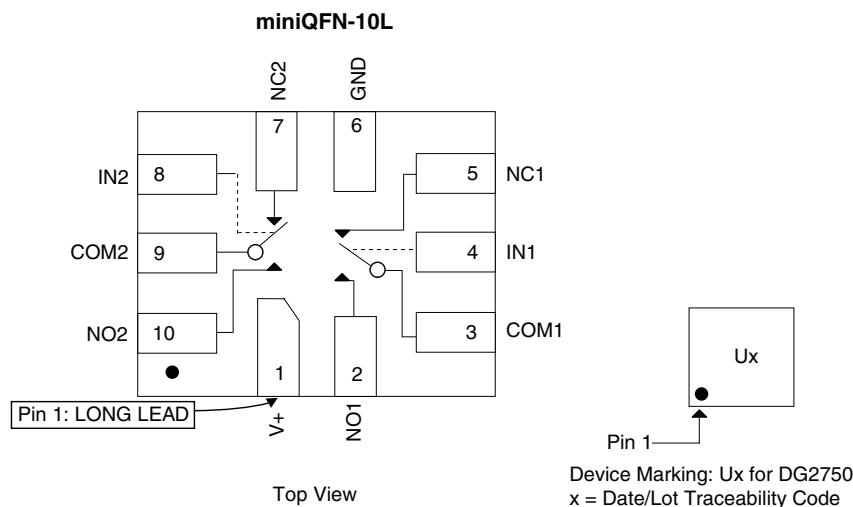
- 1.6 V to 5.5 V single power rail operation
- Capable to switch negative swing audio without DC blocking capacitor
- Low signal distortion: THD+N < -98 dB
- Low on-resistance
- 1.4 V high logic
- Latch-up current > 600 mA (JESD78)
- ESD (HBM): 8 kV
- Reduced power consumption
- Reduce board space
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



### APPLICATIONS

- Cellular phones
- Portable media players
- Computer and game machine
- Handheld healthcare and instruments

### FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



<b>ORDERING INFORMATION</b>			
PART NUMBER	TEMPERATURE RANGE	PACKAGE	SIZE
DG2750DN-T1-E4	-40 °C to +85 °C	miniQFN-10	1.4 mm x 1.8 mm x 0.55 mm
DG2750DN1-T1-GE4		UTMQFN-10	1.4 mm x 1.8 mm x 0.35 mm

<b>TRUTH TABLE, DG2750</b>		
IN1 (PIN 4)	IN2 (PIN 8)	FUNCTION
0	X	COM1 = NC1
1	X	COM1 = NO1
X	0	COM2 = NC2
X	1	COM2 = NO2

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)		
PARAMETER	LIMIT	UNIT
Reference to GND	V+, IN COM, NO, NC <sup>a</sup>	-0.3 to +6 (V+) -5.5 or -2.5 whichever higher, (V+ + 0.3)
Current (Any Terminal except COM, NO, NC, IN)	30	mA
Continuous Current (COM, NO, NC, IN)	$\pm 250$	
Peak Current (Pulsed at 1 ms, 10 % Duty Cycle)	$\pm 500$	
Storage Temperature (D Suffix)	-65 to +150	°C
Power Dissipation (Packages) <sup>b</sup>	208	mW
ESD (Human Body Model) I/O to GND	8	kV
Latch-up (per JESD78)	600	mA

**Notes**

- a. Signals on COM, NO, NC, exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings
- b. All leads welded or soldered to PC board
- c. Derate 2.6 mW/°C above 70 °C

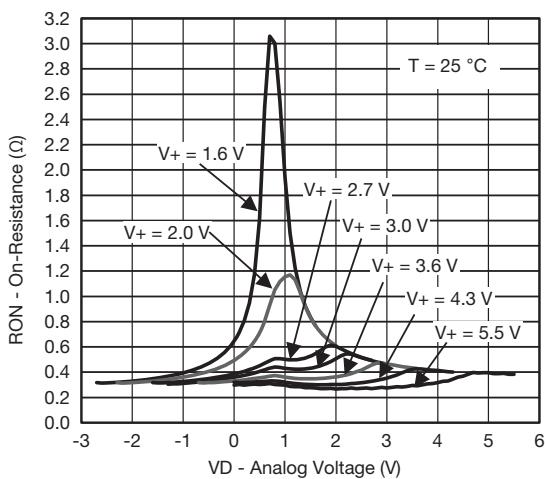
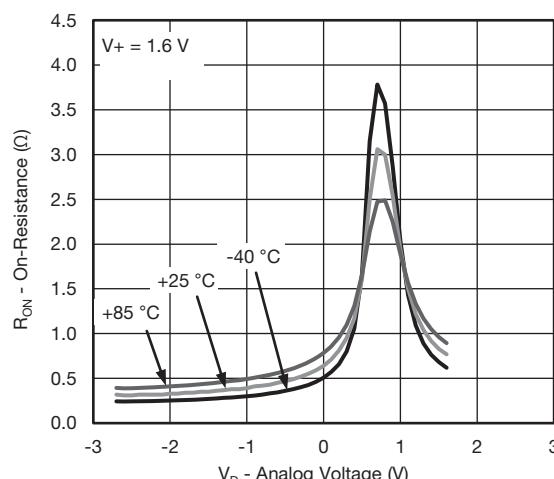
<b>SPECIFICATIONS</b> ( $V_+ = 2.7 \text{ V}, \pm 10 \%$ )							
PARAMETER	SYMBOL	TEST CONDITIONS OTHERWISE UNLESS SPECIFIED	TEMP. <sup>a</sup>	LIMITS			UNIT
				MIN. <sup>b</sup>	TYP. <sup>c</sup>	MAX. <sup>b</sup>	
<b>Analog Switch</b>							
Analog Signal Range <sup>d</sup>	$V_{\text{ANALOG}}$		Full	-2.5	-	$V_+$	V
On-Resistance	$R_{\text{DS(on)}}$	$V_+ = 2.7 \text{ V},$ $V_S = ((V_+) - 4.5 \text{ V}, -1 \text{ V}, 0 \text{ V}, 1 \text{ V}, 2 \text{ V}, V_+),$ $I_S = 100 \text{ mA}$	Room	-	0.45	1	$\Omega$
On-Resistance Match	$\Delta R_{\text{ON}}$		Full	-	-	1.3	
On-Resistance Flatness	$R_{\text{ON}} \text{ Flatness}$		Room	-	0.1	-	
Switch Off Leakage Current	$I_{\text{NO/NC(off)}}$		Room	-	0.3	-	
	$I_{\text{COM(off)}}$	$V_+ = 2.7 \text{ V},$ $V_{\text{NC/NO}} = -2.5 \text{ V} \text{ or } 2.5 \text{ V},$ $V_{\text{COM}} = 2.5 \text{ V} \text{ or } -2.5 \text{ V}$	Room	-	50	-	nA
Channel On Leakage Current	$I_{\text{COM(on)}}$		Full	-250	-	250	
			Room	-	50	-	
			Full	-250	-	250	
<b>Digital Control</b>							
Input Voltage High	$V_{\text{IH}}$	$V_+ = 2.7 \text{ V} \text{ to } 4.3 \text{ V}$	Full	1.4	-	-	V
Input Voltage Low	$V_{\text{IL}}$		Full	-	-	0.6	
Input Capacitance	$C_{\text{IN}}$		Room	-	6.5	-	pF
Input Current	$I_{\text{INL}} \text{ or } I_{\text{INH}}$	$V_{\text{IN}} = 0 \text{ or } V_+$	Full	-1	-	1	$\mu\text{A}$

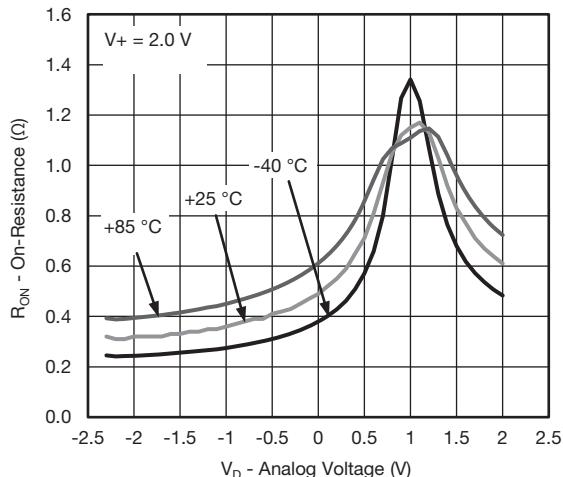
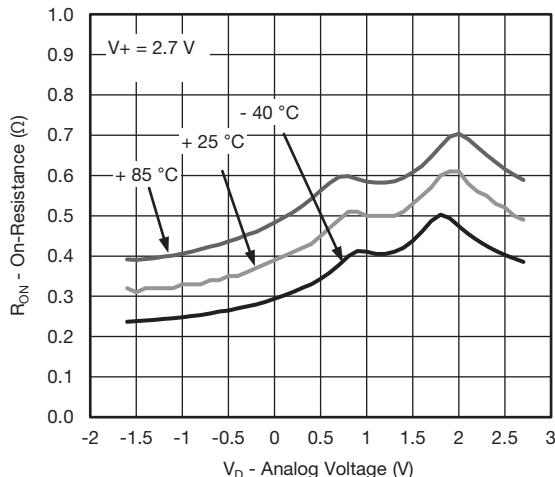
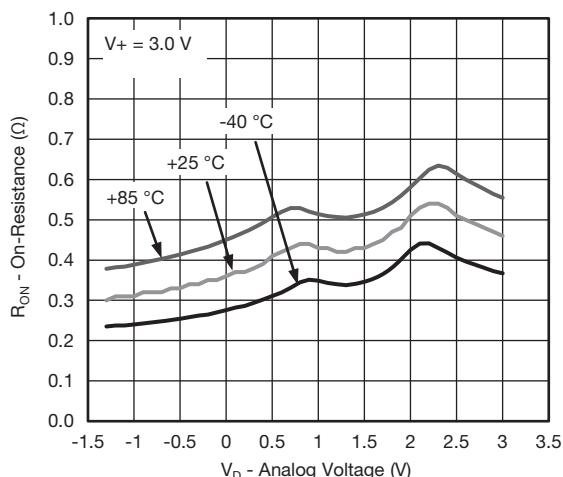
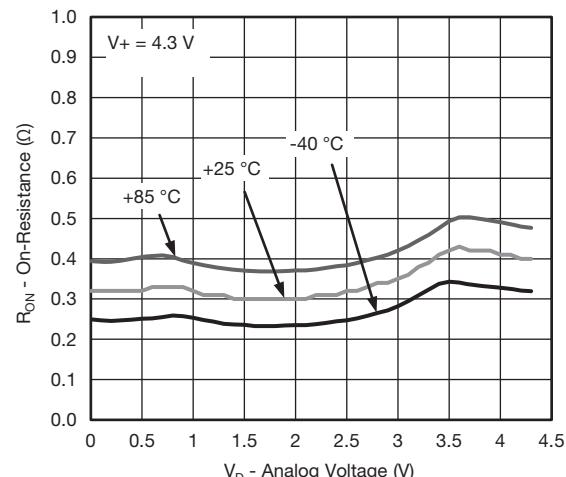
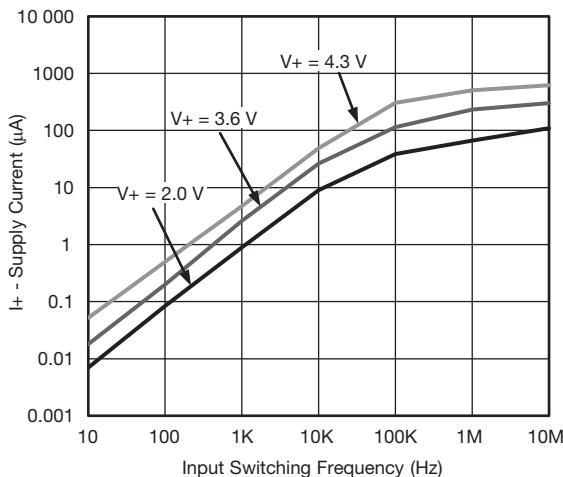
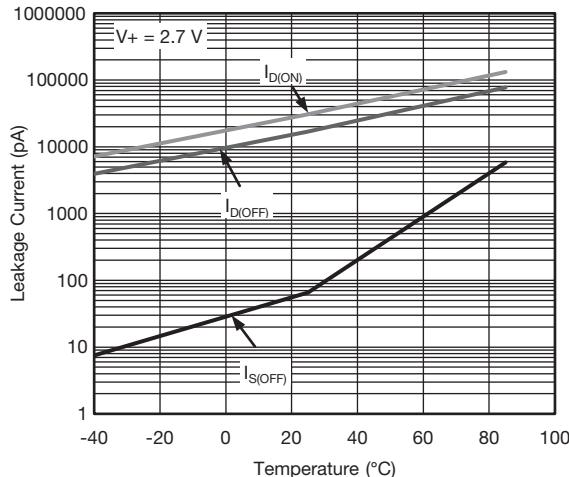
<b>SPECIFICATIONS</b> ( $V_+ = 2.7 \text{ V}, \pm 10\%$ )		<b>TEST CONDITIONS OTHERWISE UNLESS SPECIFIED</b>	<b>TEMP.<sup>a</sup></b>	<b>LIMITS</b> $-40^\circ\text{C}$ to $+85^\circ\text{C}$			<b>UNIT</b>
<b>PARAMETER</b>	<b>SYMBOL</b>			<b>MIN.<sup>b</sup></b>	<b>TYP.<sup>c</sup></b>	<b>MAX.<sup>b</sup></b>	
<b>Dynamic Characteristics</b>							
Break-Before-Make Time <sup>e, d</sup>	$t_{BBM}$	$V_+ = 3 \text{ V}, V_S = 1.5 \text{ V}, R_L = 50 \Omega, C_L = 35 \text{ pF}$	Room	800	1160	-	ns
Enable Turn-On Time <sup>e, d</sup>	$t_{ON(EN)}$		Full	1000	-	-	
Enable Turn-Off Time <sup>e, d</sup>	$t_{OFF(EN)}$		Room	-	1200	2100	
Charge Injection <sup>d</sup>	$Q_{INJ}$		Full	-	-	2500	
Total Harmonic Distortion Plus Noise <sup>d</sup>	THD+N		Room	-	33	130	
Off-Isolation <sup>d</sup>	OIRR		Full	-	-	150	
Crosstalk <sup>d, f</sup>	$X_{TALK}$	$V_+ = 3 \text{ V}, R_L = 50 \Omega, C_L = 5 \text{ pF}, f = 300 \text{ kHz}$	Room	-	4	-	pC
Bandwidth <sup>d</sup>	BW		Room	-	< -98	-	dB
Channel-Off Capacitance <sup>d</sup>	$C_{NC/NO(off)}$		Room	-	-54	-	dB
Channel-On Capacitance <sup>d</sup>	$C_{COM/NC/NO(on)}$		Room	-	-60	-	MHz
Power Supply			Room	-	49	-	MHz
Power Supply Range	$V_+$		Room	-	36	-	pF
Power Supply Current	$I_+$	$V_{IN} = 0 \text{ V}, \text{ or } V_+$	Full	-	106	-	$\mu\text{A}$

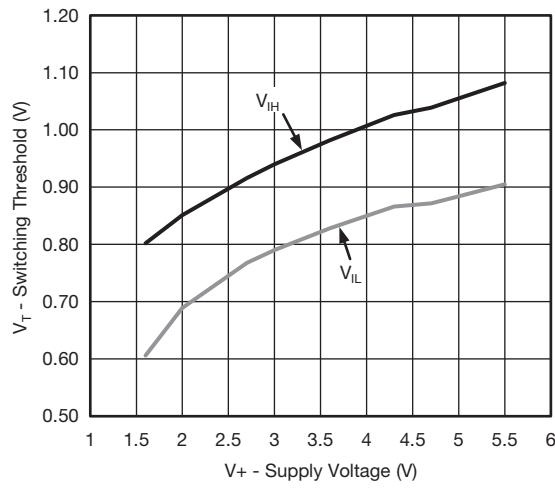
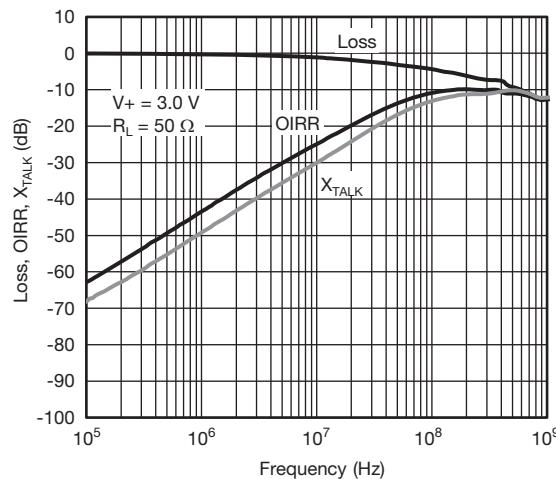
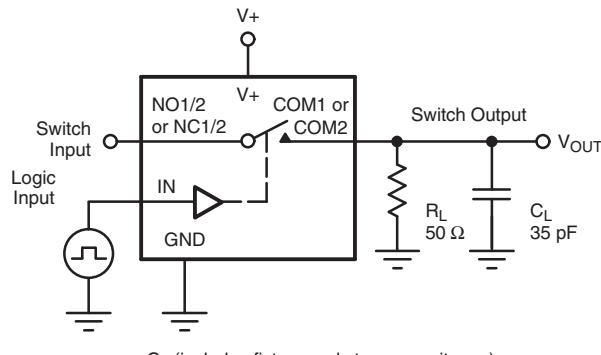
**Notes**

- a. Room =  $25^\circ\text{C}$ , Full = as determined by the operating suffix
- b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet
- c. Typical values are for design aid only, not guaranteed nor subject to production testing
- d. Guaranteed by design, not subjected to production test
- e.  $V_{IN} = V_+$  voltage to perform proper function
- f. Crosstalk measured between channels

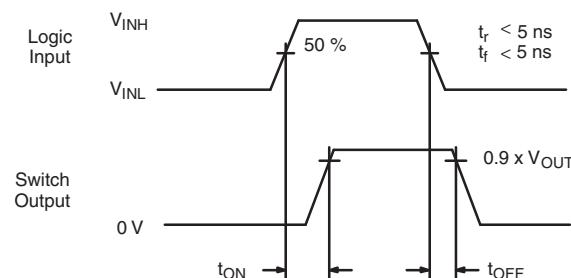
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** ( $25^\circ\text{C}$ , unless otherwise noted)

**On-Resistance vs.  $V_D$  and Single Supply Voltage**

**On-Resistance vs. Analog Voltage and Temperature**

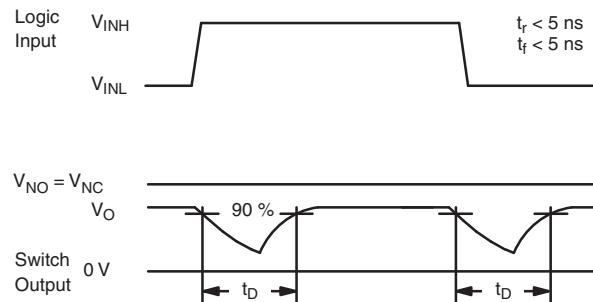
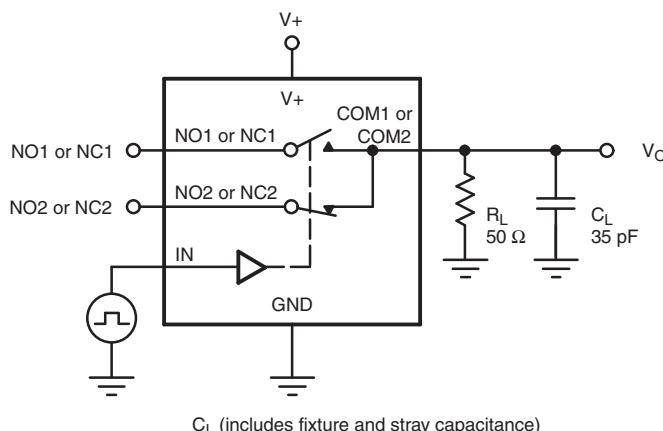
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**On-Resistance vs. Analog Voltage and Temperature**

**On-Resistance vs. Analog Voltage and Temperature**

**On-Resistance vs. Analog Voltage and Temperature**

**On-Resistance vs. Analog Voltage and Temperature**

**Supply Current vs. Input Switching Frequency**

**Leakage Current vs. Temperature**

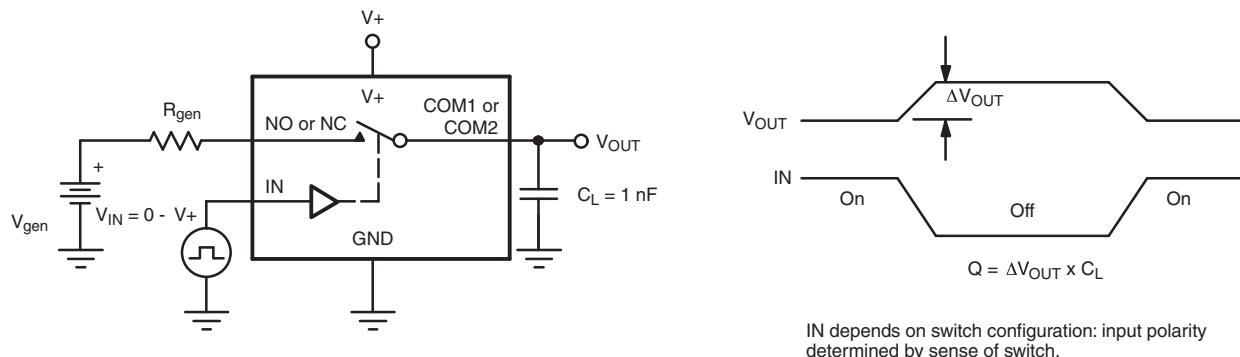
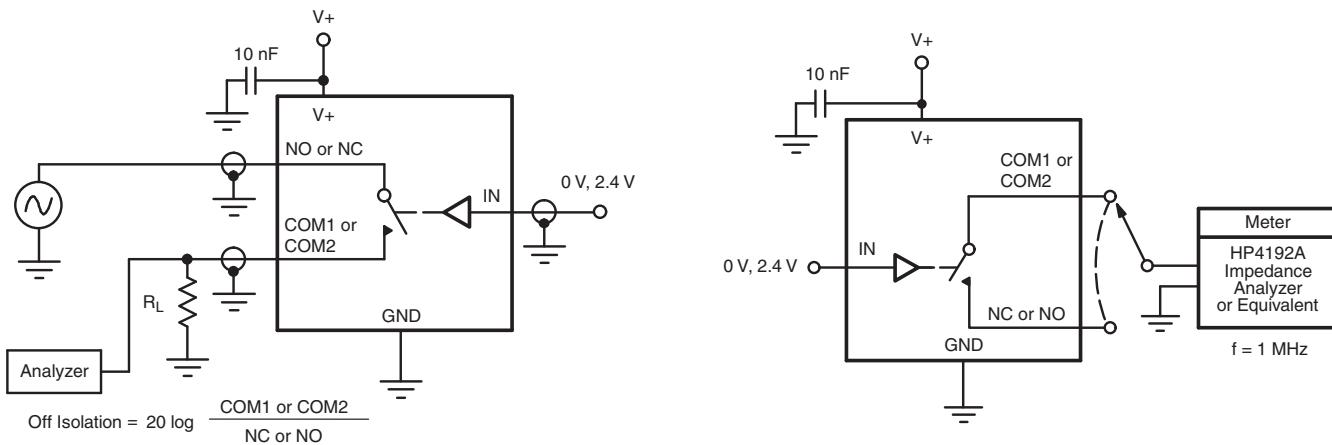
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Switching Threshold vs. Supply Voltage**

**Insertion Loss, Off-Isolation, Crosstalk vs. Frequency**
**TEST CIRCUITS**


$$V_{OUT} = V_{COM} \left( \frac{R_L}{R_L + R_{ON}} \right)$$



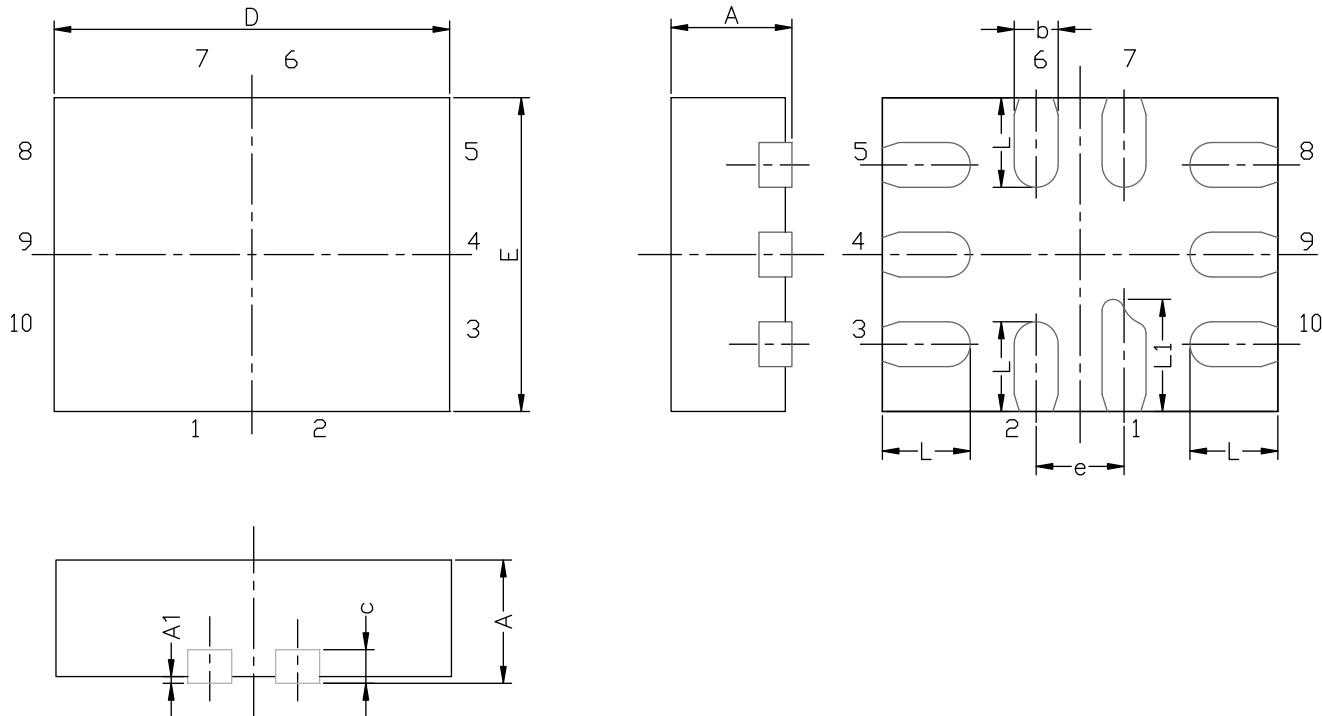
Logic "1" = Switch on  
Logic input waveforms inverted for switches that have the opposite logic sense.

**Fig. 1 - Switching Time**

**Fig. 2 - Break-Before-Make Interval**

**TEST CIRCUITS**

**Fig. 3 - Charge Injection**

**Fig. 4 - Off-Isolation**
**Fig. 5 - Channel Off/On Capacitance**

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## MINI QFN-10L CASE OUTLINE



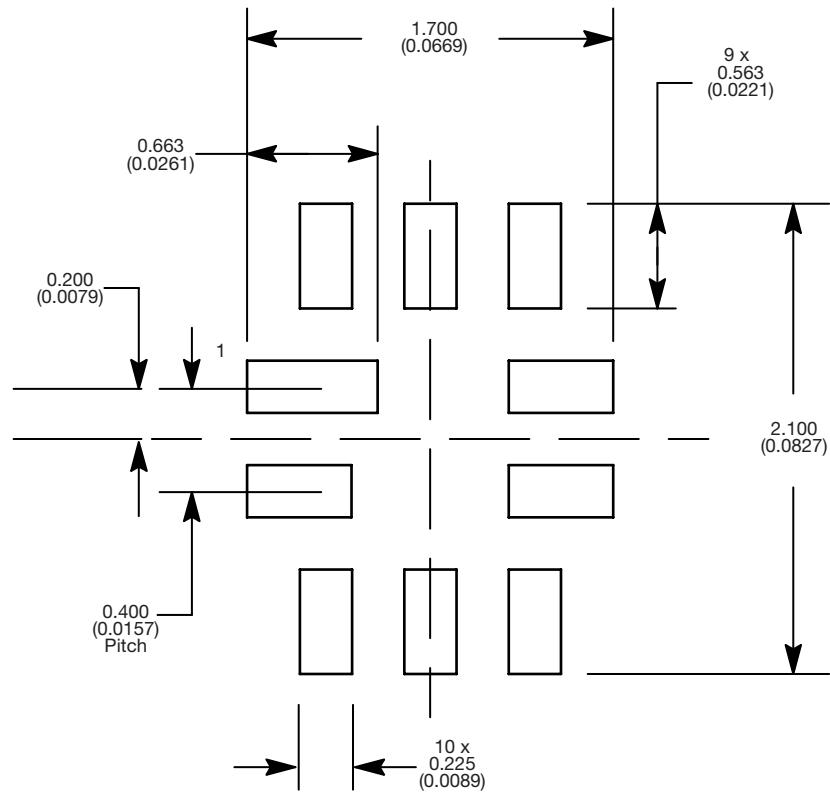
DIM	MILLIMETERS			INCHES		
	MIN.	NAM.	MAX.	MIN.	NAM.	MAX.
A	0.45	0.55	0.60	0.0177	0.0217	0.0236
A1	0.00	-	0.05	0.000	-	0.002
b	0.15	0.20	0.25	0.006	0.008	0.010
c	0.150 or 0.127 REF <sup>(1)</sup>			0.006 or 0.005 REF <sup>(1)</sup>		
D	1.70	1.80	1.90	0.067	0.071	0.075
E	1.30	1.40	1.50	0.051	0.055	0.059
e	0.40 BSC			0.016 BSC		
L	0.35	0.40	0.45	0.014	0.016	0.018
L1	0.45	0.50	0.55	0.0177	0.0197	0.0217

**Note**

<sup>(1)</sup> The dimension depends on the leadframe that assembly house used.

 ECN T16-0163-Rev. B, 16-May-16  
 DWG: 5957

### RECOMMENDED MINIMUM PADS FOR MINI QFN 10L



Mounting Footprint  
Dimensions in mm (inch)



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