

LGA C Series

15 - 100 Watts

Data Sheet

Total Power: 15 - 100 W
of Outputs: Single

SPECIAL FEATURES

- 3, 6, 10 and 20 A output current rating
- Wide input voltage range; up to 14 V
- Adjustable output voltage; 0.59-5.1 V
- Excellent transient response
- High efficiency
- Output margining
- Power enable
- Minimal airflow requirement
- Termination voltage capability
- Ultra compact profile and footprint
- RoHS compliant
- Remote sense
- Termination voltage capability

SAFETY

- Designed to meet EN60950
- International Standards for Solderability: J-STD-002B IEC-60068-2-58



Electrical Specifications

Output		3/6/10 A Models		20 A Model	
Output voltage	See Page 3	0.59 - 5.1 V			
Output setpoint accuracy	0.1% trim resistors	±1.0%			
Line regulation		±0.2%			
Load regulation		±0.5%			
Max Current Max Power		15/30/50 W		100 W	
Overshoot	At turn-on	0%			
Undershoot	At turn-off	0 mV			
Ripple and noise 5 Hz to 20 MHz	See Note 1 V _{in} = 5 V, V _{out} = 2.5 V	20/25/30 mV		30 mV	
Transient response	See Notes 1 and 2 V _{in} = 5 V, V _{out} = 2.5 V	100/160/160 mV 15 μs recovery to within regulation band		1175 mV 15 μs recovery to within regulation band	
Input					
Input voltage range ³		3 - 14 Vdc		4.5 - 14 Vdc	
Input current	Enable On at (0 A) Enable Off	50 mA 5 mA			
Start-up time	Power up Enable On/Off	3 ms 2 ms			
General					
Efficiency	V _{in} = 5 V _{out} , V _O = 2.5 V, I _{out} = 50% I _{max}	92% typ.		92% typ.	
Switching frequency		1 MHz		800 kHz	
Material flammability		UL94V-0			
MTBF	12 V @ 40 °C 100% load Bellcore 332	> 20,000,000 hours			
Coplanarity		150 μm			
Thermal performance See Technical Reference Note	Operating ambient Non-operating ambient	-40 °C to +85 °C -40 °C to +125 °C			

Electrical Specifications

Protection		
Short circuit	Hiccup, non-latching	
Overvoltage	Hiccup, non-latching	
Minimum Recommended System Capacitance	3/6/10 A Model	20 A Model
Short circuit	1 μ F	10 μ F

Operating Information

Standard Model Numbers	Output Power (Max.)	Input Voltage	Output Voltage	Output Current		Efficiency (Typical)	Regulation	
				Min	Max		Min	Max
LGA03C-00SADJJ	15 W	3 - 14.0 Vdc	0.59 - 5.1 Vdc	0 A	3 A	92%	$\pm 0.2\%$	$\pm 0.5\%$
LGA06C-00SADJJ	30 W	3 - 14.0 Vdc	0.59 - 5.1 Vdc	0 A	6 A	92%	$\pm 0.2\%$	$\pm 0.5\%$
LGA10C-00SADJJ	50 W	3 - 14.0 Vdc	0.59 - 5.1 Vdc	0 A	10 A	92%	$\pm 0.2\%$	$\pm 0.5\%$
LGA20C-01SADJJ	100 W	4.5 - 14.0 Vdc	0.59 - 5.1 Vdc	0 A	20 A	91%	$\pm 0.2\%$	$\pm 0.5\%$

Model Number System with Options



Product Family	Rated Output Current	Performance	Input Voltage	Type of Output	Options	RoHS Compliance
LGA	XX	C	00	SADJ	X	J
	Rated Output Current 03 = 3 Amp 06 = 6 Amp 10 = 10 Amp 20 = 20 Amp	Performance C = Cost Optimized	Input Voltage 00 = 3 - 14.0 V 01 = 4.5 - 14.0 V	Type of Output Single Adjustable Output	Options X = Various Options (see Sales Rep)	RoHS Compliance J = Pb free (RoHS 6/6 compliant)

Heatsink Number System with Options



Product Family	Product	Purpose	Height*
LGA	HTSK	KIT	XXX
Land Grid Array	Heatsink	Heatsink and Adhesive	Total Height (LGA20 + Heatsink) 045 = 0.45" 048 = 0.48" 050 = 0.50"

* Height is the total height of the LGA20C-00SADJJ with heatsink attached.

Application Equations

Setting Output Voltage

Default output voltage: 0.591 V

The output voltage may be adjusted with a resistor placed between the "Trim" and "-Sense" pin.

The formula for calculating the value of this resistor is:

$$R_{\text{trim}} (\text{k}\Omega) = \frac{1.182}{V_{\text{out}} - 0.591}$$

See Technical Reference Note for other trimming methods.

Setting Margin Control

To margin the output up, pull the margin control pin high. To margin down, pull the margin control pin low. If the pin is left floating, the feature is disabled. The maximum margining range is $\pm 33\%$ of the output default voltage setting, with maximum output at 5.5 V

$$V_{\text{margin_up}} = 0.1182 * \frac{R_{\text{margin}}}{R_{\text{ofs+}}} * \frac{R_{\text{trim}} + 2\text{k}}{R_{\text{trim}}}$$

$$V_{\text{margin_down}} = 0.1182 * \frac{R_{\text{margin}}}{R_{\text{ofs-}}} * \frac{R_{\text{trim}} + 2\text{k}}{R_{\text{trim}}}$$

Setting Under Voltage Lock Out – 3, 6, 10 A Models

Default Turn-on voltage: 2.9 V (300 mV Hysteresis)

The Turn-on voltage may be adjusted with a resistor placed between the "Enable" and "Ground" pins.

The formula for calculating the value of this resistor is:

$$R_{\text{UVLO}} (\text{k}\Omega) = \frac{14.81 * 6.81}{(6.81 * V_{\text{Turn_on}}) - 18.16}$$

***ONLY USE WITH OPEN COLLECTOR DEVICE**
***DO NOT DRIVE PIN WITH A VOLTAGE**

Setting Under Voltage Lock Out – 20 A Models

Default Turn-on voltage: 4.3 V (300 mV Hysteresis)

The Turn-on voltage may be adjusted with a resistor placed between the "Enable" and "Ground" pins.

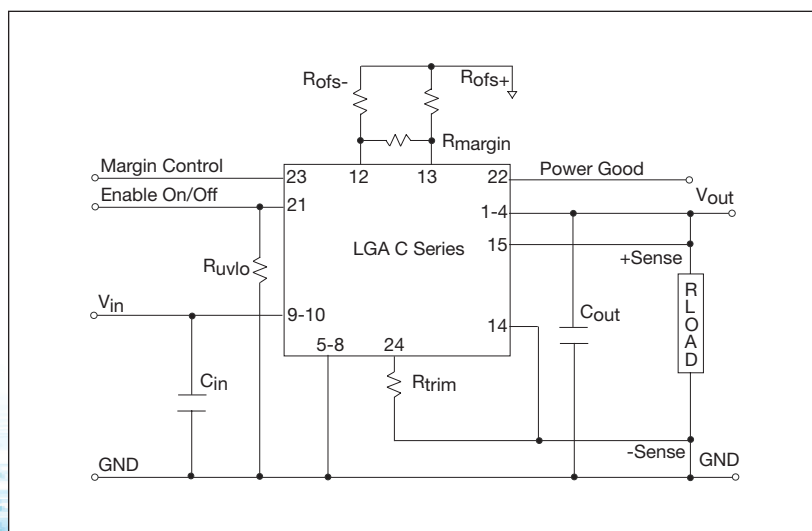
The formula for calculating the value of this resistor is:

$$R_{\text{UVLO}} (\text{k}\Omega) = \frac{30.1 * 4.22}{(8.577 * V_{\text{Turn_on}}) - 34.32}$$

***ONLY USE WITH OPEN COLLECTOR DEVICE**
***DO NOT DRIVE PIN WITH A VOLTAGE**

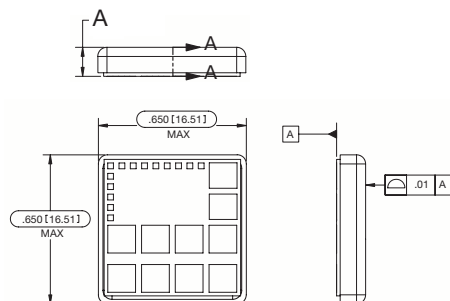
Notes:

1. Measured as per recommended minimum system capacitance.
2. $di/dt = 10 \text{ A}/\mu\text{s}$, $12 \text{ V}_{\text{in}} = \text{Norm}$, $T_c = 25^\circ\text{C}$, load change = 50% to 100% I_{max} .
3. Internal input capacitance is rated 16 Vdc maximum.



External input fusing is recommended.

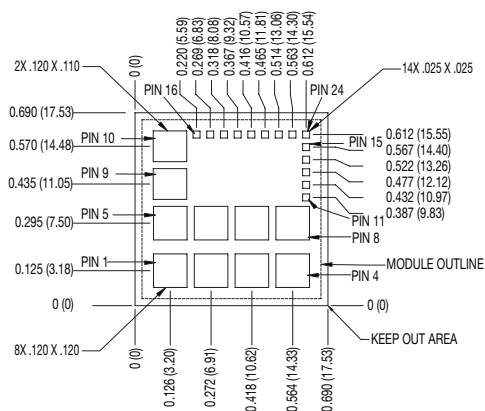
Mechanical Drawing and Footprint



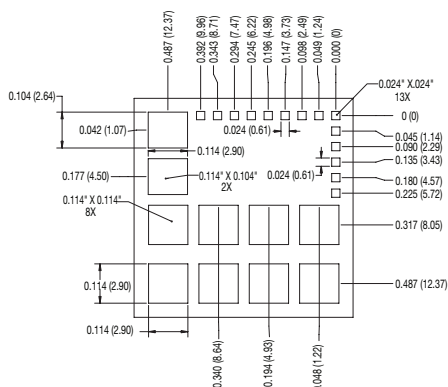
Component Height

Model #	DIM A in (mm)
LGA03 LGA06 LGA10	0.129 (3.27)
LGA20	0.210 (5.33)

Recommended System Board Footprint



Recommended Solder Paste Stencil



Pin Assignments

Single Output

1	Vout
2	Vout
3	Vout
4	Vout
5	GND
6	GND
7	GND
8	GND
9	Vin
10	Vin
11	NC
12	- Offset
13	+ Offset
14	- Sense
15	+ Sense
16	NC
17	NC
18	NC
19	NC
20	NC
21	Enable
22	Power Good
23	Margin Control
23	Trim

WORLDWIDE OFFICES

Americas

2900 S.Diablo Way
Tempe, AZ 85282
USA
+1 888 412 7832

Europe (UK)

Waterfront Business Park
Merry Hill, Dudley
West Midlands, DY5 1LX
United Kingdom
+44 (0) 1384 842 211

Asia (HK)

14/F, Lu Plaza
2 Wing Yip Street
Kwun Tong, Kowloon
Hong Kong
+852 2176 3333

ARTESYN
EMBEDDED TECHNOLOGIES

www.artesyn.com

While every precaution has been taken to ensure accuracy and completeness in this literature, Artesyn Embedded Technologies assumes no responsibility, and disclaims all liability for damages resulting from use of this information or for any errors or omissions. Artesyn Embedded Technologies, Artesyn and the Artesyn Embedded Technologies logo are trademarks and service marks of Artesyn Embedded Technologies, Inc. All other names and logos referred to are trade names, trademarks, or registered trademarks of their respective owners. © 2015 Artesyn Embedded Technologies, Inc.

For more information: www.artesyn.com/power
For support: productsupport.ep@artesyn.com

Данный компонент на территории Российской Федерации

Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

<http://moschip.ru/get-element>

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru

moschip.ru_4

moschip.ru_6

moschip.ru_9