

Typical Applications

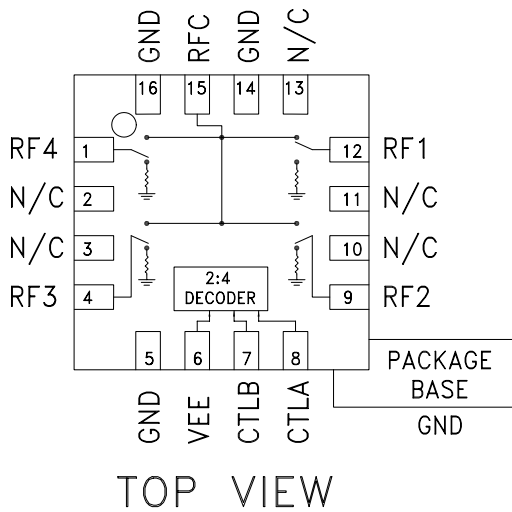
This switch is suitable for usage in DC - 8.0 GHz 50-Ohm or 75-Ohm systems:

- Broadband
- Fiber Optics
- Switched Filter Banks
- Wireless below 8 GHz

Features

- Broadband Performance: DC - 8 GHz
- High Isolation: 40 dB@ 6 GHz
- Low Insertion Loss: 1.8 dB@ 6 GHz
- Integrated 2:4 TTL Decoder
- 16 Lead 3x3mm QFN Package: 9 mm²

Functional Diagram

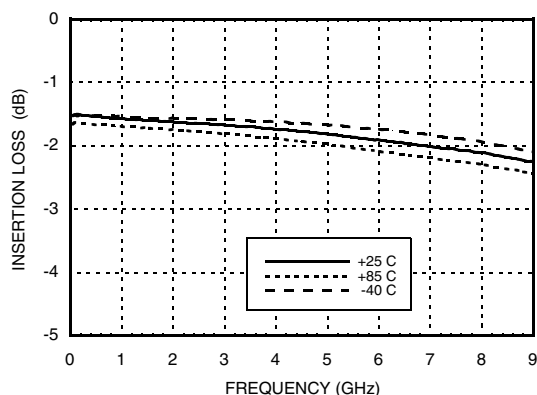
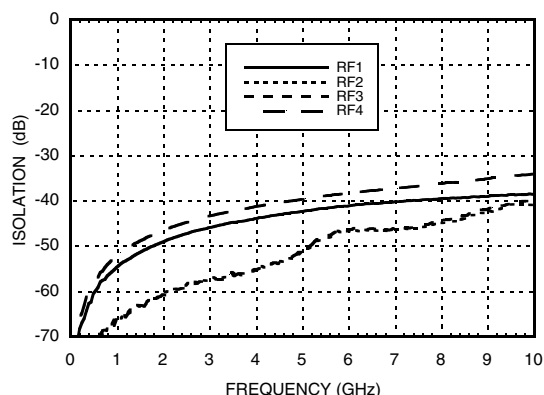
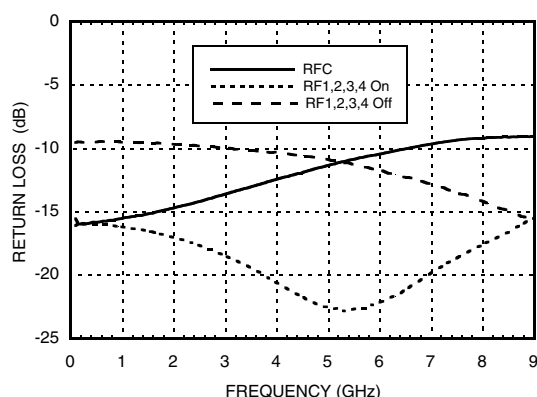
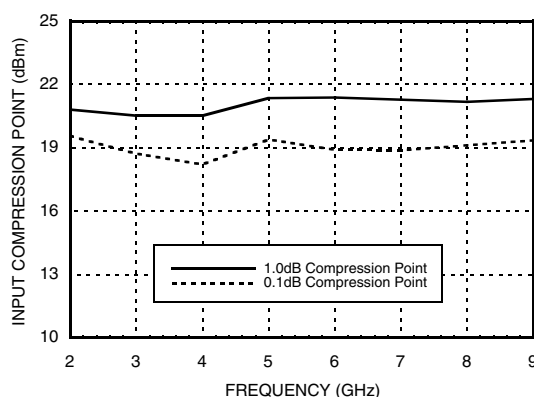
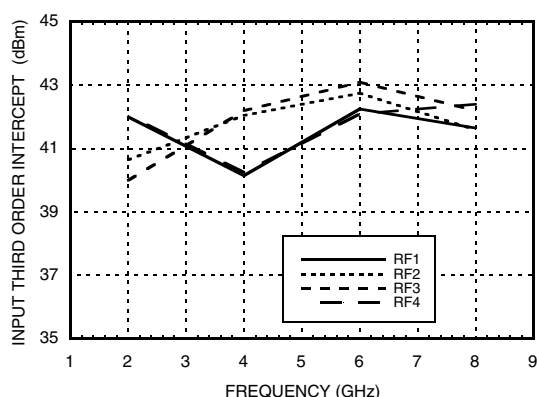


General Description

The HMC344LP3 & HMC344LP3E are broadband non-reflective GaAs MESFET SP4T switches in low cost leadless surface mount packages. Covering DC to 8 GHz, this switch offers high isolation and low insertion loss and extends the frequency coverage of Hittite's SP4T switch product line. This switch also includes an on board binary decoder circuit which reduces the required logic control lines to two. The switch operates using a negative control voltage of 0/-5V, and requires a fixed bias of -5V.

Electrical Specifications, $T_A = +25^\circ \text{C}$, With 0/-5V Control, 50 Ohm System

Parameter	Frequency	Min.	Typ.	Max.	Units
Insertion Loss	DC - 2.0 GHz		1.6	2.0	dB
	DC - 6.0 GHz		1.8	2.2	dB
	DC - 8.0 GHz		2.1	2.5	dB
Isolation	DC - 2.0 GHz	43	48		dB
	DC - 4.0 GHz	36	41		dB
	DC - 6.0 GHz	34	40		dB
	DC - 8.0 GHz	31	36		dB
Return Loss	DC - 2.0 GHz	12	15		dB
	DC - 4.0 GHz	9	12		dB
	DC - 6.0 GHz	8	11		dB
	DC - 8.0 GHz	5	8		dB
Return Loss	DC - 8.0 GHz	7	10		dB
Input Power for 1 dB Compression	0.5 - 8.0 GHz	17	21		dBm
Input Third Order Intercept (Two-Tone Input Power = +7 dBm Each Tone)	0.5 - 8.0 GHz	37	40		dBm
Switching Characteristics	DC - 8.0 GHz		35		ns
			150		ns

**GaAs MMIC SP4T NON-REFLECTIVE
SWITCH, DC - 8 GHz**
Insertion Loss vs. Temperature

Isolation

Return Loss

0.1 and 1 dB Input Compression Point

Input Third Order Intercept Point

Bias Voltage & Current

Vee Range = -5.0 Vdc $\pm$ 10%		
Vee (Vdc)	Iee (Typ.) (mA)	Iee (Max.) (mA)
-5.0	3.0	6.0

Control Voltages

State	Bias Condition
Low	-3V to 0 Vdc @ 60 μ A Typical
High	-5 to -4.2 Vdc @ 5 μ A Typical

* Isolation is recorded above insertion loss & measured at output of switch.

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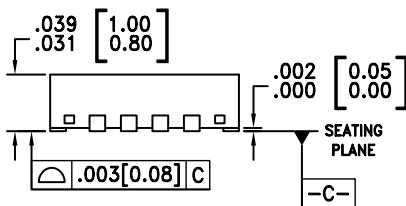
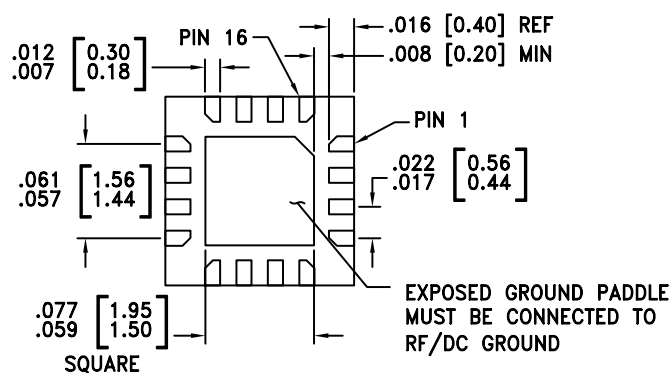
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Bias Voltage Range (Vee)	-7.0 Vdc
Control Voltage Range (A & B)	Vee -0.5V to +1.0 Vdc
Channel Temperature	150 °C
Thermal Resistance (Insertion Loss Path)	143 °C/W
Thermal Resistance (Terminated Path)	1,030 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
Maximum Input Power	+24 dBm
ESD Sensitivity (HBM)	Class 1A

Control Input		Signal Path State
A	B	RFCOM to:
High	High	RF1
Low	High	RF2
High	Low	RF3
Low	Low	RF4



The diagram shows a 4x16 barcode grid. The top row contains the numbers 1, 16, 13, and 12. The second row contains the characters 'N', 'N', 'N', and 'X'. The third row contains the characters 'X', 'X', 'X', and 'X'. The bottom row contains the character 'X' and the number 9. The grid is divided into two sections by a vertical line. The left section is labeled 'HMC PART NUMBER (DIGITS ONLY)' and the right section is labeled 'LOT NUMBER'. The top-left section contains the numbers .122, .114, and the digits [3.10] and [2.90]. The bottom-right section contains the numbers .122, .114, and the digits [3.10] and [2.90]. The grid is also labeled with the numbers 5 and 8 at the bottom.

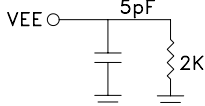
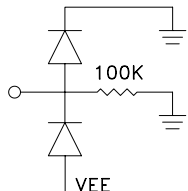


1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS].
3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM.
PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED PCB LAND PATTERN.

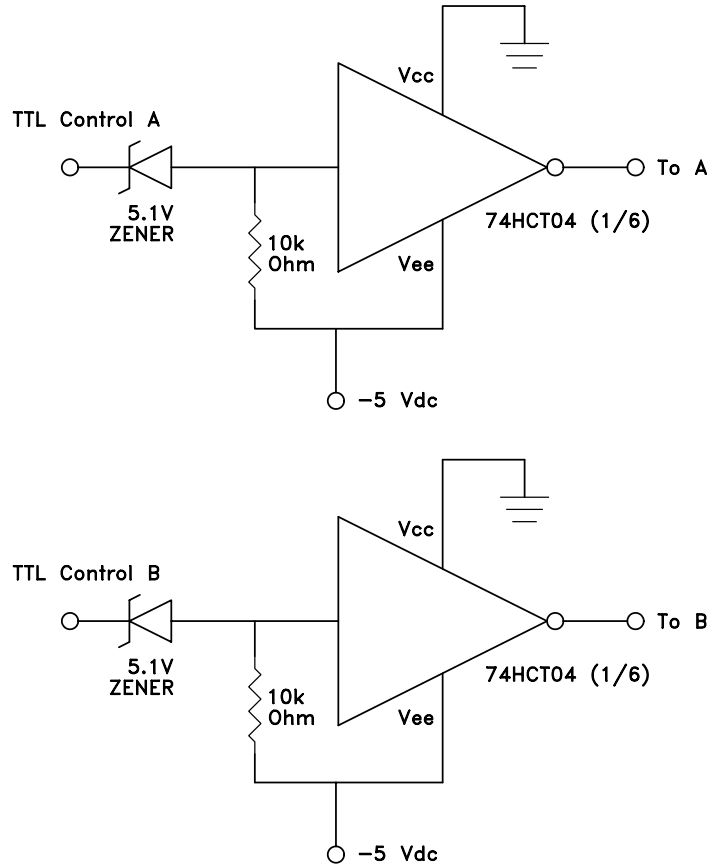
Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
HMC344LP3	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	344 XXXX
HMC344LP3E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>344</u> XXXX

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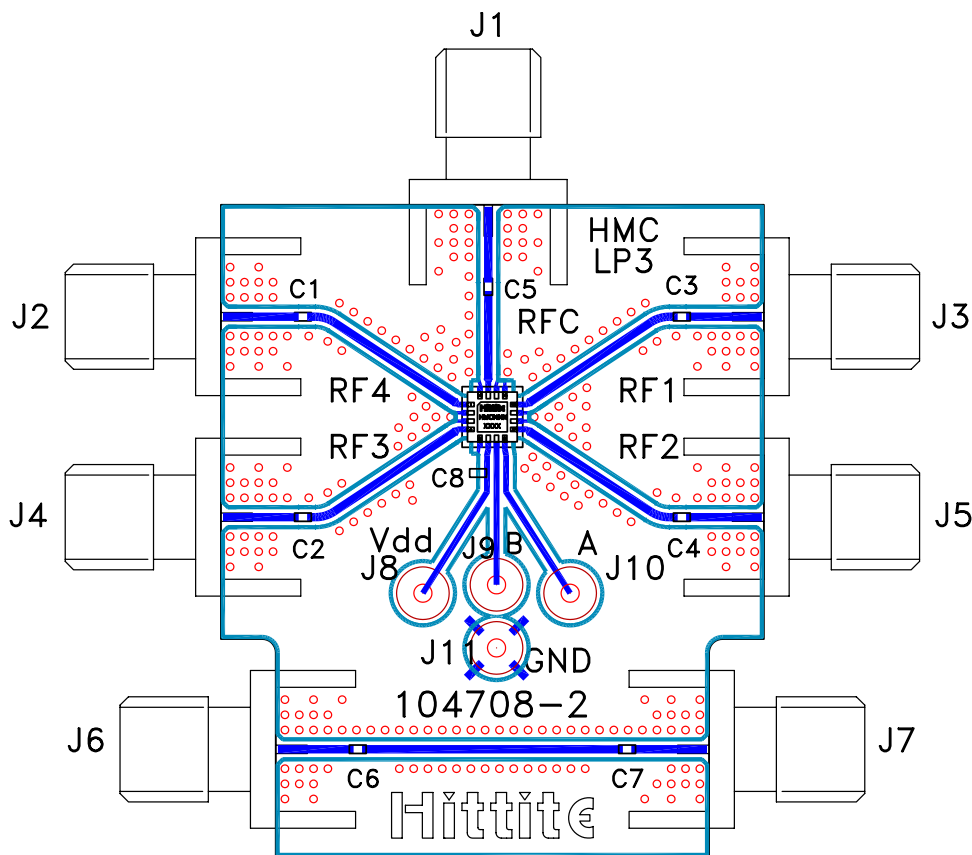
Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 4, 9, 12, 15	RF4, RF3, RF2, RF1, RFC	This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required if RF line potential is not equal to 0V.	
2, 3, 10, 11, 13	N/C	This pin should be connected to PCB RF ground to maximize isolation.	
5, 14, 16	GND	Package bottom has exposed metal paddle that must also be connected to PCB RF ground.	
6	VEE	Supply Voltage -5V ± 10%	
7	CTLB	See truth table and control voltage table.	
8	CTLA	See truth table and control voltage table.	

TTL Interface Circuit



Evaluation PCB



List of Materials for Evaluation PCB 105311 [1]

Item	Description
J1 - J7	PCB Mount SMA RF Connector
J8 - J11	DC Pin
C1 - C7	0 ohm res, 0402 Pkg. [3]
C8	10k pF Capacitor, 0603 Pkg.
U1	HMC344LP3 / HMC344LP3E SP4T Switch
PCB [2]	104708 Evaluation PCB 1.29"x1.55"

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

[3] Select and replace with a suitable capacitor value for applicable operating frequency range.

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 ohm impedance and the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru

moschip.ru_4

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moschip.ru_9