MAX5717 Evaluation Kit

Evaluates: MAX5717/MAX5719

General Description

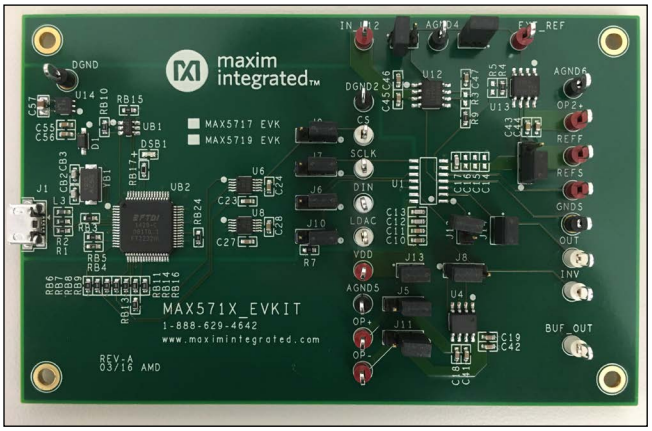
The MAX5717/MAX5719 evaluation kit (EV kit) demonstrates the MAX5717/MAX5719, 16/20-bit DAC. The EV kit includes a graphical user interface (GUI) that provides communication over SPI with an on-board master IC.

The MAX5717 EV kit comes with the MAX5717GSD+ installed and the MAX5719 comes with the MAX5719GSD+ installed.

Features

- On-Board SPI Controller
- On-Board Output Buffer (MAX9632)
- On-Board +4.096V Reference Voltage (MAX6126)
- Windows XP®, Windows® 7/8/8.1/10-Compatible Software

Board Photo



Quick Start

Required Equipment

- MAX5717/MAX5719 EV kit (includes micro-USB cable)
- Windows PC
- Digital Oscilloscope

Note: In the following sections, software-related items are identified by bolding. Text in bold refers to items directly from the EV kit software. Text in **bold and underlined** refers to items from the Windows operating system.

Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation:

1) Visit **HERE** to download the latest version of the EV kit software, *5717EVKit.ZIP*. Save the EV kit software to a temporary folder and uncompress the ZIP file.
2) Verify that all jumpers on the EV kit are in their default positions, as shown in Table 1.
3) Connect a probe from the oscilloscope at the OUT test point.
4) Connect the USB cable from the PC to the MAX5717/MAX5719 EV kit board.
5) Open the EV kit GUI, *MAX5717EVKit.exe* and select **Device->MAX5717PMB** option (or MAX5719PMB).
6) Click the **Scan Adapters** button. Then select the option **PMODxxxxxx** (where xxxxxx is numeric) and click the **Connect** button. See Figure 1 and Figure 2.
7) Click the **Sample Once** button and verify the oscilloscope waveform form matches that of the GUI.

***Ordering Information* appears at end of data sheet.**

19-8551; Rev 0; 6/16

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Table 1. Jumper Descriptions

JUMPER	SHUNT POSITION	DESCRIPTION
J2	Installed	Used for a buffered reference voltage. Connects the output voltage from the MAX6126 to the input of the MAX9632.
	Not installed*	Disconnects the output voltage from the MAX6126 to the input of the MAX9632.
J3	Installed*	Powers the MAX6126 using USB supply.
	Not installed	Disconnects the USB supply from the MAX6126. User must apply 5V at the IN_U12 test point to use the MAX6126.
J4	1-2*	Connects the output voltage from the MAX6126.
	2-3	User-Supplied Voltage Reference. Connects to the MAX9632 output. User must apply voltage reference at the EXT_REF test point and 5V at the OP2+ test point to power the MAX9632 (U13).
J5	1-2*	Powers the MAX9632's V_{CC} pin using USB supply.
	2-3	User-supplied power to the MAX9632 (U4). Apply 5V to 15V at the OP+ test point. Recommended for use in bipolar mode.
J6	1-2*	DIN from on-board master.
	2-3	User-supplied DIN. Apply respective signal at DIN test point.
J7	1-2*	SCLK from on-board master.
	2-3	User-supplied SCLK. Apply respective signal at SCLK test point.
J8	1-2*	Connects the negative input to the output of the MAX9632.
	2-3	Connects the MAX5717/MAX5719's INV pin to the negative input of the MAX9632.
J9	1-2*	CS from on-board master.
	2-3	User-Supplied CS. Apply respective signal at CS test point.
J10	1-2*	LDAC from On-Board Master.
	2-3	User-Supplied LDAC. Apply respective signal at LDAC test point.
J11	1-2	Do Not Use
	2-3*	User-Supplied Power to the MAX9632 (U4). Apply -5V to -15V at the OP- test point. Recommended for use in bipolar mode.
J12	Installed*	Connects the MAX5717/MAX5719's RFB pin to the MAX9632's output.
	Not installed	Disconnects the MAX5717/MAX5719's RFB pin to the MAX9632's output.
J13	Installed*	Powers the MAX5717/MAX5719 using the USB power.
	Not installed	User-supplied power. Apply 5V at the V_{DD} test point.
J14	Installed*	Connects the MAX5717/MAX5719's OUT pin to the MAX9632's IN+ pin.
	Not installed	Disconnects the MAX5717/MAX5719's OUT pin to the MAX9632's IN+ pin.

**Default position.*

General Description of Software

The main window of the MAX5717/MAX5719 EV kit software contains controls to evaluate the MAX5717 and MAX5719 ICs. Included is a waveform generator that allows the user to quickly evaluate the device.

USB2PMB Adapter

The controls within the **USB2PMB** groupbox allow the user to select the appropriate USB2PMB devices. When **Scan Adapters** button is pressed, it will update the dropdown list with all USB2PMB devices. With the EV kit connected to the PC, **PMODxxxxxx** (where xxxxxx is numeric) will appear within the dropdown list. Make the appropriate selection respective of the IC and press on the **Connect** button.

Sample

The Sample groupbox contains the sample rate and SPI SCLK options applicable to EV kit. Sample rate ranges from 5000sps to 200000sps. The SPI SCLK is selectable from 80kHz to 15MHz. Once configured, the user can either press the Sample Once or Sample Continuous button.

Transfer Function

The EV kit can work in either unipolar or bipolar mode. Please refer to Unipolar and Bipolar section of the data sheet.

Plot Configuration

The Scope display can plot the waveform generator's data in units of LSBs or voltage in a time domain. In addition, provides an FFT plot of the raw data for the MAX5717 or MAX5719.

Signal Setup

The **Signal Setup** controls are similar to a function generator and can be used to quickly evaluate the EV kit. It provides waveforms in sine, left and right sawtooth, triangle, square, and white noise. Amplitude, Offset, and Frequency can be adjusted for each waveform.

General Description of Hardware

The MAX5717/MAX5719 EV kit demonstrates the MAX5717/MAX5719, 16/20-bit DAC. The EV kit includes an on-board master IC for all SPI and I/O communication.

User-Supplied SPI

To evaluate the EV kit with a user-supplied SPI bus, place shunts on the 2-3 position of jumper J6, J7, J9, and J10. Apply the user-supplied SPI signals to the SCLK, CS, DIN, and LDAC test points. Make sure the return ground is the same as the MAX5717/MAX5719's ground.

User-Supplied V_{DD}

The MAX5717/MAX5719 is powered through USB by default. For user-supplied V_{DD}, remove the shunt of the jumper J13 and apply +5V at the V_{DD} test point.

User-Supplied Power for Voltage Reference (U12)

The voltage reference is powered through USB by default. For user-supplied power, remove the shunt of the jumper J3 and apply +5V at the IN_U12 test point.

User-Supplied Voltage Reference

The MAX9632 (U13) is an optional buffer for the reference of the MAX5717/MAX5719. Apply only +5V to the OP2+ test point when a voltage reference is applied at the EXT_REF test point.

User-Supplied Power for Buffer (U4)

The MAX9632 is an optional buffer for the output of the MAX5717/MAX5719. Place shunts on the 2-3 position of jumpers J5 and J11. Apply only +5V to +15V at the OP+ test point and -5V to -15V at the OP- test point.

Unipolar and Bipolar Output

When in unipolar output and the output buffer is in use, a shunt should be placed in the 2-3 position of jumper J5, 1-2 position of the jumper J8, 2-3 position of jumper J11, and open position of jumper 12. When in bipolar output, a shunt should be placed in the 2-3 position of jumper J5, 2-3 position of the jumper J8, 2-3 position of jumper J11, and closed position of jumper J12.

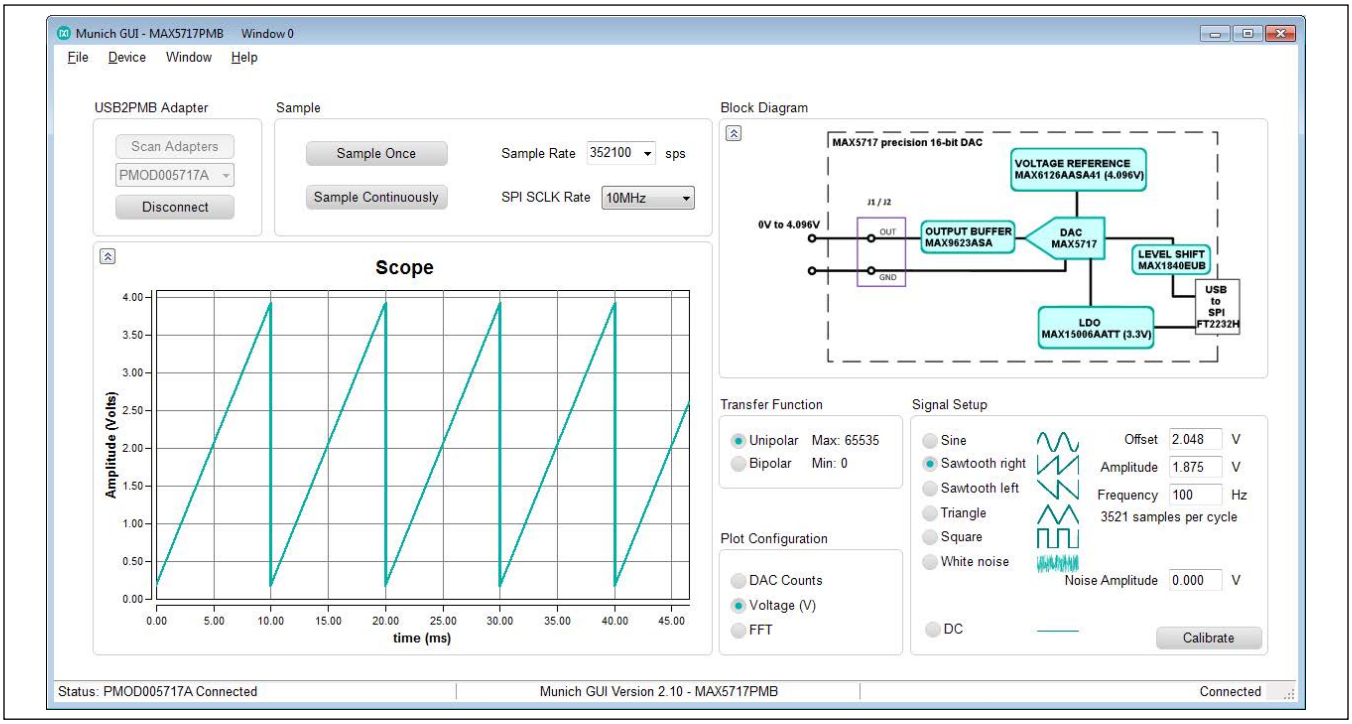

Figure 1. MAX5717 EV Kit Main Window

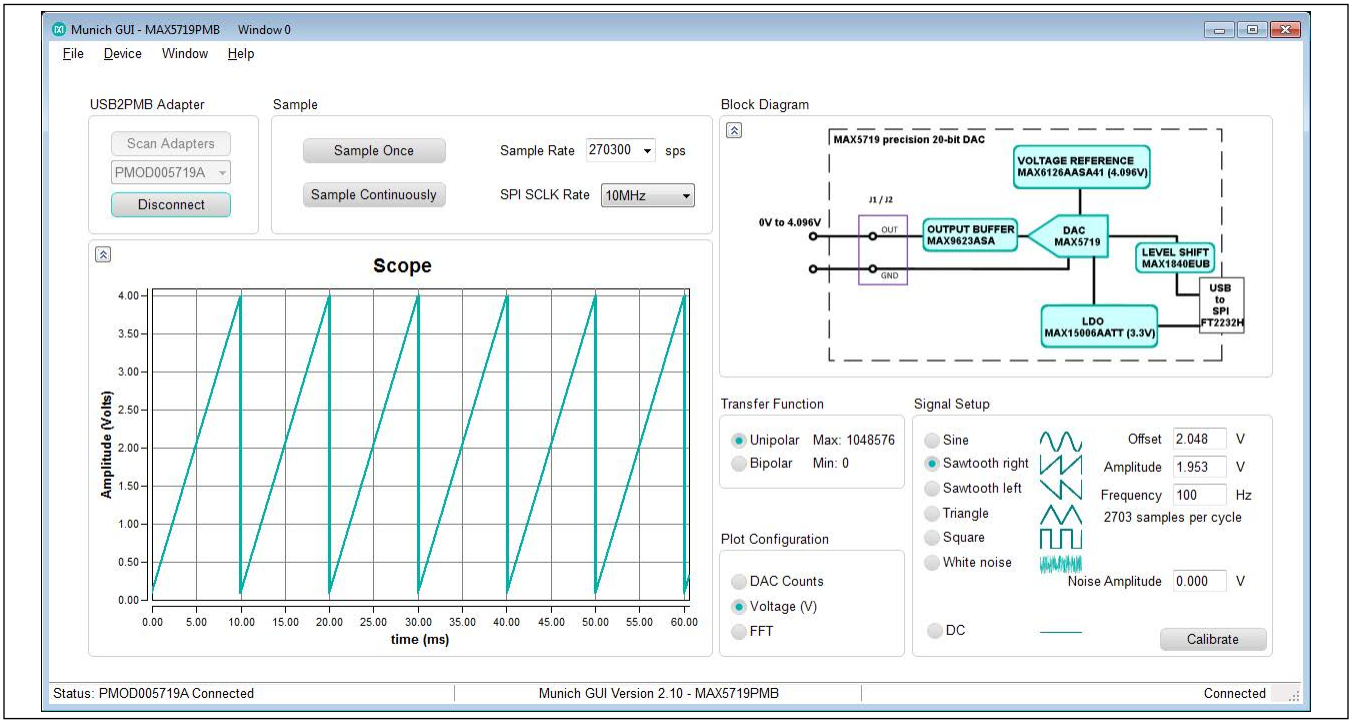

Figure 2. MAX5719 EV Kit Main Window

Component Information, PCB Layout, and Schematics

See the following links for component information, PCB layout diagrams, and schematic.

- **MAX5717/MAX5719 EV BOM**
- **MAX5717/MAX5719 EV PCB Layout**
- **MAX5717/MAX5719 EV Schematic**

Ordering Information

Part	TYPE
MAX5717EVKIT#	EV KIT
MAX5719EVKIT#	EV KIT

#Denotes RoHS compliant.

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	6/16	Initial Release	—

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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TITLE: Bill of Materials

DATE: 03/03/2016

DESIGN: max571x_evkit_a

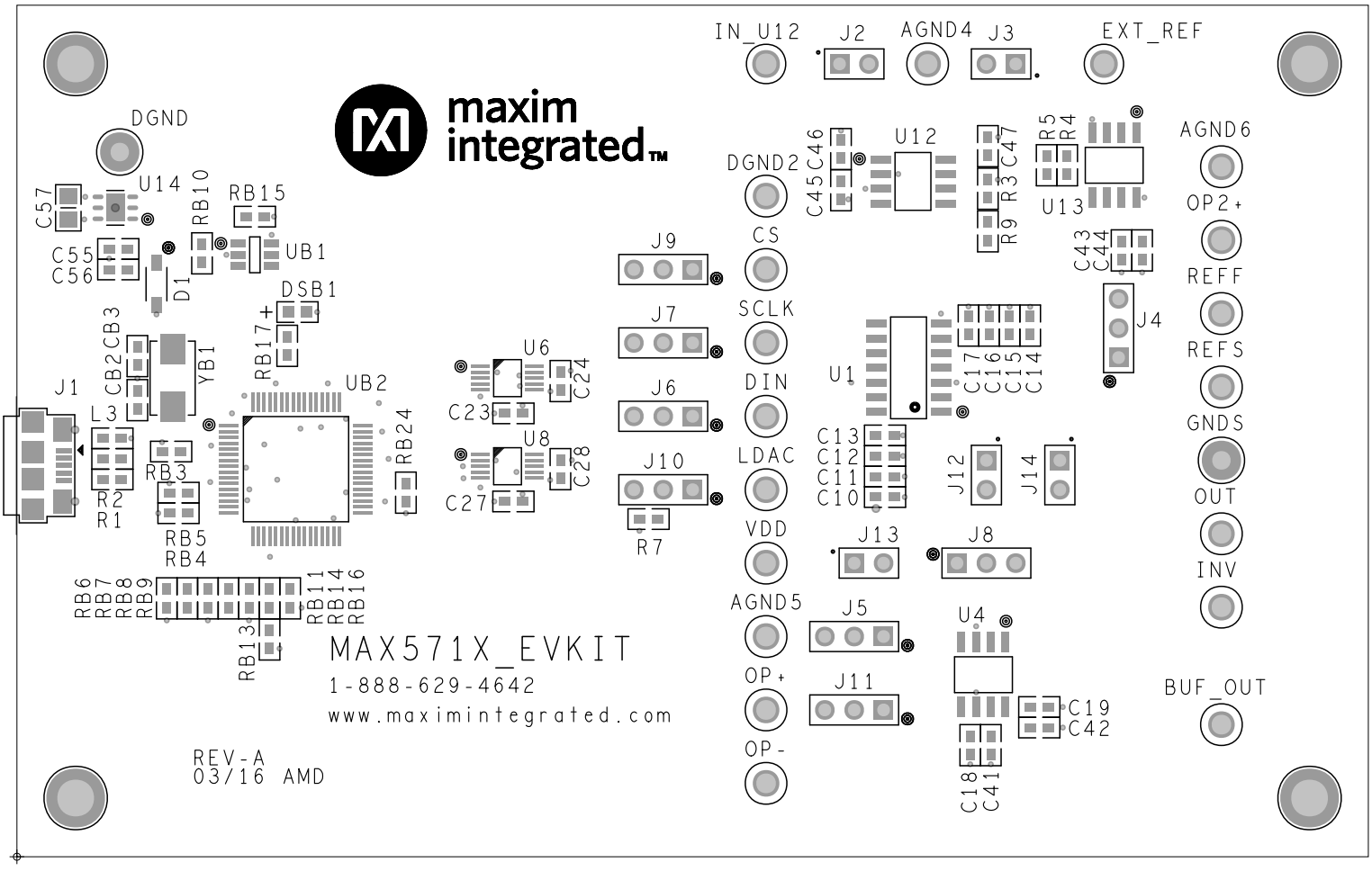
VARIANT:dni

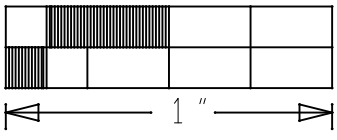
ITEM	QTY	REF DES	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
1	3	AGND4-AGND6	5011	?	5011	TEST POINT; PIN DIA=0.125IN; TOTAL LENGTH=0.445IN; BOARD HOLE=0.063IN; BLACK; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH; RECOMMENDED FOR BOARD THICKNESS=0.062IN
2	7	CS, DIN, INV, OUT, LDAC, SCLK, BUF_OUT	5012	?	5012	TEST POINT; PIN DIA=0.125IN; TOTAL LENGTH=0.445IN; BOARD HOLE=0.063IN; WHITE; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH; RECOMMENDED FOR BOARD THICKNESS=0.062IN
3	10	C1, C3, C5-C7, C21, C22, C29, C30, C56	C0603C104K3RAC; GRM188R71E104KA01; C1608X7R1E104K	KEMET/MURATA/TDK	0.1UF	CAPACITOR; SMT; 0603; CERAMIC; 0.1uF; 25V; 10%; X7R; -55degC to + 125degC; +/-15% from -55degC to +125degC; NOT RECOMMENDED FOR NEW DESIGN USE - 20-000u1-01
4	3	C2, C4, C20	GRM21BR61E475KA	MURATA	4.7UF	CAPACITOR; SMT (0805); CERAMIC CHIP; 4.7UF; 25V; TOL=10%; MODEL=X5R; TG=-55 DEGC TO +125 DEGC; TC=+/
5	7	C10, C18, C19, C44-C47	C0603C104K4RAC; GCM188R71C104KA37; C1608X7R1C104K; GRM188R71C104K; C0603X7R160-104KNE	KEMET/MURATA/TDK/VENKEL LTD.	0.1UF	CAPACITOR; SMT (0603); CERAMIC CHIP; 0.1UF; 16V; TOL=10%; TG=-55 DEGC TO +125 DEGC; TC=X7R; NOT RECOMMENDED FOR NEW DESIGN USE 20-000u1-01
6	1	C11	C0603C103K5RAC; GRM188R71H103K;C0603X7R500-103KNE	KEMET/MURATA/VENKEL LTD.	0.01UF	CAPACITOR; SMT; 0603; CERAMIC; 0.01uF; 50V; 10%; X7R; -55degC to + 125degC; USE 20-00u01-M8 FOR NEW DESIGN
7	1	C12	C0603C102K5RAC; GRM188R71H102KA01; C0603X7R500-102KNE	KEMET/MURATA/VENKEL	1000PF	CAPACITOR; SMT; 0603; CERAMIC; 1000pF; 50V; 10%; X7R; -55degC to + 125degC; +/-15% from -55degC to +125degC, USE 20-1000p-E4 FOR NEW DESIGN

8	2	C13, C17	C0603C0G500-181JNE; GRM1885C1H181J	VENKEL LTD./MURATA	180PF	CAPACITOR; SMT (0603); CERAMIC CHIP; 180PF; 50V; TOL=5%; MODEL=; TG=-55 DEGC TO +125 DEGC; TC=C0G
9	7	C23, C24, C27, C28, C41-C43	C0603X5R160-105KNP; EMK107BJ105KA; C1608X5R1C105K; GRM188R61C105K	VENKEL LTD./TAIYO YUDEN/TDK/MURATA	1UF	CAPACITOR; SMT; 0603; CERAMIC; 1uF; 16V; 10%; X5R; -55degC to + 85degC; 0 +/-15% degC MAX.USE 20-0001u-63 FOR NEW DESIGN
10	1	C55	C0603C105K4RAC; GRM188R71C105KA12; C1608X7R1C105K; EMK107B7105KA	KEMET/MURATA/TDK/TAIYO YUDEN	1UF	CAPACITOR; SMT (0603); CERAMIC CHIP; 1UF; 16V; TOL=10%; MODEL=; TG=-55 DEGC TO +125 DEGC; TC=X7R
11	1	C57	TMK212BBJ106KG-T; CL21A106KAFN3N	TAIYO YUDEN	10UF	CAPACITOR; SMT (0805); CERAMIC CHIP; 10UF; 25V; TOL=10%; MODEL=; TG=-55 DEGC TO +85 DEGC; TC=X5R
12	2	CB2, CB3	C0603C0G500-180JNE; C1608C0G1H180J; GRM1885C1H180J	VENKEL LTD./TDK/MURATA	18PF	CAPACITOR; SMT (0603); CERAMIC CHIP; 18PF; 50V; TOL=5%; MODEL=; TG=-55 DEGC TO +125 DEGC; TC=C0G
13	1	D1	MBR0520L	FAIRCHILD SEMICONDUCTOR	MBR0520L	DIODE, SCHOTTKY, SOD-123, PIV=20V, Vf=0.385V@If=0.5A, If(ave)=0.5A
14	2	DGND, GNDS	5011	KEYSTONE	N/A	TEST POINT; PIN DIA=0.125IN; TOTAL LENGTH=0.445IN; BOARD HOLE=0.063IN; BLACK; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH; RECOMMENDED FOR BOARD THICKNESS=0.062IN; NOT FOR COLD TEST
15	1	DGND2	5011	?	5011	TEST POINT; PIN DIA=0.125IN; TOTAL LENGTH=0.445IN; BOARD HOLE=0.063IN; BLACK; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH; RECOMMENDED FOR BOARD THICKNESS=0.062IN
16	1	DSB1	LG L29K-G2J1-24	OSRAM	LG L29K-G2J1-24	DIODE; LED; SMT (0603); Vf=1.7V; If(test)=0.002A; -40 DEGC TO +100 DEGC
17	3	OP2+, IN_U12, EXT_REF	5010	KEYSTONE	N/A	TESTPOINT WITH 1.80MM HOLE DIA, RED, MULTIPURPOSE; NOT FOR COLD TEST
18	1	J1	10118192-0001LF	FCI CONNECT	10118192-0001LF	CONNECTOR; FEMALE; SMT; MICRO USB B TYPE RECEPTACLE; RIGHT ANGLE; 5PINS
19	5	J2, J3, J12-J14	PEC02SAAN	SULLINS	PEC02SA	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 2PINS
20	8	J4-J11	PEC03SAAN	SULLINS	PEC03SA	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 3PINS

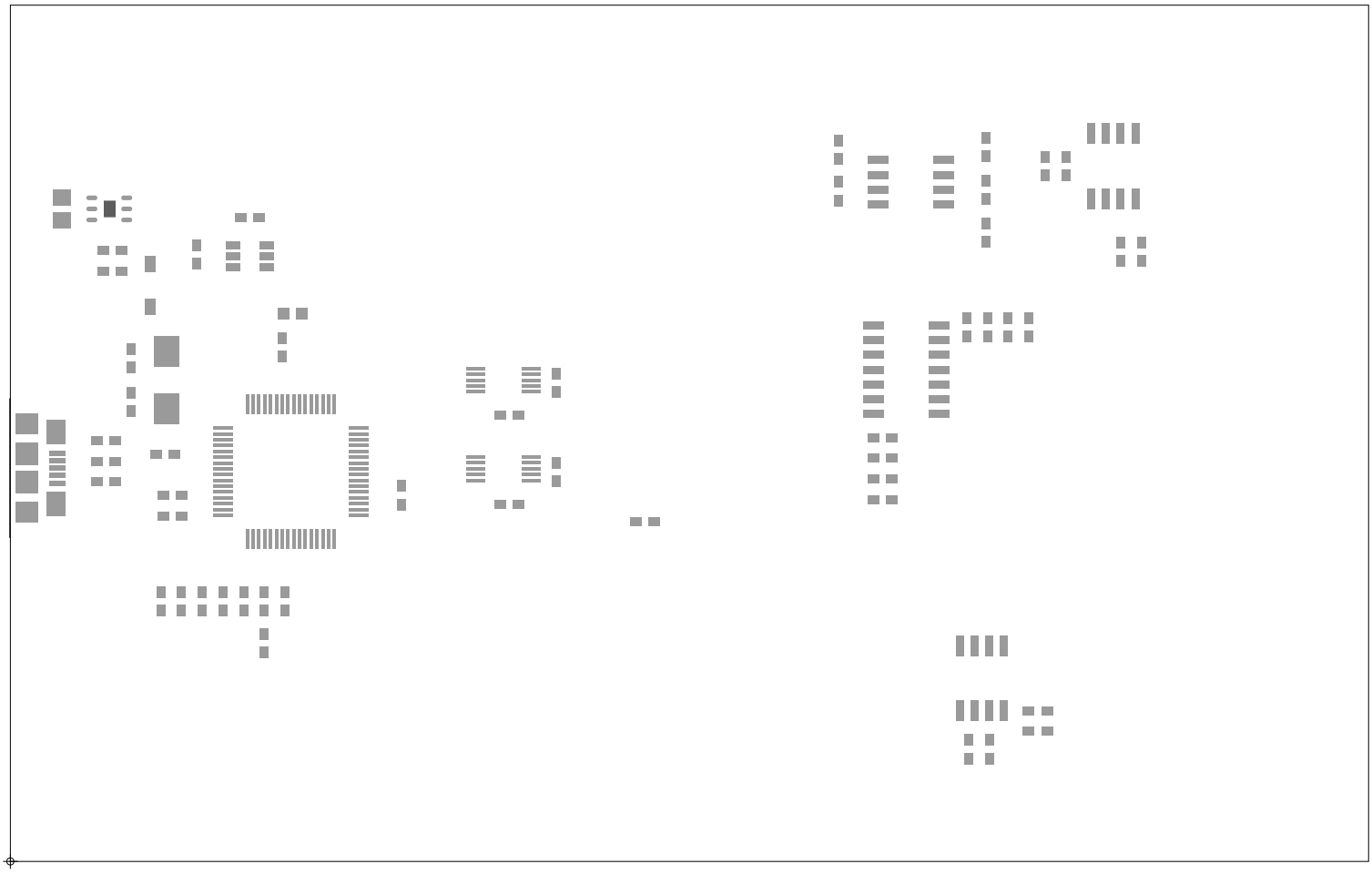
21	1	L3	MPZ1608S601A	TDK	600	INDUCTOR; SMT (0603); FERRITE-BEAD; 600; TOL=+/-25%; 1A; -55 DEGC TO +125 DEGC
22	5	OP+, OP-, VDD, REFF, REFS	5010	?	5010	TESTPOINT WITH 1.80MM HOLE DIA, RED, MULTIPURPOSE
23	6	R1, R2, RB7, RB9, RB14, RB16	ERJ-3EKF28R0V	PANASONIC	28	RESISTOR; 0603; 28 OHM; 1%; 100PPM; 0.10W; THICK FILM
24	2	R4, R9	RC1608J000CS; CR0603-J/-000ELF;RC0603JR-070RL	SAMSUNG ELECTRONICS/BOURNS/YAGEO PH	0	RESISTOR; 0603; 0 OHM; 5%; JUMPER; 0.10W; THICK FILM
25	1	R7	CRCW060310K0JN; ERJ-3GEYJ103V	VISHAY DALE; PANASONIC	10K	RESISTOR; 0603; 10K OHM; 5%; 200PPM; 0.10W; THICK FILM
26	1	RB3	CRCW060312K0FK	VISHAY DALE	12K	RESISTOR, 0603, 12K OHM, 1%, 100PPM, 0.10W, THICK FILM
27	1	RB4	CRCW060315K0FK	VISHAY DALE	15K	RESISTOR, 0603, 15K OHM,1%, 100PPM, 0.10W, THICK FILM
28	7	RB5, RB6, RB8, RB11, RB13, RB15, RB24	CRCW060310K0FK; 9C06031A1002FK; ERJ-3EKF1002	VISHAY DALE/YAGEO PHICOMP/PANASONIC	10K	RESISTOR; 0603; 10K; 1%; 100PPM; 0.10W; THICK FILM
29	1	RB10	CRCW06032K20FK	VISHAY DALE	2.2K	RESISTOR, 0603, 2.2K OHM, 1%, 100PPM, 0.10W, THICK FILM
30	1	RB17	CRCW06034K70FK	VISHAY DALE	4.7K	RESISTOR; 0603; 4.7K; 1%; 100PPM; 0.10W; THICK FILM
31	1	U1	MAX5719	MAXIM	MAX5719	EVKIT PART - IC; MAX5719; NSOIC14 150MIL; PKG. DWG. NO.: 21-0041
32	2	U4, U13	MAX9632ASA+	MAXIM	MAX9632ASA+	IC; OPAMP; PRECISION, LOW-NOISE, WIDE-BAND AMPLIFIER; NSOIC8 150MIL; -40 DEGC TO +125 DEGC
33	2	U6, U8	MAX1840EUB	MAXIM	MAX1840EUB	IC; TRANS; LOW-VOLTAGE SIM/SMART CARD LEVEL TRANSLATOR; UMAX10
34	1	U12	MAX6126AASA41+	MAXIM	MAX6126AASA41	IC; VREF; ULTRA-HIGH PRECISION; ULTRA-LOW NOISE; SERIES VOLTAGE REFERENCE; NSOIC8 150MIL; -40 DEGC TO +125 DEGC
35	1	U14	MAX15006AATT+	MAXIM	MAX15006AATT+	IC; VREG; ULTRA-LOW QUIESCENT-CURRENT LINEAR REGULATOR; TDFN6-EP 3X3
36	1	UB1	93LC66BT-I/OT	MICROCHIP	93LC66BT-I/OT	IC; EPROM; 4K MICROWIRE SERIAL EEPROM; SOT23-6
37	1	UB2	FT2232HL	FUTURE TECHNOLOGY DEVICES INTL LTD.	FT2232HL	IC; MMRY; DUAL HIGH SPEED USB TO MULTIPURPOSE UART/FIFO; LQFP64
38	13	XJU1-XJU13	STC02SYAN	SULLINS ELECTRONICS CORP.	STC02SYAN	TEST POINT; JUMPER; STR; TOTAL LENGTH=0.256IN; BLACK; INSULATION=PBT CONTACT=PHOSPHOR BRONZE; COPPER PLATED TIN OVERALL

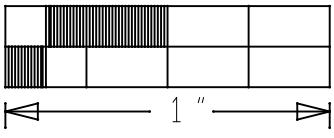
39	1	YB1	ABM7-12.000MHZ-D2Y-T	ABRACON	12MHZ	CRYSTAL; SMT ; 18PF; 12MHZ; +/-20PPM; +/-30PPM
40	1		MAX	MAXIM	PCB	PCB: MAX
TOTAL	117					
DO NOT PURCHASE(DNP)						
ITEM	QTY	REF DES	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
1	3	C14-C16	N/A	N/A	OPEN	PACKAGE OUTLINE 0603 NON-POLAR CAPACITOR - EVKIT
2	2	R3, R5	N/A	N/A	OPEN	PACKAGE OUTLINE 0603 RESISTOR - EVKIT
3	1	R6	N/A	N/A	SHORT	PACKAGE OUTLINE 0805 RESISTOR - EVKIT
TOTAL	6					
PACKOUT (These are purchased parts but not assembled on PCB and will be shipped with PCB)						
ITEM	QTY	REF DES	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
1	1	PACKOUT	88-00711-SML	N/A	?	BOX;SMALL BROWN 9 3/16X7X1 1/4 - PACKOUT
2	1	PACKOUT	87-02162-00	N/A	?	ESD BAG;BAG;STATIC SHIELD ZIP 4inX6in;W/ESD LOGO - PACKOUT
3	1	PACKOUT	85-MAXKIT-PNK	N/A	?	PINK FOAM;FOAM;ANTI-STATIC PE 12inX12inX5MM - PACKOUT
4	1	PACKOUT	EVINSERT	N/A	?	WEB INSTRUCTIONS FOR MAXIM DATA SHEET
5	1	PACKOUT	85-84003-006	N/A	?	LABEL(EV KIT BOX) - PACKOUT
6	1	X1	AK67421-1-R	ASSMANN	OR; MALE; USB; USB2.0 MICRO CONNECTION CABLE; USB B MICRO MALE TO USB A	
TOTAL	6					



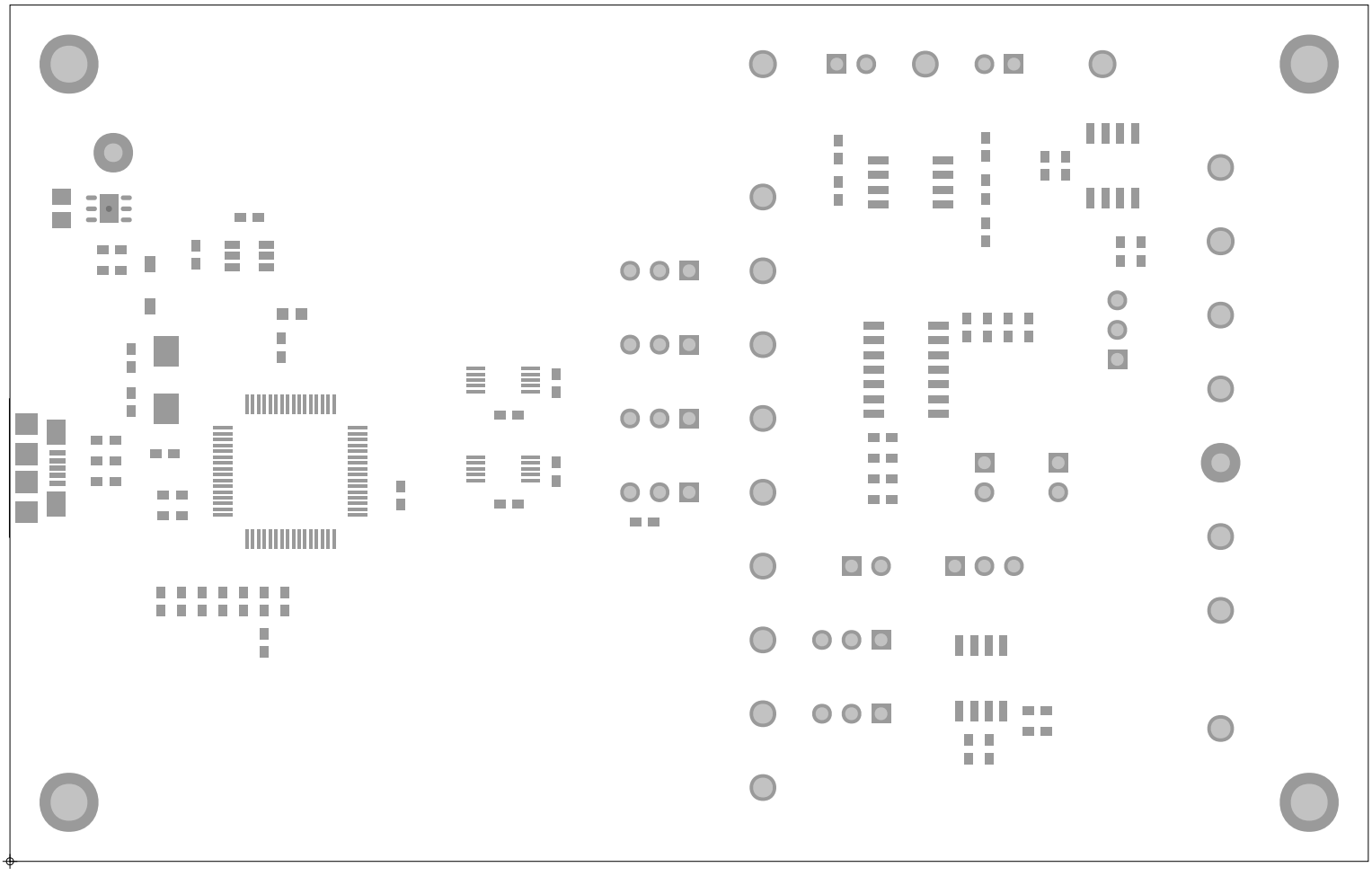


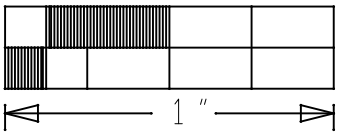
TOP SILKSCREEN



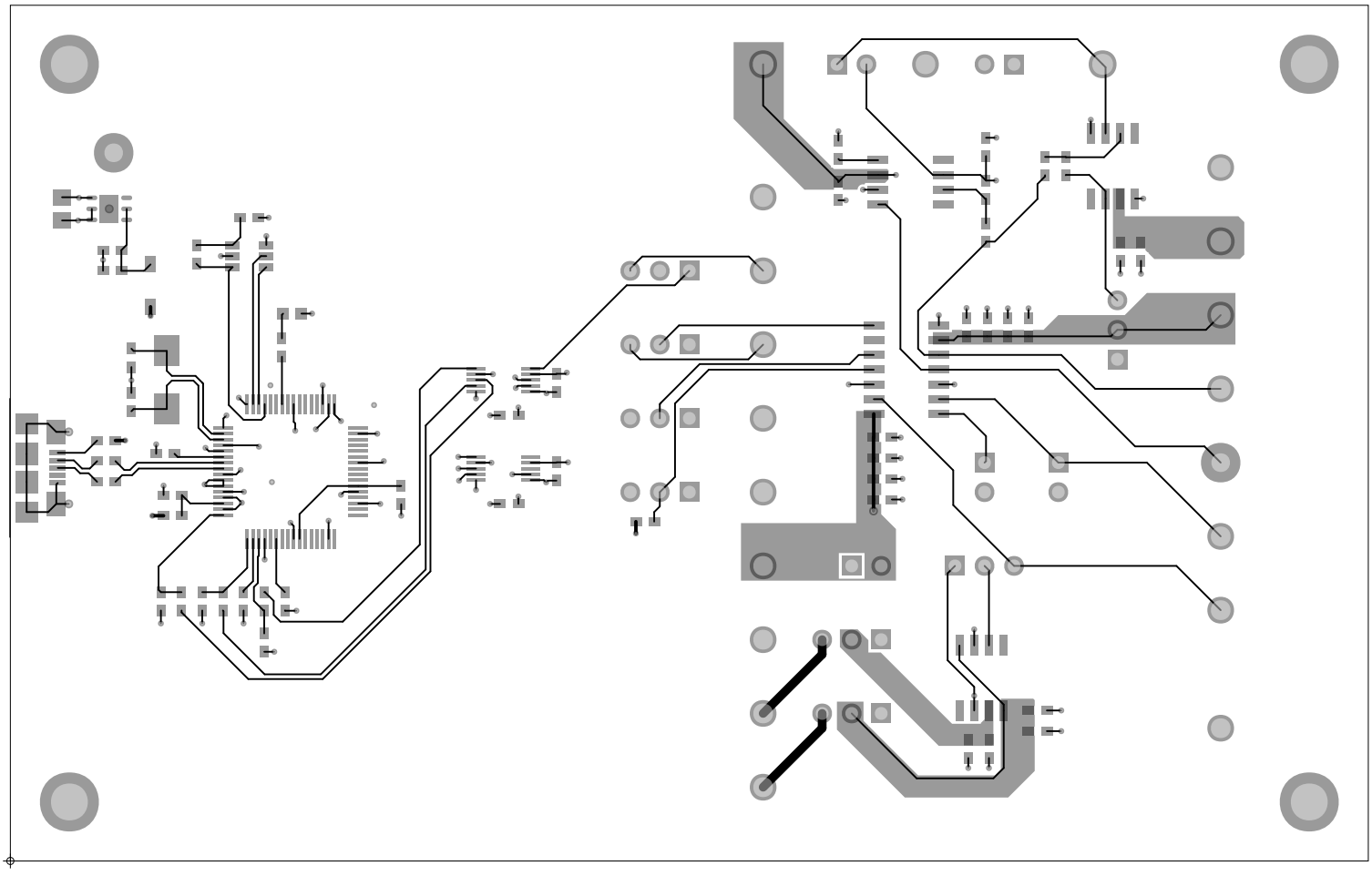


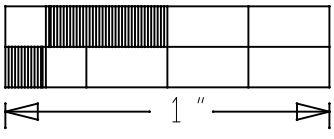
TOP PASTE





TOP MASK





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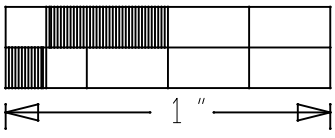
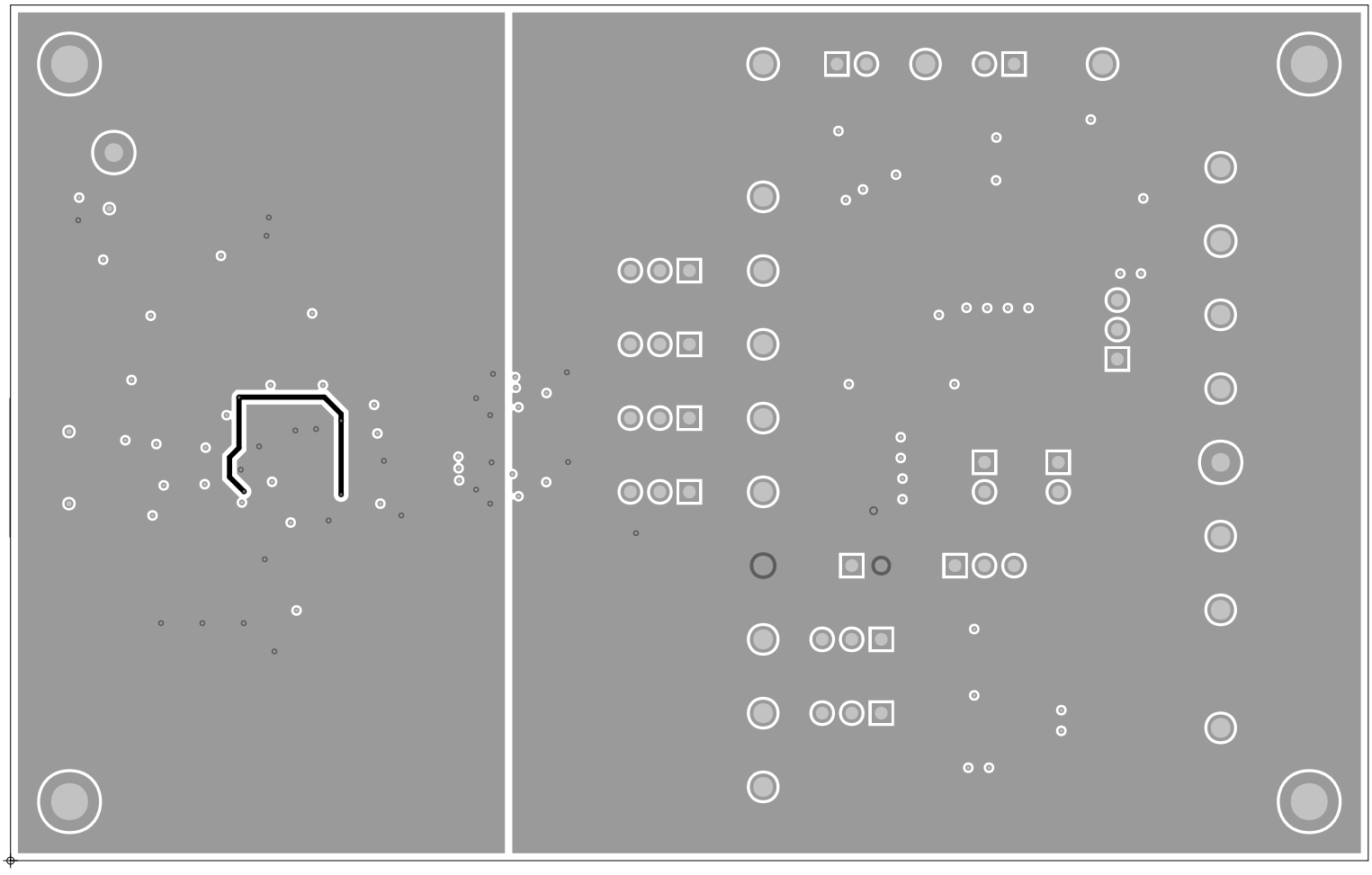
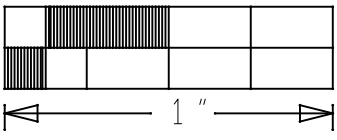
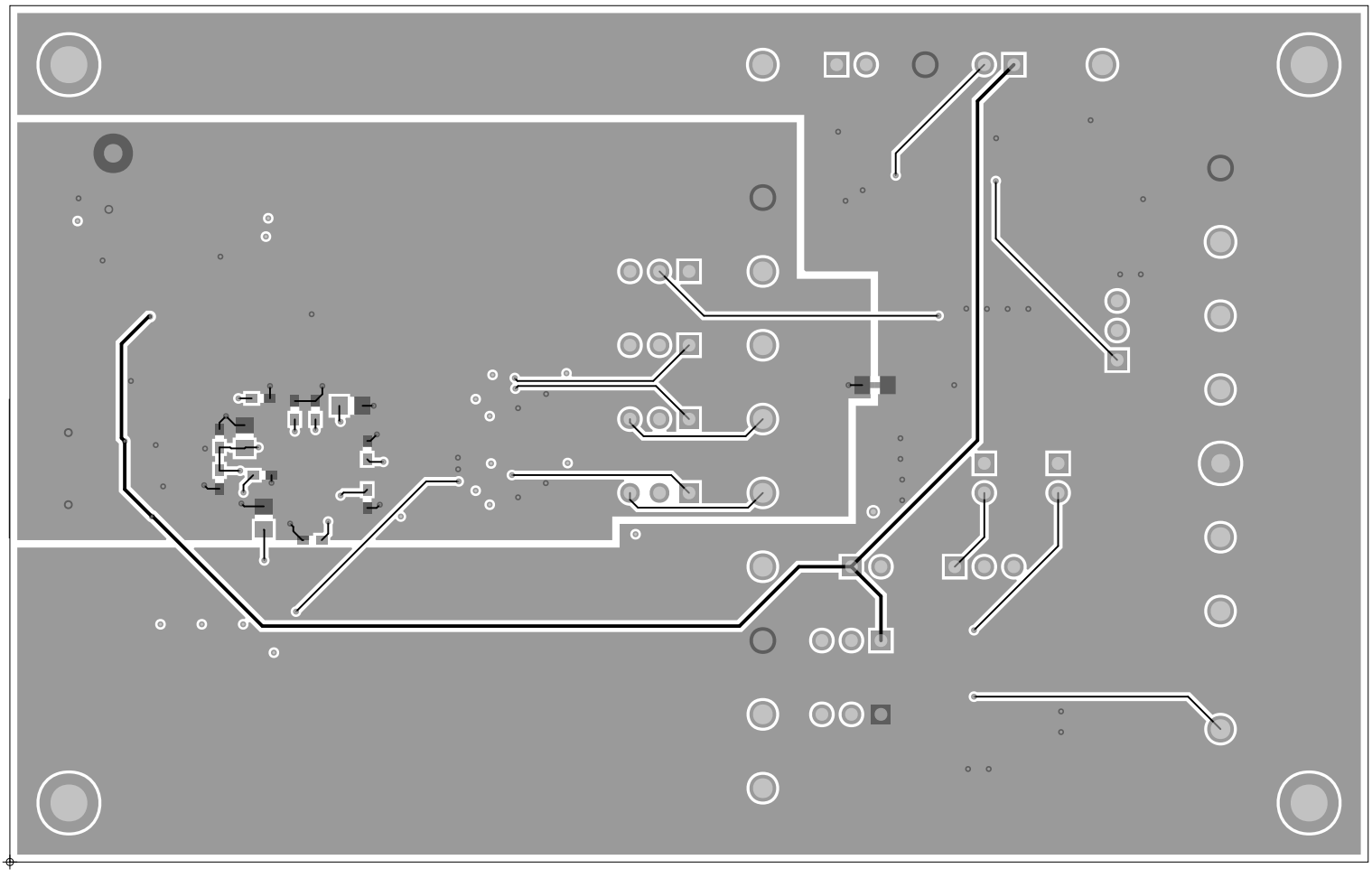


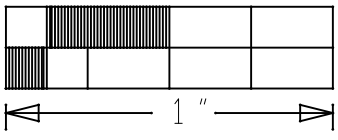
LAYER 2



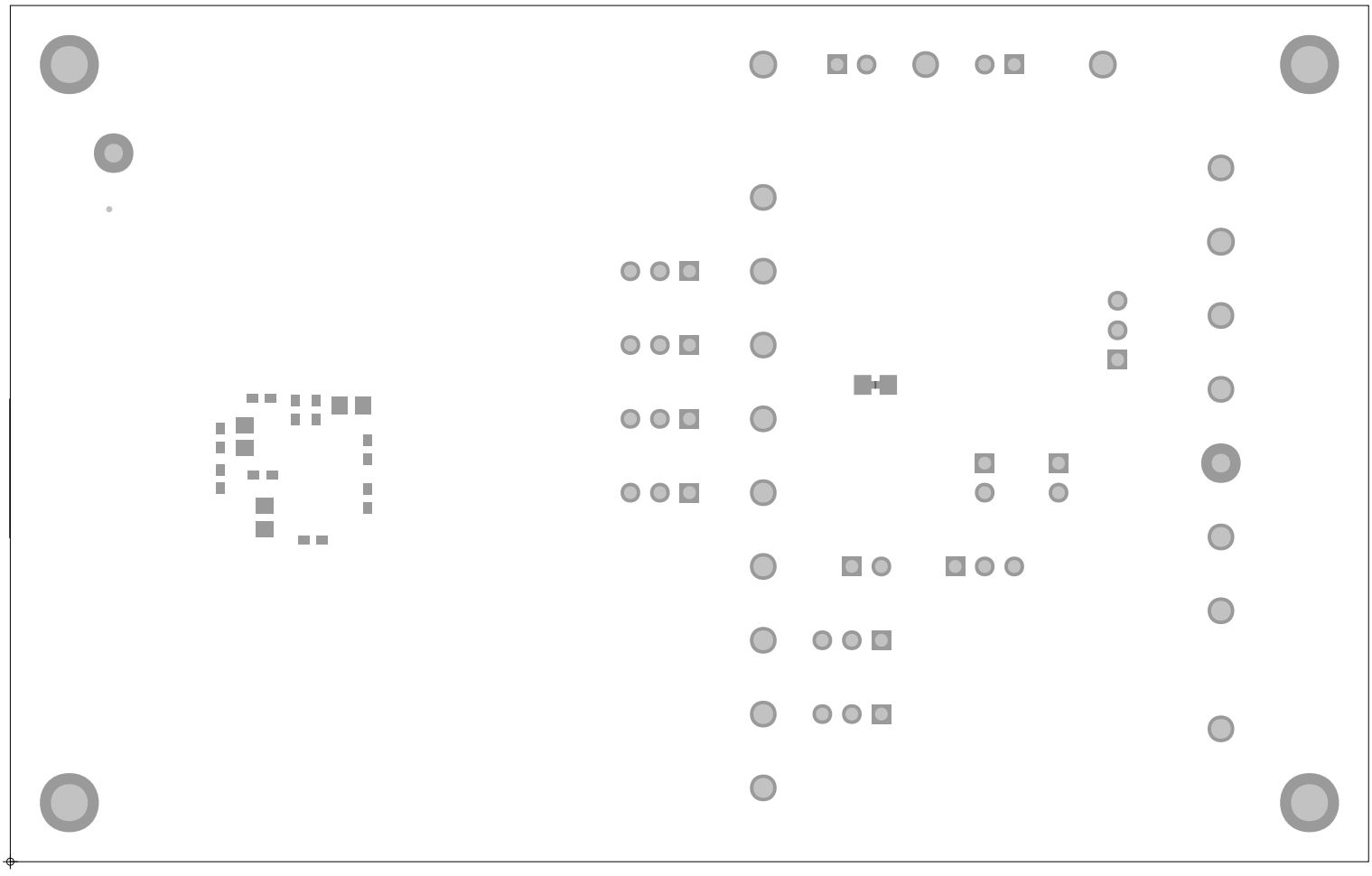


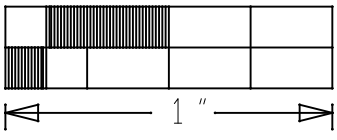
LAYER 3





BOTTOM





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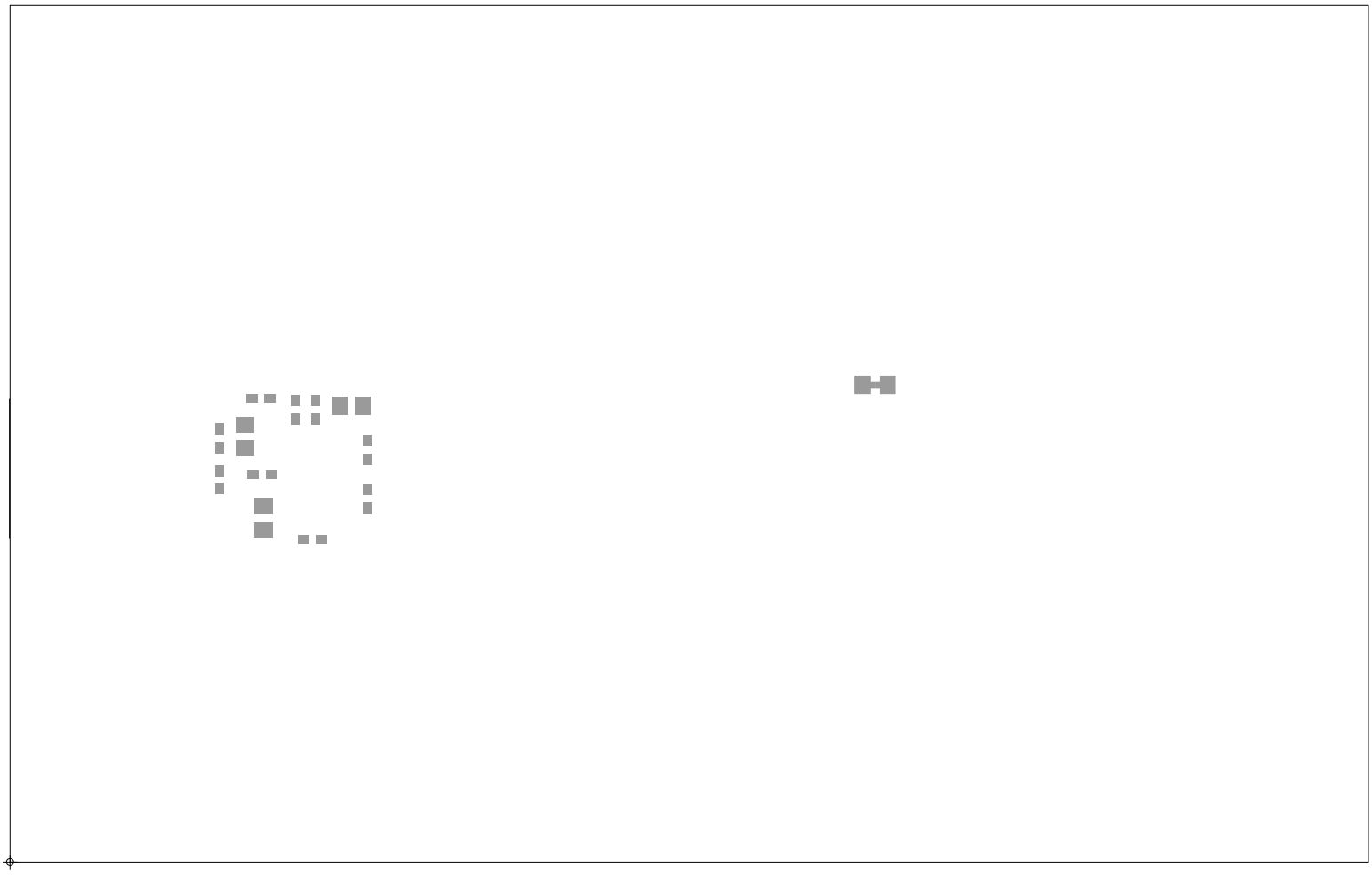

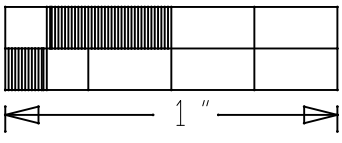

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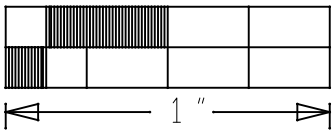

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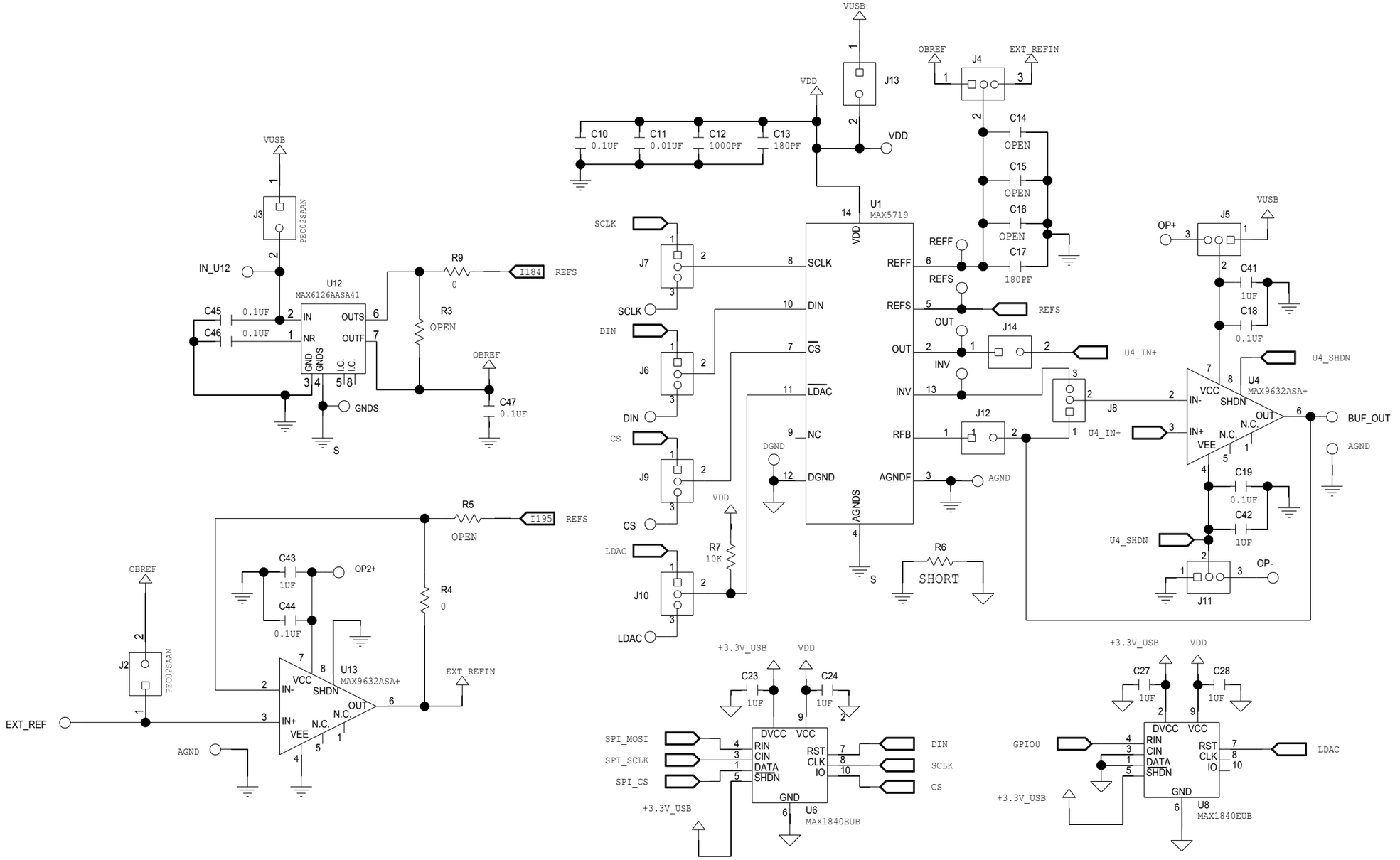
VUSB
J3
PEC02SAAN
IN_U12
U12
MAX6126AASA41
C45
0.1UF
C46
0.1UF
IN
NR
OUTS
OUTF
GND
GNDS
N.C.
N.C.
R9
0
I184
REFS
R3
OPEN
OBREF
C47
0.1UF
GNDS
S
VUSB
J13
VDD
VDD
C10
0.1UF
C11
0.01UF
C12
1000PF
C13
180PF
U1
MAX5719
14
VDD
SCLK
J7
SCLK
SCLK
DIN
J6
DIN
DIN
CS
J9
CS
CS
LDAC
J10
LDAC
LDAC
8
SCLK
10
DIN
7
CS
11
LDAC
9
NC
DGND
12
DGND
AGNDS
4
S
OBREF
J4
EXT_REFIN
1
3
2
C14
OPEN
C15
OPEN
C16
OPEN
C17
180PF
REFF
6
REFF
REFS
5
REFS
REFS
OUT
J14
OUT
2
1
2
U4_IN+
INV
INV
13
J12
RFB
1
1
2
AGNDF
3
AGND
J8
3
2
1
U4_IN+
VUSB
J5
OP+
3
1
2
C41
1UF
C18
0.1UF
U4_SHDN
7
8
U4
MAX9632ASA+
VCC
SHDN
IN-
IN+
VEE
OUT
N.C.
N.C.
2
3
5
1
6
BUF_OUT
AGND
4
C19
0.1UF
C42
1UF
U4_SHDN
2
1
3
OP-
J11
R5
OPEN
I195
REFS
C43
1UF
OP2+
C44
0.1UF
OBREF
J2
PEC02SAAN
2
1
7
8
U13
MAX9632ASA+
VCC
SHDN
IN-
IN+
VEE
OUT
N.C.
N.C.
2
3
5
1
4
6
R4
0
EXT_REFIN
EXT_REF
AGND
R7
10K
VDD
R6
SHORT
+3.3V_USB
VDD
C23
1UF
C24
1UF
SPI_MOSI
SPI_SCLK
SPI_CS
DVCC
VCC
RIN
CIN
DATA
SHDN
GND
RST
CLK
IO
4
3
1
5
9
2
7
8
10
6
U6
MAX1840EUB
DIN
SCLK
CS
+3.3V_USB
VDD
C27
1UF
C28
1UF
GPIO0
DVCC
VCC
RIN
CIN
DATA
SHDN
GND
RST
CLK
IO
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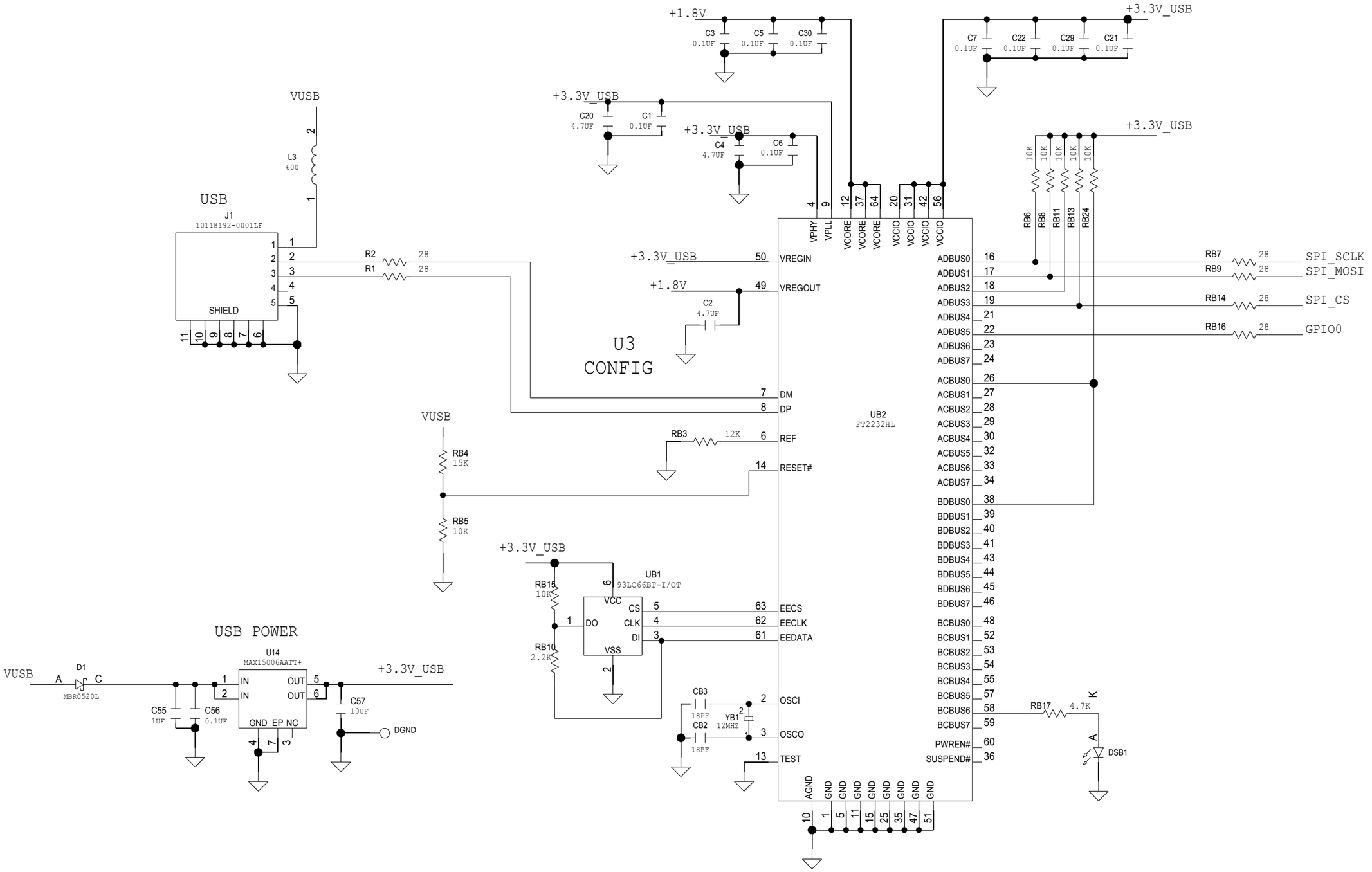
10
6
U8
MAX1840EUB
LDAC
+3.3V_USB


+1.8V
C3 0.1UF
C5 0.1UF
C30 0.1UF
+3.3V_USB
C7 0.1UF
C22 0.1UF
C29 0.1UF
C21 0.1UF
VUSB
L3 600
USB
J1 10118192-0001LF
SHIELD
R2 28
R1 28
+3.3V_USB
C20 4.7UF
C1 0.1UF
C4 4.7UF
C6 0.1UF
10K
RB6
RB8
RB11
RB13
RB24
VPHY
VPLL
VCORE
VCORE
VCORE
VCCIO
VCCIO
VCCIO
VCCIO
+3.3V_USB
VREGIN
+1.8V
VREGOUT
C2 4.7UF
U3
CONFIG
DM
DP
UB2
FT2232HL
ADBUS0
ADBUS1
ADBUS2
ADBUS3
ADBUS4
ADBUS5
ADBUS6
ADBUS7
RB7 28 SPI_SCLK
RB9 28 SPI_MOSI
RB14 28 SPI_CS
RB16 28 GPIO0
ACBUS0
ACBUS1
ACBUS2
ACBUS3
ACBUS4
ACBUS5
ACBUS6
ACBUS7
BDBUS0
BDBUS1
BDBUS2
BDBUS3
BDBUS4
BDBUS5
BDBUS6
BDBUS7
BCBUS0
BCBUS1
BCBUS2
BCBUS3
BCBUS4
BCBUS5
BCBUS6
BCBUS7
PWREN#
SUSPEND#
VUSB
RB4 15K
RB5 10K
RB3 12K
REF
RESET#
+3.3V_USB
RB15 10K
RB10 2.2K
UB1
93LC66BT-I/OT
VCC
CS
CLK
DI
DO
VSS
EECS
EECLK
EEDATA
CB3 18PF
CB2 18PF
YB1 12MHZ
OSCI
OSCO
TEST
RB17 4.7K
DSB1
AGND
GND
USB POWER
VUSB
D1 MBR0520L
U14 MAX15006AATT+
IN
IN
OUT
OUT
GND EP NC
+3.3V_USB
C55 1UF
C56 0.1UF
C57 10UF
DGND

Общество с ограниченной ответственностью «МосЧип» ИНН 7719860671 / КПП 771901001
Адрес: 105318, г.Москва, ул.Щербаковская д.3, офис 1107

Данный компонент на территории Российской Федерации

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http://moschip.ru/get-element

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Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

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